

# 68

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## MICRO JOURNAL

**VOLUME VI ISSUE V • Devoted to the 68XX User • May 1984**  
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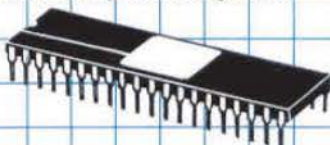
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# '68'

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FOREIGN

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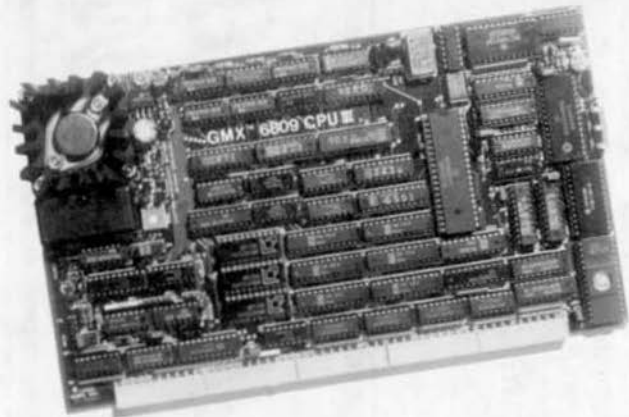
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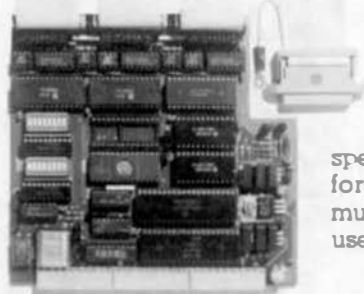
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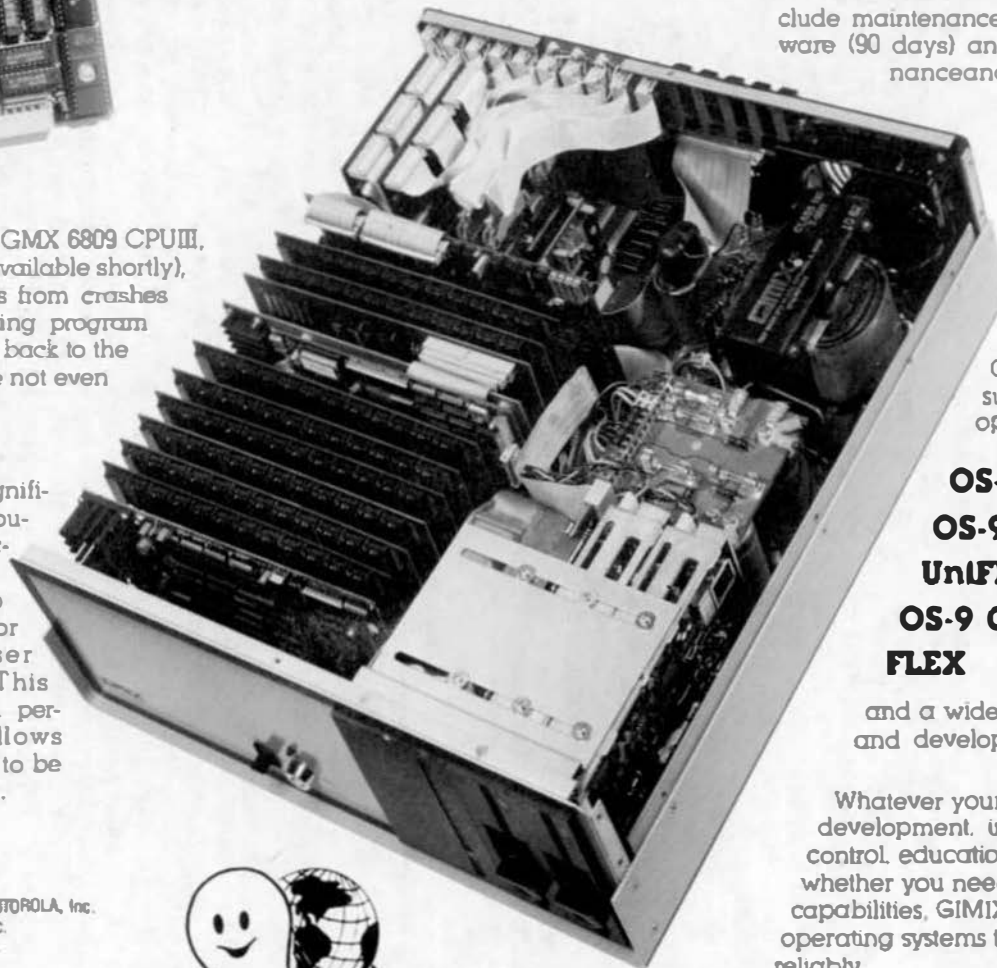
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## 3

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- Expand** — restores a "compressed" file to the original state.
- Split** — breaks a file into smaller files.
- Space** — indents lines with optional spacing between lines.
- Code** — decodes any key on a keyboard to hex.
- Qsort** — quick sort for small files, directories, etc.
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## 4

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By: Ronald W. Anderson

As published in 68 MICRO JOURNAL™

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M.C2  
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MODEM.C2  
SCIPKG.C1  
U.C4  
PRINT.C4  
SET.C5  
SETBAS1.C5

File load program to offset memory — ASM PIC  
Memory move program — ASM PIC  
Printer dump program — uses LOGO — ASM PIC  
Simulation of 6800 code to 6809, show differences — ASM  
Modem input to disk (or other port input to disk) — ASM  
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## Color Micro Journal

The Color Computer Monthly Magazine

\$1.95 per issue Vol. 1, Issue 2 October, 1983

### THIS 'N THAT

The **BIG NEWS** this month is that OS-9 has finally arrived for the Color Computer. The **ASTONISHING** part of the Radio Shack OS-9 Package, besides the price, is the **documentation**. You 'Old Time Radio Shack Followers' will not believe what you see. Jon Shirley has been telling us that the main reason for the "lack" of documentation with a lot of their products was the restrictions placed on releasing that information by Microsoft. I

One of the "Operating Systems of the Future" is now available for the "little old Color Computer"; OS-9. Freely translated, OS-9 means "Operating System for the 6809" (OS-9 is now being written for the 68000, also). Since it is fairly obvious that UNIX and "UNIX-Type" Operating Systems will be running on just about every computer to come out in the next few years, a whole new language is beginning to appear on the horizon.

### Color Computer OS-9; the Package

We had been running a preliminary release of OS-9 on the Color Computer for a few weeks, and received the "official Radio Shack" version for review a couple of days ago. To put it mildly, this package is **IMPRESSIVE**! For \$69.95 (Radio Shack Catalog Number ~~26-3030~~), you receive a 9 1/2" x 7 5/8" x 2" package containing 4

### OS-9 on the COLOR COMPUTER

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# Flex User Notes

Ronald W. Anderson  
3540 Sturbridge Court  
Ann Arbor, MI 48105

## More Assembler Vs. Compiler

Dan Farnsworth received a copy of last month's column at the same time as it was sent to '68' or possibly a little sooner, and he has responded quickly.

"First, a few remarks on your last response. I do not agree that your loop and decision instructions are any clearer than mine in assembler. They both have approximately the same number of characters and the meaning is perfectly clear. Your program example using the 16 bit integer arithmetic only proves that you didn't use the right math package. You admit to having a Floating Point routine. Why didn't you 'INCLUDE FLOAT.LIB', and delete all the functions that weren't needed? Let me put the shoe on the other foot, if you had written your program in PL/9 with 7 digit accuracy and then found out you needed 10 digit accuracy, what would you do? In my Floating Point routine all you would have to do is EQU PC (precision counter) to 10 and reassemble the program.

"Your reference to a listing of 25 pages compared to 6 or 7 pages of HLL code is way off target. While I admit you would create a 25 or 30 page listing, 20 of those pages would be from your library of previously written and debugged programs, leaving only 6 or 7 pages to input. Let us not compare primitive assembly language techniques to HLL's when I am advocating a much more sophisticated approach.

"The power of any programming system is in the commands that are available. I have 350 commands in my runtime package and another 20 routines that I can call from disk. Compare this with the 30 to 50 commands available in HLLs. There are 15 different ways I can output data to the screen or printer. Believe me, that is pure computer power at your fingertips.

"The mathematics package is the big stumbling block for assembly language programming. Once this problem is solved the road is down hill all the way. I will share with your readers the 10 digit BCD package I use for the Business programs. BCD was chosen because there are no conversion errors that you get when going from binary to decimal. Five bytes are required to contain a ten digit BCD number and the range is from -20 million to +80 million. Four 5 byte work areas are required and these are named NUMBER, TOTAL, BUFFER & MULBUF. These are sequential so their relationship to each other is fixed. The source code for the add and subtract routines is as follows:

```
NUMBER RMB 5
TOTAL RMB 5
BUFFER RMB 5
MULBUF RMB 5
```

\* SUBR TO ADD BUFFER TO TOTAL  
\* WITH RESULTS IN TOTAL.

```
ADDBCD CLC
      LDB #5
      LDX #TOTAL+5
ADDBCI LDA 0,-X
      ADCA 5,X
      DAA
      CLEAR THE CARRY BIT
      NUMBER OF BYTES TO ADD
      POINT TO LSB+1
      DECR X AND GET BYTE
      ADD BUF BYTE TO TOTAL BYTE
      DECIMAL ADJUST
```

```
STA 0,X      SAVE RESULT IN TOTAL
DECB         DECR THE BYTE COUNTER
BNE ADDBCI   LOOP UNTIL DONE
RTS
```

\* SUBR TO SUBTRACT BUFFER FROM TOTAL

```
SUBBCD LDX #BUFFER   POINT TO SUBTRAHEND
      JSR COMP        COMPLEMENT BCD NUMBER
      BRA ADDBCD      GO ADD COMPLEMENTED NBR
```

"In the next installment I will present the source code for GETNUM, the number input routine. In the next few months the complete BCD package will be presented. You Monday morning quarterbacks are invited to make any constructive comments. I'm an old dog but I would like to learn some new tricks."

## Response

I'm going to make a general response and then go with some specific examples that might make a point or two for me. First, of course the loop and decision instructions aren't any longer in Assembler than in PL/9 or "C". However the examples Dan gave lumped the "procedure" into one line marked (procedure). Certainly I don't believe, and neither does Dan that his procedure in assembler will be as short as my procedure in PL/9 or "C" or Pascal. The examples below will prove my point. While I do agree that the loop control statements are no longer in assembler, I'm absolutely not willing to agree that they are just as clear if only because there is so much code between the start and end of many loops. Of course a very good programmer like Dan would break a long section of code into short subroutines and the code in the loop could be only a few lines containing subroutine calls, but few of us have enough discipline to do that.

Dan made reference to my example of the machine program in assembler, and asked why I didn't use my floating point package. Perhaps the example was not the best. I had used 16 bit integer arithmetic for some hardware limitations. At the time, I had to squeeze the program into one 2K ROM, and the floating point package was 1.5K long. It was more an exercise in "let's see if we can do that program in 2K and get enough accuracy for the application." We did, but it was too difficult to maintain as the product concept changed the program requirements. I'm sorry I didn't have absolutely equivalent programs to compare. I still don't in this reply, because the two floating point packages I have do not have the same capabilities.

I must admit that Dan has a point about the length of program listing. I didn't take into account the "library functions" in the assembler program version as I did in the compiled version. If you are not confident in your math package enough so that you can "no list" it, you shouldn't be using the math package for any serious application anyway. I still don't believe it will be as short as in my HLL, particularly for "number crunching" applications.

You have 350 commands available and actually prefer that to my 30 or 50????? I must be missing something. How can 350 arbitrary and very "particularized" commands be better than 50 general ones? Do you have a 7 page EQU library file that you include in every assembler program you write, so you can use those subroutines?

The whole 6809 instruction set is only about 60 instructions if you consider the regularity and don't count every variation on a theme. (I know someone is going to say that the 6809 has 1423.786923 instructions). That is, LDA 0,X LDA 0,Y



LDA 0,U LDA 0,S LDB 0,X... etc. You could make 8 or 12 instructions out of those or you could generalize LD(R) n,(R) and get a bunch of instructions down to one general one.

You say you have 15 ways to output data to the screen! You probably do it with 15 different subroutine calls. YUCH and PHOOEY! You miss the point of simplicity in the HLLS if you think that is better!!!!

```
PRINTR(PI,6,1);
PRINTR(PI,12,6);
PRINTR(PI,20,4);
```

Result in my PL/9:

```
3.1
3.141593
3.1416
```

Most of the assembler programmers I know would write three subroutines and call them PR061, PR126, and PR204 respectively. The value is in generalization of instructions, not number of instructions! One instruction can give me more than 15 ways to output a number to the screen if it is general enough, and I have only to remember that one single instruction (or construction if you like).

Your BCD math package looks nice. Based on what you said, I expect that you have fixed the decimal point two digits from the right end of the number, and that your integer arithmetic is therefore in pennies. Your stated limit of +80,000,000 is 8 digits, so I think it is safe to assume that your maximum number is actually +80,000,000.00.

I'm sure you will tell us later how you handle monthly interest calculations, say of 14.5% annual interest, which make the monthly rate 1.2083333, or the multiplier for the interest 0.01208333? I am not interested in a fixed point arithmetic for what I need to do in general. Fixed point results in the problems I mentioned last month of overflow or underflow in my programs unless I carefully monitor each calculation for proper scaling. If I need calculation accuracy to six places after the decimal point, I will need to carry out the calculations to 15 or 18 digits if I use fixed point. That means that I have to calculate 30 to 36 digit intermediate results for multiply.

I am not saying that I couldn't use fixed point, just that it is a pain, and after all, the computer is supposed to worry about things like that for me so I don't have to, isn't it?

I remind you and our readers that you have not shown the code for your COMP routine. The 10's complement of a ten digit number takes a little time to perform. Also you have to get your value into the register you call NUMBER. That requires a 5 byte move routine.

#### Compare Assembler and Compiler

I said last time that I would let Dan have the last word, but he has challenged me to prove something or show how compiler is easier to use than assembler. I will therefore present some program listings and indicate the relative difficulty of using assembler programming for floating point calculations. For a simple example, I am going to program a subroutine to calculate the weight of a steel cylinder given its radius and height.

WEIGHT = PI \* RADIUS \* RADIUS \* RHO \* HEIGHT

RHO is the density of steel. Since the ASCII character set doesn't include greek letters, I have to spell it out. If radius and height are in inches, and the weight is to be in ounces, then RHO is the density in ounces per cubic inch, approximately 4.528.

I have two math packages with which to demonstrate. The first is TSC's BCD floating point package. I'm making the assumption that a 6809 version is available, which is valid, since the 6800 version could be reassembled to run on a 6809, though the code wouldn't be optimized. This package has defined some "floating point" registers. In general a register is defined as:

XSIGN RMB	\$00=+ \$FF=-
XOP RMB 4	first digit always 0
XEX RMB 1	binary signed exponent

The package has provision for variable precision, but this gives 7 digits, comparable to the other two packages. The three floating point registers that we need to use are X, Y, and R. X and Y generally contain the two arguments for an operation, and the result appears in the R register. Note that I am not talking about the 6809 machine registers, but about some software defined "floating point" registers. We are going to write a simple program module to perform the calculation. In each of the three examples we need to define the constants PI and RHO, and then to reserve space for the variables HEIGHT, RADIUS, and WEIGHT. I've assumed that HEIGHT and RADIUS somehow magically got put into the variables of those names, for all three.

Listing WTBCD is the code to perform the calculation with the BCD package. Note that most of the code, (at least the majority of it) is taken up with moving arguments into the X and Y registers, and moving the result from the R register.

I have written a binary math package that uses the User stack for calculations. Part of that package is a simple math interpreter that reads a list of argument addresses and operation codes and performs the math. The code for the calculation is considerably reduced at the expense of increasing the complexity of the math package. Listing WTBIN is the code for this package. JSR MATH causes the math interpreter to scan the list of arguments and operations. The last operation STR (store) causes a return from the math routine with the program counter pointing at the next byte, which in this case is the RTS from the weight calculation. PI and RHO have to be defined in binary (hex) representation, which may require a separate program for calculating those representations when you write the assembler program. Note that I have not bothered to do that in the example program but just used dummy constants with all zero bytes.

Lastly, I've included a PL/9 program to do the same calculation. The saving in the actual calculation, CALCWT is obvious. PL/9 has the capability of reading the algebraic equation directly and generating the code necessary to perform the calculation (as would any Pascal or "C" compiler with floating point capability).

How do the amounts of code generated compare? It is very hard to say, because it is necessary to do a great deal of estimating in making the comparison. The BCD package byte count includes the routines to input and output numbers as ASCII strings. It comes to about 1500 bytes. The CALCWT portion in itself generated 92 bytes of code, the remainder being the math package.

The binary package with the routines necessary to

input and output numbers as ASCII strings would be about 1750 bytes, and the CALWT generated only 26 bytes of code. The 1750 is estimated. Actual code was about 1250 but that includes only being able to input and output numbers represented as +3141593-06 in ASCII form. That is, the handlers to convert to a number with embedded decimal point would have to be added, and of course the inverse function for input numbers, so I think the estimate is rather close.

The PL/9 version is unquestionably more straightforward than either of the others. It, however generated 2K of code with ASCII output capability, and 3K with ASCII input and output capability. The PL/9 package is more capable with regard to number I/O than either of the others. It will input ASCII with embedded decimal point, and will output in that form or revert to scientific notation if the number is too large or small. The calculation itself generated 44 bytes, considerably better than the BCD assembler version but not nearly as good as the package with the math interpreter. All the above byte counts include the complete four function math package with Add, Subtract, Multiply and Divide functions, since it would be rather difficult to subdivide the math package into four separate chunks that could be included optionally if they were needed, and most likely rather unnecessary, since any program doing any amount of calculation would use all four functions anyway.

Again, I must caution against the conclusion that the compiler version generates twice as much code as the assembler version. Let's assume that we wanted to do 100 calculations of the complexity of the one given here. The BCD package would then generate about 10.7K of code. The PL/9 version about 7.4K, and the binary about 4.7K. Compilers tend to look inefficient for small programs. Frequently they generate less "incremental" code than would be generated doing the job in assembler (compare 92 bytes for the calculation in BCD math package with 44 in PL/9).

Again I say that it is almost impossible to compare oranges with oranges in making these comparisons. These packages don't have the same capabilities, though I eliminated the scientific function code from all three for comparison. Perhaps another way to get a handle on the code generated would be to remove the ASCII input output capability of all three and compare. All three seem to come in around 1K of code for the test with those routines not included, that is with just the calculation and the floating point math capability.

I suppose there are some of you out there that will still argue that the assembler versions are clearer and easier to understand, or that the effort of writing such code is not that much more than the PL/9 version. You are going to have a very hard time convincing me of that at this point. I think I've shown here that the code efficiency can be better or worse one way or the other depending on the size of the program AND the particular math package or compiler that you use.

#### A Related Subject

I'd like to digress here for a minute and make a case for the use of Macros in assembler programming. You must have noticed the repetitive nature of the code for the BCD math package. Why write the same group of lines of code over and over again with different operands when you can do it with a few Macros? Of course you will have a macro definition library file that will be included with any calculation program, and the amount of code generated won't change, but the source listing becomes shorter. Listing WTBCDM shows two macro

definitions, one using some conditional assembly, so that we can use one macro for multiplying two arguments from somewhere in memory, or can multiply the result of a previous calculation by a new argument. The macro is set up so that if the first argument is "\*", the result of the previous calculation will be moved to the X register, and the second named argument will go to the Y register. Lastly the FPMUL will be done. The macro could be made more general at the expense of a little more typing, by including the operation in the arguments. We could write a macro CALC that would do for add, subtract, multiply, or divide, simply by making the first parameter, the operation. Rather than writing FMUL \*,RADIUS,RESULT we would be able to do it by CALC FMUL \*,RADIUS or to add two numbers, CALC FPADD,PI,THREE,RESULT.

The listing shows how, with the macros that were defined above the calculation, CALCWT can be written in 6 lines. In order to see how well the macros have generated the required code, I've turned the macro expansion option on and assembled the program again as WTBCDMC. This listing shows the macro calls, and then lists the generated code. You will see that the code and byte count for all three versions of WTBCD is the same.

See, guys, I do know a little about assembler code, but I still would rather do it the easy way with a good compiler. By the way, I realize that I've not written the above code in position independent form. The original packages were not position independent, and I just fell in line with that approach in the examples, which are of course just examples anyway.

Keep the comments coming. This series is generating more response than anything that has ever appeared in this column. I guess we'll have to put all the columns with this material together in a book when we're done. Dan, you will have to have a share in the royalties on that one.

#### Reader Response

Bill Dickinson of Windrush wrote me a letter recently and he had a comment that covers a topic perhaps a bit broader than the present subject, but includes it. I will quote Bill directly.

"We are constantly dealing with people who are absolutely amazed at what we can do with the MC6809 and code produced by PL/9. There seems to be a lot of 'bigger is better' snobbery in the micro community that falls into the following groups:

1. If you are not using the latest 16/32 bit micro in your product, you can't be designing good products.
2. If your program is not written in assembly language it can't be fast.
3. If you use a high level language you NEED lots of RAM and ROM in the system. Therefore only the latest generation of 16/32 bit micros are suitable.
4. You NEED a processor with 1 MB + linear addressing to handle large programs and therefore the 8 bit micros can't do the job.

"My own limited experience with the micro community is that there are a lot of preconceived notions about the power of 8 bit micros out there! The worst of it is that the people who have formed these opinions are so convinced of the validity of their position that you can talk yourself blue in the face trying to argue the technical merits (or lack of them) of their position.



"We do not own or desire to own an emulator. Have you ever tried to convince someone that has used an emulator that it is possible to develop software and hardware without one?"

Note from Ron A.

We had an in circuit emulator with our first Exorciser system from Motorola. To my knowledge it was never ever used by anyone at the company, and I know for certain that I never used it. I have never had an "ICE" nor have I had a logic analyzer. I think the use of either would simply slow me down in the realm of my applications programs. I can see a need for a logic analyzer by people who write software in other realms. An in circuit emulator simply allows one to single step through a program in the "target system". The program should have been debugged before it got into the target system in the first place, by testing each routine with dummy data, and if necessary, single stepping with a tracer/debugger such as DEBUG from TSC.

Back to Bill's Letter

"From what I have seen of the code efficiency of the compilers available for the 68000 (et al) you NEED multi-megabytes just to cope with their gross inefficiency! The commercial boys and the large process control boys might need the amount of memory available with a 68000 but 99% of the control jobs we tackle can be done in well under 48K in PL/9 alone, and we do some very large machine tool and instrumentation control programs! My feeling about the 68000 in our line of work is that it is like using a 20mm machine gun to hunt deer ... it'll work BUT it is just too much machine for the job and has a lot of undesirable side effects."

Thanks Bill for those comments. My experience parallels Bill's directly. In one case, someone wrote me, sending an example of a single calculation using scientific functions via a BCD floating point math package (all programmed in assembler) with the comment that "This one equation runs for 25 seconds." My PL/9 coding of it took 33 milliseconds, about 750 times faster.

The point is that all assembly code is not guaranteed to be good, structured, fast, or efficient, and all compiled code is not guaranteed to be bad, slow, or inefficient.

That ends the quote from Bill Dickinson. I'd like to add that the comments regarding the efficiency of the compilers available for the 6800/09, sound like the ones I made a few years ago about the compilers available for the 6800/09. I firmly believe that it is possible to have very efficient compilers for the 68000, and that it is only a matter of time before they become available, just as the efficient Pascal, "C" and PL/9 compilers became available for the 6809. Bill did go on to add that he does believe that more efficient compilers for the 68000 are possible, and in fact that he is waiting for their appearance.

I was recently told by someone that he would use "C" for his 6809 development projects so that they would eventually be transportable to a 68000 system. I disagree. I won't ever want to move software that I have done and that runs adequately on a 6809, to the 68000. If I use a 68000, it will be for new applications that require its "power" and speed.

68000 Educational Module

The company bought one of these for me to look at. It arrived just today, and I don't have much to

report this time. I have a cross compiler that will run on the 6809 system under FLEX, and I will be learning Assembler for the 68000 in the coming weeks and months. I firmly believe that a programmer ought to learn assembler for whatever processor he programs. It is always handy to know, and it helps a great deal in understanding the output of a compiler that generates assembler source code as an intermediate step in the compile process.

Don't be surprised if you see the prime number sieve benchmark here coded in 68000 assembler in a few months. Let's see if the 68000 is getting the bad reputation reported above in Bill's letter due to the instruction set, or due to inefficient software being available for it. Many of you readers may be too new to computing to remember the early reputation of the 6800 as a real "slug" because of the slowness of execution of the first BASIC that was available for it. (The BASIC interpreter was great, just slow).

/\* CALCULATE WEIGHT OF A STEEL CYLINDER \*/

```
ORIGIN = $0;
STACK = $2FFF;
```

```
GLOBAL
REAL
  HEIGHT,
  RADIUS,
  WEIGHT;
```

```
REAL PI 3.141593;
REAL RHO 4.528;
```

```
INCLUDE REALSHRT.LIB.1;
INCLUDE IQSHRT.LIB.1;
```

```
PROCEDURE CALCWT;
  WEIGHT = PI * RADIUS * RADIUS * RHO * HEIGHT;
ENDPROC; /* MAKES THIS A SUBROUTINE */
```

```
*
* CALCULATE THE WEIGHT OF A STEEL CYLINDER USING
* TSC BCD MATH PACKAGE ASSUMING 6809 VERSION AVAILABLE
*
* MATH PKG EQUATES
2000 FPMUL EQU $2000
2015 MOVEB EQU $2015
3000 RSGM EQU $3000
3006 ISLGM EQU $3006
300C YSLGM EQU $300C
*
* CONSTANTS
*
1000 ORG $1000
*
1000 00 03 14 15 PI FCB 000,003,014,015,092,001 SIGN, 7 DIGITS, EXP
1004 92 01
1006 00 04 52 00 RHO FCB 000,004,052,000,000,001 DENSITY OF STEEL
```

Flex User Notes listings

continues on page 47

# OS9 USER NOTES

By: Peter Dibble  
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## More About the CoCo Disk Driver

After sending in last month's column I had second thoughts about what I said about the OS-9 disk driver for the CoCo. I didn't believe what I had written. The gist of what I said was that Microware and Microsoft together were to blame for the non-standard disk driver included with the CoCo OS-9. The boot ROM in the CoCo loads just 15 sectors from track 34 on the boot disk into set locations in memory and jumps to them. This is Microsoft's idea of a nice way to boot a computer. What I said last month was that Microware managed to squeeze all of OS-9 into those 15 sectors by extensive compression of the code. This sounded pretty extreme to me, but I thought that was what I had heard from Ken Kaplan out at Microware.

Later, I became certain that I misunderstood Ken. There is no way all the core resident parts of OS-9 could be squeezed into that amount of disk, and, if all of OS-9 was loaded by the ROM boot, why does the CoCo have a two stage boot?

I called Microware to check my facts. I was wrong. In the first stage of the boot the CoCo ROM does load data from 15 sectors on track 34 into memory and jump to it, but only a few important parts of OS-9 are loaded: the kernel, the init module, and the OS-9 bootstrap. These are the modules that are found in ROM on other OS-9 systems. The next stage of the boot uses the OS-9 bootstrap which was loaded in the first pass to do a normal OS-9 boot. The parts of OS-9 loaded in the first phase of the boot had to be squeezed hard, but much of the disk driver is loaded in the second phase of the boot.

There were a number of ways for Microware to get a full-featured disk driver into the CoCo, but they didn't. The restrictions on the first phase of the boot forced them to deviate from OS-9 standards in the boot module part of the disk driver. I believe they couldn't find a way to interest Tandy in the extra work (and memory) required to discard the boot after its work was done and load a driver that worked independently. That is certainly reasonable. Why should Tandy be interested in making it easy for people to use non-Tandy peripherals?

In any case, the problem seems to be solved. D. P. Johnson is advertising software that lets CoCo OS-9 deal with every disk format my Glimix can handle. I haven't tried his software, but I have heard from satisfied customers. I also own a 256K memory board made by Dan Johnson. I purchased one of the first boards he sold and had the kind of difficulties one might expect. I came to respect Dan Johnson while we struggled together to fix the problems which I discovered. He is good with hardware and software and VERY conscientious. I can't recommend the software because I haven't tried it (yet). I do recommend the man who sells it.

## Where Next?

I have two very different OS-9 systems, a very large Glimix Level Two system and a CoCo. They fall at almost opposite extremes of the spectrum of microcomputers. The CoCo is so light and small that I think *nothing* of tucking it under my arm and walking a mile down to campus. The Glimix is so heavy that I am daunted by the thought of moving that stack of hardware even a few feet. The CoCo can't really handle more than one concurrent user. I routinely have two users on my Glimix and know people whose Glimix machines typically serve four or more concurrent users. The CoCo includes full graphics and a "terminal" protocol which is consistent across all CoCos. This is a big issue for other OS-9 users, particularly software developers who have to write programs which can be configured for any terminal.

Noting the similarities and differences between these computers has given me a lot of ideas about the kind of hardware I would like to see OS-9 running on. I imagine all computer users spend some time dreaming about the system they would have if only...

My dream computer is a personal computer, or, to use the popular phrase, a personal work station. I have grown used to the idea of OS-9 Level Two as a multi-user operating system, but I still prefer to think of it as a very powerful single-user system. Sharing computers is a way to save money. When I imagine the computer I would like, I don't consider money first.

Naturally, my dream computer runs OS-9 Level Two. It includes a bit-mapped screen (color optional), several dedicated processors, support for some graphics input device (I haven't chosen between a bit pad, a mouse and a light pen), and more than plenty of memory.

Many people seem to think that 128K is the right amount to run OS-9 Level Two in. Now you CAN run Level Two in even less, but you don't really appreciate it until you get to at least 192K. My dream machine would have at least 192K upgradable to 256K, better still, 512K. There are so many uses for memory! Solid state disk drives or caches give better access times than hard disks but use a lot of memory. Complex programs can take lots of memory, but, when they are well written, they are powerful and easy to use. Sometimes lots of memory is needed for simple storage of data. I know a woman who keeps running out of space for her spread sheet on an IBM PC. She has about 600K! So let's put lots of memory in the dream machine.

Graphics hardware is never good enough. At any rate that's the way I react to it. If the resolution and the number of colors is sufficient, the screen takes too long to update. If data is displayed by fussing with parameter lists and registers, the system is too limited. If the screen is bit-mapped, it takes too much attention from the CPU to control the screen. The best solution seems to be to have a separate processor that deals with a bit-mapped display. If the graphics processor has a very high speed connection to the rest of the system, and can be dynamically programmed to do more than just update the screen, the result should be speed and flexibility in graphics.

There is use for more than one special processor in my dream computer. If graphics support is included in the package, it would be foolish to require a terminal to be attached to the computer; an attached keyboard would be sufficient. A dedicated processor to scan the keyboard would take another load off the main processor. The other I/O devices could also use their own processors. My Glimix uses a 6809 on one of its serial cards to take some of the interrupt load off the main processor. It speeds my machine up a little, but doesn't have any other use. If the software for the I/O processor was loaded (and reloaded) by the main processor it would let the serial board be programmed to handle high-speed networks and other applications where timing is important. Even disk controllers could use their own special processors. I don't know of any programmable disk controllers that could do for disk I/O what smart serial cards has done for terminal I/O. The Glimix Intelligent serial card contains a good part of SCFMAN. By unloading this work onto a special processor more cycles are left for user programs. RBFMAN is more complicated than SCFMAN and uses more CPU time. If most of that work could be done by a separate processor still more of the resources of the main processor would be available for the user.

In fact, why talk about the main processor? In many cases OS-9 processes don't share memory with one another. If the dream computer had a bus where additional processor boards with some memory and perhaps I/O could be inserted, OS-9 could run independent processes on their own procedures. Most personal work station users don't need to run more than three or four processes at a time, so including many of what amounts to separate computers in the package would be wasteful. But, if the power is available the applications will arrive.

Mice are making a big splash these days. The Xerox Star, the Apple Macintosh and Lisa, and lots of more expensive work stations are using them. I would definitely pick a mouse over a joy stick. I have more trouble deciding that a light pen or graphics pad isn't a better tool than a mouse. The graphics pad is very precise and the stylus can be used about like a mouse. The arguments against graphics pads are that they are expensive, require desk space, and, for some applications, force the person using them to mentally map from the bit pad to the screen. The cost problem I will ignore -- after all this is a dream computer. The other two problems apply to mice as well. A light pen doesn't require desk space or a mental mapping, but I don't find

them very precise and my hand obscures the screen when I am pointing. I can't make up my mind.

A fancy computer like this deserves fancy software. The peanut-butter and jelly programs now available for OS-9 just don't live up to the hardware.

My pet peeve with OS-9 software has always been its lack of excellent editors. I like Dynastar fine, and I have heard nice things about Screditor and Stylograph, but these programs are at least five years behind the state-of-the-art. My dream machine deserves something special. Do you suppose EMACS could be ported to OS-9?

A real database program would be nice. Something more than a filing cabinet or stack of index cards metaphor.

I bet Knuth's TeX would run on something like this. Some good graphics programs, especially a graphics editor would make the graphics support a lot more useful. A real statistical program like SAS, or SPSS would make some people happy. Others need really good communications software.

Languages aren't as important as the software written in them, but OS-9 is still painfully short of languages. I bet APL would run well under OS-9. Fortran is old fashion, but we really should have it. Those are the fundamental languages, but there is an endless list, including: Pilot, PL/I, Logo, Smalltalk, and others.

Networking is another sexy topic these days. Expensive computers (which my dream machine is turning out to be) are generally used by people for whom communication is terribly important. Electronic mail, electronic calendars, and sharing of files and other resources are important to them. OS-9 doesn't include networking software, but I think it will be at least as easy to run over a network as any other operating system.

Enough of the dreaming. Truly, my dream machine is not so very far away. I/O processors exist, and I am sure more are coming. I have heard talk about slave processors. There are graphics boards available for the SS-50 bus that are a lot like what I have in my dream machine. The CoCo comes with bit mapped graphics standard.

For my Gimix I can hope for I/O and slave processors and a better (and less expensive) graphics board. For my CoCo I can aim low and hope for a disk controller with an onboard buffer, or aim high and look for a real Level Two system with as much done in hardware as possible (I/O, sound, and graphics). From my viewpoint as a Level Two user I think Tandy would be crazy not to offer a CoCo with Level Two. For a software person like me, it is fun to think up lots of things that hardware people should do for us, but the most important part of any computer is its software.

Some of my software wish list will have to wait for better hardware, in particular for more memory. Much of it can be done now. I have done some primitive networking myself. A really special database program or editor would push a 6809 hard, but might be possible. I have heard from people who are working on lots of nice things for OS-9. Pretty near every piece of software for my dream machine is a project someone is working on now.

More Noise from the CoCo

Last month I included a driver for the Digital-to-Analog converter in the CoCo. That driver was useful for low-speed D/A applications, but it didn't do very well at sound generation. The highest pitch my driver could manage was something of a gurgle. The speed problem wasn't in the driver. It takes a long time for a character to get through SCFMAN. Even when a block of characters goes through together there is enough delay in the transmission of each character to make smooth, high-frequency waves impossible. Fortunately, generating music isn't the only purpose for an A/D converter. Controlling lab instruments, motors, and such are all fine applications which only require a voltage to be changed infrequently -- 10 times per second at most.

I ended last month's column with a few suggestions for ways to make the A/D driver better at generating sound. This month I went ahead and took my suggestions. This month's A/D driver does a pretty good job of making music. It even makes nice chords. I made the improvement I suggested last month. If the driver receives a zero, it places the next 360 bytes sent to it in a special buffer. Characters that don't go into the buffer cause the contents of the buffer to be transmitted through the D/A a number of times corresponding to the magnitude of the character

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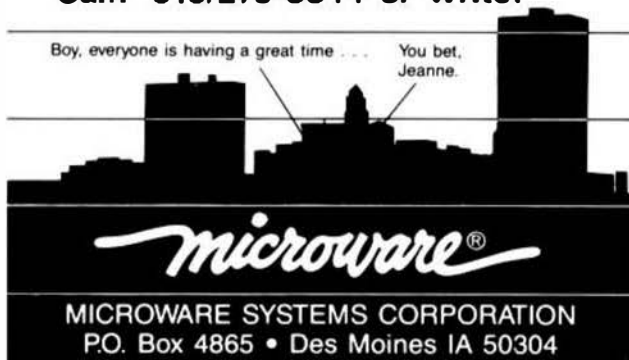
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written. Since it takes a fixed amount of time to transmit the huffer, each character from \$01 to \$FF will take a fixed amount of time to send. This way each character sends a note of a set duration whatever the pitch.

At first I used a buffer 128 bytes long. That was easy to handle in BEEPER, but it was hard to build a wave in. It is important that a whole number of cycles fit into the buffer. It was difficult to generate a wave that fit precisely into 128 values. Numbers like 90 and 360 work better when angles are measured in degrees (if they are measured in radians it is hard to make any integers come out evenly.) I tried a 90 byte buffer, but I found it hard to store smooth, high-pitched tones in it. After the buffer got over 128 bytes long, I used the D register to offset the index into it so length didn't make much difference. I chose 360 bytes as the length of the buffer because it is an easy number to work with when generating the wave.

Interrupts are a problem to time-dependent things like music. I tried BEEPER with interrupts masked and unmasked. When interrupts are unmasked the sound is definitely not pure; however, when the interrupts are masked lots of bad things happen. With interrupts masked nothing happens except operation of the D/A. Time doesn't get updated, the keyboard doesn't get scanned, and, if you are using the RS-232 port, it comes to a halt. In the version of BEEPER included with this column I commented out the ORCC and ANDCC. Try it both ways and choose for yourself.

### This Month's Driver

I got a little carried away with the test driver for BEEPER. The program calls for a magnitude and frequency for a wave (the numbers are only relative). The sine wave generated with these numbers is added to whatever wave has already been generated. The resulting wave is displayed. If a Y is entered, the wave is loaded into BEEPER's buffer and a few beeps are sent, if an A is entered another sine wave is prompted for and added to the existing wave, and if anything else is entered the wave is erased and the program starts over with a clean slate.

I am afraid that this test driver is another program that needs work. BEEPER truncates numbers greater than 63 to 63. If the sum of the sine waves loaded into BEEPER's buffer is greater than 63 at any point the wave will be clipped (as hi-fi people say). It would be good if BEEPER would check for this. I also get pretty frustrated when I don't like the last sine wave I added to a wave I am building and have to wipe out the entire waveform to get rid of it. On the other hand I am rather partial to the graphic display of the waveform.

### The Users Group

Things aren't moving as quickly for the OS-9 Users Group as we hoped they would. We published our first news letter (called MOTD) months ago. By the way, if you are a member and didn't receive a copy of MOTD, send a note to the Users Group. Our system for keeping track of members seems pretty reliable, but it may have cracks in it. We are working on the second issue. The most important thing to most members seems to be the software exchange. There have been a number of problems getting the software exchange disks out. The most interesting problem has been a disk incompatibility between 40 track disks written by 80 track drives on two different manufacturer's systems. Watch for this problem!

There also seems to be some trouble getting disks. Three dollars per disk delivered to a member is a very low price. It's hard to be too impatient. In any case, barring another serious hold-up, the disks should be in the mail by late March. Let me say again that we don't know the disk format many users need. If I guess wrong, send the Users Group a letter and we'll try to find a way to straighten things out.

```

00001      NAME      BEEPER
00002      [FPI]      Use OS9DEFS, SOFDEFS and IODEF
00003      [ENDC]      and ENDC
00004
00005      USE
00006      TITLE      DEVICE DESCRIPTOR
00007
00008      NAME      SET
00009      [FPI]      DEVICE OBJECT
00010      [ENDC]      BSEND, BPARAM, TYPE, REENT+1, FNAME, ORNAME
00011      FCB      READ, WRITE, MODES
00012      [FPI]      WFF, WFF, $20 PORT ADDRESS
00013      [ENDC]
00014      FCB      OPTL      Length of options section

```

```

00014      0012      OPTIONS      EQU      # DT_SOF
00015      0012      00      FCB      # OPTIONS
00016      0001      OPTL      EQU      # OPTIONS
00017
00018      0013      FF23      CNTL1      FDB      WFF23      address of control byte 1
00019      0015      FF03      CNTL2      FDB      WFF03      address of control byte 2
00020      0017      42454500      BPARAM      FCS      /BEEP/      name of this module
00021      0018      5343C6      FNAME      FCS      /SOF/      File Manager name
00022      001E      42454550      OR_NAME      FCS      /BEEPER/      Device driver name
00023      0024      58A8A3      ENMOD
00024      0027      BSEND      EQU      #
00025      0027      TTL      #      DEVICE DRIVER FOR D/A
00026
00026      00E1      TYPE      SET      DRIVE OBJECT
00027      00B1      REENT+1      SET      REENT+1
00028      0000      BPCD00B      MOD      BPCD00B, BPARAM, TYPE, REVS, ENTER, MEMSIZE
00029      0010      ORG      ORG      V_SOF
00030      0010      PORTA      RMB      2      PORT ADDRESS
00031      001F      CNTL1V      RMB      1      HOLD CNTL1 VAL
00032      0020      CNTL2V      RMB      1      HOLD CNTL2 VALUE
00033      0021      CNTL1A      RMB      1      HOLD CNTL1 ADDR
00034      0023      CNTL2A      RMB      1      HOLD CNTL2 ADDR
00035      0025      COFFSET      RMB      2      OFFSET IN BUFFER
00036      0168      BUFLDN      EQU      90+4
00037      0027      BUFLDN      RMB      BUFLDN
00038      018F      MEMSIZE      EQU      #
00039      0000      03      FCB      READ, WRITE, DRIVER MODE
00040      000E      42454550      FCS      /BEEPER/      Program Name
00041      0014      01      FCB      #      EDITOR
00042
00043      # Enter points
00044
00045      ENTER
00046      0015      160006      LIBRA      INIT
00047      0018      160042      LIBRA      READ
00048      001B      160047      LIBRA      WRITE
00049      001E      160000      LIBRA      GETSTAT
00050      0021      16009B      LIBRA      PUTSTAT
00051      0024      16009C      LIBRA      TERM
00052      0027      INIT
00053
00054      # U ADDRESS OF DEVICE STATIC STORAGE
00055      # Y ADDRESS OF DEVICE DESCRIPTOR MODULE
00056
00057      0027      NEAR13      LDI      CNTL1, Y      Have the address of cntl1 byte
00058      002A      AF321      STI      CNTL1A, U      from the D.Descriptor to static
00059      002D      A684      LDR      #1      save the value of the cntl1 by
00060
00061      002F      A7C81F      STA      CNTL1V, U
00062      0032      8A08      ORA      #080      set one of the bits
00063      0034      A784      STA      #1      that turns on sound
00064      0036      NEAR15      LDI      CNTL2, Y      Have the address of cntl2 byte
00065      0039      AF323      STI      CNTL2A, U      from the D.Descriptor to static stor
00066      003C      A684      LDR      #1      save the value of the cntl2 by
00067      003E      A7C820      STA      CNTL2V, U
00068      0041      84F7      ANDA      #0FF+008      set the other bit
00069      0043      A784      STA      #1      that turns on sound
00070      0045      8D08      BSR      INITBUF      Initialize the sound buffer
00071      0047      5F      CLRBI      CLEAR CARRY
00072      0048      E7C825      STB      COFFSET, U      Offset in a two byte field
00073      0048      E7C826      STB      COFFSET+1, U
00074      004E      39      RTS      RETURN
00075
00076      # Put something that won't sound too bad
00077      # into the sound buffer
00078
00079      004F      INITBUF      LEAI      BUFFER, U
00080      0052      30C827      LLD      #BUFLDN-1
00081
00082      0055      E788      STB      D, #1
00083      0057      8D0001      SUBB      #1
00084      005A      2CF7      BEQ      INITLOOP
00085      005C      39      RTS
00086
00087      READ
00088      # U ADDRESS OF DEVICE STATIC STORAGE
00089      # Y ADDRESS OF PATH DESCRIPTOR
00090      # RETURN CHARACTER READ IN A
00091
00092      005D      A613      LDI      V, PORT, U
00093      005F      A684      LDR      #1      set the value in the D/A regss
00094      0061      44      LSRBI      #1
00095      0062      44      LSRBI      #1      Shift out the low order bytes
00096      0063      5F      CLRBI      Clear carry
00097      0064      39      RTS
00098
00099      WRITE
00100      # U ADDRESS OF DEVICE STATIC STORAGE
00101      # Y PATH DESCRIPTOR
00102      # A VALUE TO WRITE
00103
00104      0065      60C826      TST      COFFSET+1, U      If offset isn't zero
00105      0068      263F      BNE      #0      we are in the process of filling
00106      006A      60C825      TST      COFFSET, U      buffer. We have to test both C
00107      006D      263A      BNE      #0      If the character to write is 0
00108      006F      4D      TSTA      the sound buffer
00109      0070      273F      BEQ      #0
00110
00111      # LOOP THROUGH BUFFER
00112
00113      0072      30C827      LEAI      BUFFER, U      the address of the sound buffer
00114      0075      3402      PSWS      A      SAME COUNT
00115      # ORCC INTERRUPTS Shut off interrupts
00116      0077      C0167      LLD      #BUFLDN-1      Offset in buffer
00117      007A      340A      PSWS      D      Save offset
00118
00119      007C      A688      LDR      D, #1      set a byte out of buffer
00120      007E      3410      PSWS      #1
00121
00122      # on second thought it would have been
00123      # better to just do a LEAI BUFFER, U later instead
00124      # of saving this value here
00125      0080      A641      LDI      V, PORT, U      The address of the D/A register
00126      0082      3402      PSWS      A      Build the byte to store in the
00127      0084      A684      LDR      #1      register
00128      0086      8A03      ANDA      #02000001, #1
00129      0088      A680      ORA      #50
00130      008A      A784      STA      #1      Store the new D/A value
00131      008C      3510      PULS      #1      recover the buffer address
00132
00133      # see note
00134      008E      E2E4      LDD      #5      set the new offset in buffer
00135      0090      8D0001      SUBB      #1      decrement the offset
00136      0092      E2E4      STD      #5
00137      0095      2E25      BEQ      #0      if it isn't negative send the

```

```

00137 0097 3262      LEAS 2,S      Clear stack
00138 0099 6AE4      DEC J,S      decrement repeat count
00139 009B 2604      BNE CYCLE     cycle if not zero
00140          * BEEP: BEEP-147M95S
00141 0070 3261      LEAS 1,S      CLEAR STACK
00142 009F 3F          CLRB
00143 00A0 3F          RTS
00144
00145 00A1          SUBF LINE
00146 00B1 080168      LDD BBUF0A    BBUF0A
00147 00B4 EDC323      STD          OFFSET,U
00148 00A7 3F          CLRB          CLEAR CARRY
00149 00A8 3F          RTS
00150
00151 *****
00152 * Load the Second buffer
00153
00154 00A9          DEFINE
00155 00A9 48          LSLA          Prepare the value
00156 00A9 48          LSLA
00157 00A9 3402      PSYS A          save it
00158 00A9 EDC323      LDD          Current offset
00159 00B0 830001      SUBD 0;
00159 00B2 EDC323      STD          Update offset
00160 00B0 30C327      LEAD          BUFFER,U
00161 00B7 1080      LEAD D,1      location to store this byte at
00162 00B8 3502      PULS A          get the byte
00163 00B0 4784      STA          store it
00164 00B6 3F          CLRB
00165 00C1 3F          RTS
00166 00C1          GETSTAT
00167 00C1          PUTSTAT
00168 00C1 3F          CLRB
00169 00C2 3F          RTS
00170 00C3          FERM
00171 *****
00172 * U DEVICE STATIC STORAGE
00173
00174 00C3 AEC321      LDD CIL1A,U
00175 00C6 A6C31F      LDA CIL1V,U
00176 00C9 A784      STA          restore the original cil1 val
00177 00CB AEC323      LDD CIL2A,U
00178 00CE A6C320      LDA CIL2V,U
00179 00D1 A784      STA          restore the original cil2 val
00180 00D3 3F          CLRB
00181 00D4 3F          RTS
00182 00D5 A6C32B      ENDD
00183 00D6          EQU

```

```

00000 error(s)
00003 warning(s)
000FF 00255 program bytes generated
01072 00370 data bytes allocated
01602 05842 bytes used for symbols

```

```

PROCEDURE TBEED2
0000 00
0032 00
0062 00
008F 00
00C7 00
00D9 00
00FC 00
013F 00
0158 00
015B DIM NOTE(360):BYTE
0167 DIM I,J,K:INTEGER
0176 DIM SOUND:INTEGER:16 Path number for A/D
0193 DIM MAGNITUDE,FREQ:INTEGER:16 variables used to form the waveform
01C4 DIM C:BYTE:16 a variable one-byte variable
01E9 DIM CMD:STRING:16 waveform command
0203
0204 OPEN #SOUND,"BEEP":WRITE
0214 DEG %s Use degrees for angles
0226 %s Initialize Note to zeros
0244 FOR I=1 TO 360
0258 NOTE(I)=0
0271 NEXT I
0271
0272
0275 %s Build waveform
0286
0289 LOOP
028B RUN OFI("ALPHA") %s make screen printable
028D %s Get parameters for a sin wave
0290 INPUT "MAGNITUDE=":MAGNITUDE
0293 INPUT "FREQUENCY=":FREQ
0296
0299 %s add the sin wave to the wave in NOTE
0320 FOR I=1 TO 360
0334 NOTE(I)=NOTE(I)+MAGNITUDE*SIN((I*FREQ))
0358 NEXT I
0363
0370 %s Display the graph
037A
037D RUN OFI("MODE",0,1)
038F FOR I=1 TO 180
039F J=NOTE(I*2)-2
03B0 K=J*4
03B8 IF J<0 THEN J=0
03C0 ENDIF
03C5 J=J*2
03D0 K=K*2
03E3 IF J<192 THEN J=192
03F7 ENDIF
03F9 IF J<192 THEN K=192
0408 ENDIF
0400 RUN OFI("LINE",I,J,I,K)
0420 NEXT I
0438
043B %s Display a little bit of the next cycle
0444 %s to demonstrate the wave is continuous
0446
0452 FOR I=181 TO 255
0462 J=NOTE((I-180)*2)-2
0484 K=J*4
04C1 IF J<0 THEN J=0
04D0 ENDIF
04D5 J=J*2
04E0 K=K*2

```

```

04EB IF J<192 THEN J=192
04FD ENDIF
04FF IF J<192 THEN K=192
0511 ENDIF
0519 RUN OFI("LINE",I,J,I,K)
0533 NEXT I
053E
0541 %s There is no prompt because the screen is full of
0578 %s graphics, but enter Y/C/D/A/C/D, or M/C/D after
0586 %s the graph has been drawn
05C1
05C9 INPUT CMD
05D4 EXITIF CMD="Y" THEN
05D8 EXITIF
05E7 IF CMD="A" THEN
05E7 FOR I=1 TO 360 %s The waveform is bad
0610 NOTE(I)=0 %s zero it and start over
0634 NEXT I
063F
0641 ENDIF
0641 RUN OFI("CLEAR")
064E
0652 ENDLOOP
0652 RUN OFI("ALPHA")
0652 RUN OFI("QUIT")
0668 C=0 %s a zero tells the driver to use the next 360 characters
06AB PUT #SOUND,C %s to build a new form
06CB PUT #SOUND,NOTE %s send the new form
06E9 PRINT "STARTING SOUND"
06F8
06FE %s Send a few beeps of different lengths
0725
0729 FOR I=100 TO 250 STEP 50
073E C=I
0744 PUT #SOUND,C
0750 BEEP 100
0754 PRINT "END OF LOOP ",I
0768 NEXT I
0773
0775
0778 %s Delay a little
0789
078C 100
0790 FOR J=1 TO 500
0791 NEXT J
07AC RETURN

```

## OS9 USERS GROUP

OS-9 Users Group  
Presidents Column  
by Dale Puckett

Before I start answering questions I would like to thank Jim Bellomo for making the live Compuserve conference with the OS-9 Users Group officers February 12 possible. Without his hard work we wouldn't have this fantastic communications medium available to us. \*\* This was written prior to the untimely death of Jim Bellomo, noted in last months 68 MICRO JOURNAL. \*\*

And, while we're passing out KUDOs I also would like to thank Tom Murphy, of SunTel in St. Louis, for all the work he did as Secretary after our election in Des Moines, last August. Tom resigned so he could personally supervise a new and rather large contract. Good luck to you Tom!

I have appointed Dave Gibson, of the Federal Highway Administration in Washington, D. C. to fill the rest of Tom's term of office.

### WHO ARE WE AND WHAT ARE WE DOING FOR YOU?

We are a Des Moines, IA based corporation formed to promote the use of computers in general and Microware's OS-9 operating system in particular. This year's officers were elected at the Second Annual Microware OS-9 seminar during August, 1983. They were given a mandate to incorporate the group, establish a method of communication and set up a software exchange. Progress is being made in all areas. However, to mimic an old saying, four officers does not a dynamic group make.

### WHAT CAN YOU DO FOR US?

You, the OS-9 user, are the lifeblood of the group. If you take an active part in the

organization by contributing programs to the software exchange, articles for the newsletter and technical help for the newcomers, we will some day be a viable force in the industry.

#### WHY SHOULD WE CARE?

The 6809 microprocessor -- the best on the market -- has run behind all others in the personal computing field since the beginning because there has been no coordination and cooperation. With the increase in the number of users made possible by Tandy's Color Computer version of OS-9, we again have a chance to become a viable force in the market place.

#### WHAT KIND OF HELP DO WE NEED?

- a. We need engineers and systems programmers to help George get the bulletin board system on line.
- b. We need recruiters and personnel types to help Peter increase the membership.
- c. We need people to help Dave Gibson put out the newsletter. We need people to manage the software library.
- d. We need others to write articles and stir up publicity.
- e. And, we need people to coordinate the exchange of information between the many local OS-9 users groups we hope to inspire.

Please, say you want to help. Send us a letter and tell us what you would like to do. We'll put you in touch with the proper committee chairman immediately. And remember if you solve a problem or create something you're proud of, send it to us for publication in MOTD and leave a note here on the Compuserve OS-9 SIG.

#### WHAT IS YOUR MEMBERSHIP POLICY?

Your membership will be good through January 1, 1985. Renewals at or after the Third Annual OS-9 Seminar in August 1984, will be good from then through December 31, 1985. All members, regardless of when they join, will receive all services of the Users Group provided since August, 1983. All OS-9 Users Group members will have access to the special Compuserve XA-4 data base which contains our complete software exchange library.

#### WHAT ARE YOUR DUES?

Every member pays \$25.00 per year. However, since there was very little activity during the first year the group existed and since we got off to a slow start this year, anyone who has joined in the past will not have to pay dues again until 1985.

#### WHAT DO I GET FOR MY MONEY?

We are trying to push forward on several fronts. I feel that the most important thing we are doing for you is maintain an excellent software exchange library. Dave Kaleita has done a terrific job so far. Then, it will be up to you the members to keep contributing.

I also hope we will be able to publish a few more news letters this year. We have planned to make the next one a technical issue filled with questions and answers. You should see it by the end of April. Please. If you have a hot bit of news you think everyone should know. Send it to:

MOTD  
OS-9 Users Group  
P. O. Box 8027,  
Des Moines, IA 50301

#### HOW DO I LET YOU KNOW I WANT TO HELP?

Send a letter to me at the address above. Please mark the outside of the envelope, "Volunteer." I will then contact the proper chairman and he will get in touch with you.

#### HOW ARE YOU GOING TO HANDLE THE INFLUX OF COCO OS-9 USERS?

Bringing the CoCo OS-9 user on line and up to speed is one of our most important priorities. The more we can help them prosper, the faster OS-9 will grow in popularity. That means it will hold a more dominant position in the marketplace and there will be more software available for all of us.

#### HOW ARE YOU ORGANIZED?

We have already set up several standing committees. Unfortunately, we have almost no one to fill the positions. However, I have defined two more that we will set up as soon as we have people to fill the positions.

Presently, the only committee chairman that is active is Dave Kaleita who heads our software exchange efforts. So far it has been a committee of one.

Kudos to "Van" and "Jack" who have volunteered to work with Dave to organize our CoCo software holdings. Thanks! This is the kind of spirit we need. If this goes the way we think it will ... Dave, Van and Jack are going to need a lot more help. Please send your "volunteer" letter.

#### WE NEED A Communications CHAIRMAN?

Our communications committee chairman has disappeared into the wordwork and George Dorner, our fantastic treasurer has had to struggle with the problems alone. Please, help him.

We have set a goal to get our OS-9 Bulletin Board on the air this year. The equipment, donated by Richard Don at GIMIX, is already on line. Now all we need are the engineers. If you live in the Chicago area -- anywhere for that matter -- and can help, please call George.

Our end goal is to have a bulletin board that looks and acts like OS-9.

#### WE NEED A MEMBERSHIP COMMITTEE?

So far, our vice president, Peter Dibble, has had to handle the membership committee chores. If you are good with people -- an Army recruiter maybe -- drop us a line. Please!

#### WHAT OTHER COMMITTEES WILL YOU BE ADDING?

a. I plan to appoint a committee to coordinate local club activity. This is an area where we can really help the CoCo crowd. This will increase the exchange of information and bring the help of the oldtimers to our new found friends.

b. I plan to appoint an education committee. This committee will help prepare the question and answer section for our newsletter. I also plan to have them work with Jim Bellomo and hope we will be able to put an OS-9 Q & A help data base here on the OS-9 SIG. This new committee will work out the details.

c. I need a Chicago area volunteer(s) to serve as an interface between the Compuserve OS-9 SIG and the OS-9 Bulletin Board. If you can handle it, drop me a line.



d. I plan to appoint a by-laws committee -- all officers will be a member also -- to study several issues. The most important is the election of officers.

#### WHAT'S THE BOTTOM LINE?

Remember, if you do nothing, the group will not live long, and you will get nothing from it. It's all up to you.

---

## READING NON-FLEX BASED DISKS

### Reading Non-Flex Based Disks

How many times have you wanted to use your Flex system to read and copy disks formatted on other non-compatible operating systems? If you are like me and have access to disks formatted under CP/M, NEWDOS-80 or SSB's DOS69D, then you will find the following three transfer utilities very useful indeed.

Even if you have no use for such transfer functions, some of the coding routines and techniques used by these utilities are invaluable for those of you who write your own Flex assembler programs.

I have included three programs; one which transfer TRS-80 NEWDOS-80 V.2 formatted data to Flex; and one which transfer CP/M, NEWDOS-80 or SSB's DOS69D, then you will find the following three transfer utilities very useful indeed.

The common routines used by all three utilities are READSS, MCY, CLC, PDATA, ROUTE, PRTSET, and RESET. Before explaining the usefulness of some of these subroutines to the Flex programmer, I would like to describe the operations of the transfer programs in general.

All three programs allow the user to view the particular disk's directory of files and transfer any existing file on that disk to FLEX (or to your terminal or printer). Each has a similar menu describing the possible functions. A help selection has been included for the CP/M and DOS utilities. This selection describes the features of the program. Since the basic facilities implemented in all three programs are the same, the help selection for the TRS utility has been omitted.

Let us now look at the limitations of each utility in turn.

The CPM-to-FLEX utility was tested using disks from the "C User's Group" and from the Osborn system. It will handle both 5" and 8" disks to the extent outlined in the program code.

The DOS-to-FLEX utility was the easiest to implement because it required no special mapping functions to read the data. Since I also run DOS69D on my 6809 system, I was able to write the program with few problems.

The TRS80-to-FLEX utility was the most difficult to implement because the disk data structure used by NEWDOS-80 is quite different from what I am used to. The concept of granules, lumps, GATs, HITs and FDEs is anything but straightforward. The documentation included at the beginning of the program explains most of the assumptions made in the code. In addition, the utility will only handle files having no more than four space areas assigned to it (in other words, it has no extents).

Now let us take a look at some of the subroutines common to the utilities, specifically, ROUTE and PRTSET.

These two functions illustrate how to use FLEX resident routines to make the routines you write more FLEXible (ha ha ha). The ROUTE subroutine allows output to be routed to a terminal, printer or file, by using OSWCH in conjunction with the OUTCH, OUTCH2 and PUTCHR routines supplied by FLEX. This is how the routine works (description taken from TSC's FLEX Programmers Manual, 1978).

If the output switch (OSWCH) is zero, output performed by the PUTCHR routine is through OUTCH and if non-zero OUTCH2 is used. The OUTCH routine usually does the same as OUTCH2, however, OUTCH may be changed by programs to refer to some other output routine (such as one to drive a printer). OUTCH2 is never changed.

By using PUTCHR to output all "routeable" information, we now have a mechanism to change the route very easily. If the output switch is non-zero, the routine OUTCH2 is used by PUTCHR to send the character. If zero, PUTCHR checks the location "File Output Address (FOA)". If it is non-zero the contents of this location is used as an address of an FCB of a previously opened-for-write file, and the character is written to the file. If zero, the routine OUTCH is called to process the character. Normally, OUTCH sends the character to the terminal. The user may, however, change the address portion of the OUTCH entry point to go to another character output routine (such as a printer output routine).

By using ROUTE along with PRTSET and RESET, the user can enhance any FLEX utility to allow routeable output. PRTSET is used to load the printer output routine if it has not already been loaded. RESET is needed to reset the output switch and close any file that might have been opened for output. It is called at the end of each selection function (COPY and DIR).

The reader will also notice the use of DSECTS and dynamic data areas (using the system stack) in the utilities. Study how these structures are used to make coding easier and more readable.

For those readers who do not wish to type in all the code, I can supply you with Xerox laser printed listings of all the programs for \$5.00 (to cover postage and handling). Or, if you want the source code to some or all of the utilities on disk, send \$4.00 per program, \$8.00 minimum.

### Happy Programming !

Editor's Note: Due to the extensive size of the assembled source listing of all three major programs, they will be continued over a period of 3 or 4 months, as available space permits.

Readers interested in not waiting or desiring not having to type in the large amount of code should take advantage of the printout or disk offer above.

DMW

---

```
*****
*                                     *
*      CPM to FLEX Utility           *
*                                     *
*      Written by: Scott R. Fraser   *
*      547 Sharron Bay              *
*      Winnipeg, Manitoba, Canada    *
*      R2G 0H8                      *
*      Ph. (204) 338-7641            *
*                                     *
*****
```

```
+ Define CPM directory dsect
*
0000      ORG      00000
0000      CPMDIR  EQU      *
0000      CPMFEF  RMB      1      file entry flag
0001      CPMNAM  RMB      8      1st part of name
0008      CPMNL  EQU      <CPMNAM  len of this part
```

```

0009      CPNEXT RMB 3      extension
0003      CPNEL EQU *CPNEXT length of extension
0008      CPNEL EQU *CPNEXT len of entire name
000C      CPNCF RMB 1      continuation flag
000D      CPNCF RMB 2      unused
000F      CPNCF RMB 1      file sector count
0010      CPNBLK RMB 16     list of blocks
0010      CPNBS EQU *CPNBLK # blocks per file entry
0020      CPNLEN EQU *      length of CPM dsect

0100      ORG $0100
*
**** DEFINE PROMPTS USED BY ALL ROUTINES
*
* PDIR routine
*
0100 HEAD EQU *
0100 000A FDB CRLF
0102 4E 41 4D 45 FCC /NAME TYPE SIZE BLOCKS/
0110 20 20 20 45 FCC / EXTENTS/
0127 000A FDB CRLF
0129 04 FCB EOT

012A WELCOME EQU *
012A 000A FDB CRLF
012C 44 65 73 63 FCC /Description of Selections:/
0144 000A 000A FDB CRLF,CRLF
014A 20 20 20 30 FCC / 0 - you're looking at it buddy'/
016C 000A FDB CRLF
016E 20 20 20 31 FCC / 1 - this selection will yield a CPM /
0195 64 69 72 65 FCC /directory listing./
01A7 000A FDB CRLF
01A9 20 20 20 20 FCC / The user is prompted for the drive/
01B2 20 6F 66 20 FCC / of the CPM disk/
01E2 000A FDB CRLF
01E4 20 20 20 32 FCC / 2 - this selection will copy a /
0206 67 69 76 65 FCC /given CPM file to a/
0219 000A FDB CRLF
021B 20 20 20 20 FCC / FLEX file. The user is/
0239 20 70 72 6F FCC / prompted for the "from" drive/
0257 000A FDB CRLF
0259 20 20 20 20 FCC / (ie. where the CPM disk is)/
027B 20 61 6E 64 FCC / and the "from" file/
028F 000A FDB CRLF
0291 20 20 20 20 FCC / name. No conversion (say/
02B1 20 6C 6F 77 FCC / lowercase -> uppercase/
02CB 000A FDB CRLF
02CA 20 20 20 20 FCC / for example) is done on this/
02ED 20 6E 61 6D FCC / name, so type it/
02FE 000A FDB CRLF
0300 20 20 20 20 FCC / exactly as it appears in your/
0324 20 64 69 72 FCC / directory listing./
0337 000A FDB CRLF
0339 20 20 20 33 FCC / 3 - exit program/
034C 000A 000A FDB CRLF,CRLF
0350 4D 6F 72 65 FCC /More to Come ...../
0367 04 FCB EOT

0368 WELCOME EQU *
0368 000A 000A FDB CRLF,CRLF
036C 4E 6F 74 65 FCC /Note: a) the user may hit (Return)/
038F 20 69 6E 73 FCC / instead of typing a/
03A3 000A FDB CRLF
03A5 20 20 20 20 FCC / drive number. In this case/
03CA 20 74 68 65 FCC / the last specified/
03D0 000A FDB CRLF
03D2 20 20 20 20 FCC / drive number will be used/
0402 20 2B 6F 72 FCC / for the FLEX work/
0414 000A FDB CRLF
0416 20 20 20 20 FCC / drive in the case no previous/
043D 20 64 72 69 FCC / drive was given//
044E 000A 000A FDB CRLF,CRLF
0452 20 20 20 20 FCC / b) the user has the option of/
0476 20 72 6F 75 FCC / routing his output/
0489 000A FDB CRLF
048B 20 20 20 20 FCC / from selection 1 or 2, to/
04A6 20 74 68 65 FCC / the printer, terminal./
04C3 000A FDB CRLF
04C7 20 20 20 20 FCC / or a file. If "printer"/

```

```

04EA 20 69 73 20 FCC / is selected and the/
04FE 000A FDB CRLF
0500 20 20 20 20 FCC / FLEX printer module hasn't/
0525 20 62 65 65 FCC / been loaded, it/
0535 000A FDB CRLF
0537 20 20 20 20 FCC / (PRINT.SYS) will/
0552 20 61 75 74 FCC / automatically be loaded/
056A 000A FDB CRLF
056C 20 20 20 20 FCC / from drive 0. If "file"/
058F 20 69 73 20 FCC / is selected, the user/
05A5 000A FDB CRLF
05A7 20 20 20 20 FCC / will be prompted for the/
05CA 20 22 74 6F FCC / "to" file name. Type/
05E0 000A FDB CRLF
05E2 20 20 20 20 FCC / this as a standard FLEX/
0604 20 66 69 6C FCC / file specification (esp:/
061C 000A FDB CRLF
061E 20 20 20 20 FCC / 0..A000.TXT). The user/
063F 20 77 69 6C FCC / will also be asked/
0652 000A FDB CRLF
0654 20 20 20 20 FCC / whether the file is/
0672 20 22 74 65 FCC / "text" or "binary". If/
068A 000A FDB CRLF
068C 20 20 20 20 FCC / type "binary" is chosen./
06AF 20 74 68 65 FCC / the FLEX space/
06BE 000A FDB CRLF
06C0 20 20 20 20 FCC / compression flag will be/
06E3 20 73 65 74 FCC / set and no expansion/
06FB 000A FDB CRLF
06FA 20 20 20 20 FCC / of tabs, etc. is performed./
0720 20 20 49 66 FCC / If ROUTE=P or T./
0732 000A FDB CRLF
0734 20 20 20 20 FCC / tabs will be expanded./
0755 000A 000A FDB CRLF,CRLF
0759 45 6E 74 65 FCC /Enter (Return) to Continue.../
0776 04 FCB EOT

0777 DIRMSG EQU *
0777 000A FDB CRLF
0779 20 20 20 20 FCC / Directory of Drive /
0792 OUTDRV RMB 1 put drive# here
0793 000A FDB CRLF
0795 04 FCB EOT

0796 ENDIR EQU *
0796 20 20 20 45 FCC / End of Directory Listing. /
07B3 48 69 74 20 FCC /Hit (Return) to Continue.../
07CE 04 FCB EOT

07CF ENDCOP EQU *
07CF 20 20 20 45 FCC / End of Copy. Enter /
07E3 38 52 65 74 FCC / (Return) to Continue.../
07FC 04 FCB EOT

07FD DRVPRM EQU *
07FD 20 20 20 45 FCC / Enter drive of CPM disk: *
081A 04 FCB EOT

081B OUTNAM RMB CPMNL
0823 2E FCC /./
0824 OUTEXT RMB CPMEL
0827 04 FCB EOT

*
* BLKOUT routine
*
082B 20 20 20 SP3 FCC / / 3 spaces
082B 04 FCB EOT

*
* COPYIT routine
*
082C INFILE EQU *
082C 20 20 20 45 FCC / Enter "from" file: /
0842 04 FCB EOT

0843 INDRV EQU *
0843 20 20 20 45 FCC / Enter "from" drive: /
085A 04 FCB EOT

```

```

085B 085B OUTFIL EQU *
095B 20 20 20 4F FCC / Output to File(F), Printer(P), /
087D 6F 72 20 54 FCC /or Terminal(Tel)? /
088E 04 FCB EDT

088F 088F TUFIL EQU *
088F 20 20 20 45 FCC / Enter "te" file: /
08A3 04 FCB EDT

08A4 08A4 BAOFIL EQU *
08A4 46 49 4C 45 FCC /FILE DOES NOT EXIST'/
08B8 04 FCB EDT

08B9 08B9 INVSFC EQU *
08B9 49 4E 56 41 FCC //INVALID FILE SPECIFICATION'/
08D4 04 FCB EDT

08D5 08D5 FIIST EQU *
08D5 53 4F 52 52 FCC /SORRY, FILE ALREADY EXISTS/
08EF 04 FCB EDT

08F0 08F0 FTYPE EQU *
08F0 20 20 20 49 FCC / Is file type Binary(B) /
090A 6F 72 20 54 FCC /or Text(Tel)? /
0917 04 FCB EDT

0918 0918 DTYPE EQU *
0918 20 20 20 49 FCC / Is this an 8" disk/
092D 20 28 64 65 FCC / (default is 5") (Y/N)? /
0946 04 FCB EDT

*
* Misc
*
0947 3C 3C 3C 20 FCB /<<< READ ERROR >>>'/
095A 04 FCB EDT

095B 50 52 49 4E PSYS FCC //PRINT/
0960 00 00 00 FCB 0.0.0
0963 53 59 53 FCC //SYS/

0966 0966 INTRO EQU *
0966 000A 000A FCB CRLF,CRLF
096A 20 20 20 46 FCC * FILE/CPM UTILITY (C) 1983"
0986 20 62 79 20 FCC * by Scott Fraser"
0996 000A 000A FCB CRLF,CRLF
099A 20 20 20 20 FCC / 0 - Directions/
09B2 20 20 20 20 FCC * 1 - CP/M Directory"
09CE 000A FCB CRLF
09D0 20 20 20 20 FCC / 2 - Copy File/
09E7 20 20 20 20 FCC / 3 - EXIT Program/
0A02 000A 000A FCB CRLF,CRLF
0A06 20 20 20 45 FCC / ENTER SELECTION: /
0A1A 04 FCB EDT

0A1B 0A1B BADIN EQU *
0A1B 20 20 20 49 FCC / INVALID SELECTION, REENTER: /
0A3A 04 FCB EDT

0A3B FILFCB RMB FCBLEN reserve FCB area

*
* Define some CP/M constants
*
0008 SECBLK EQU 8 8 sectors per block
000D SSIZ8 EQU 128 8bytes per sector (8")
0010 SSIZ5 EQU 256 8bytes per sector (5")
000F OFF8 EQU 2 data offset for 8" disk
0000 OFF5 EQU 3 data offset for 5" disk
001A EOF EQU 91A delimits end of file (CMTL 2)
0000 IIRBEG EQU 0 block 0 of start of CP/M dir
00E3 DREND EQU 0E3 signifies end of a dir block
0009 TABEND EQU 909 universal tab char (CtrlI)

087B DRV RMB 1 holds current drive#
087C 00 OPNPLG FCB 0 file open bits
0080 OPN EQU 2100000000 0file is open
0040 TEX EQU 2010000000 0file is text
0020 BINRV EQU 2001000000 0file is binary
087B TTYWID RMB 1 save TTY Width value here
087E 00 CONFIG FCB 0 0=5" disk, otherwise 8" disk

```

```

087F BUFSIZ RMB 2 holds buffer size
0881 BLKDNV RMB 2 size of file is sectors
0883 EDBUF RMB 2 end of buffer address
0885 SECSIZ RMB 2 holds sector size
0887 MAPADR RMB 2 holds addr of map tble
0889 MAPLEN RMB 1 holds len of map tble

0800 BSIZ5 EQU SSIZ5+SECBLK size of buffer (5")
0400 BSIZ8 EQU SSIZ8+SECBLK size of buffer (8")

088A BUFFER RMB BSIZ5*2 alloc max buffer size

0050 EXTSIZ EQU CPMS*5 block list size
:90A BLKLIST RMB EXTSIZ reserve for block list

*ORG UCA
180A 20 01 START BRA START1 ***** Begin Program *****
180C 04 FCB 4 <<<< VERSION # >>>>

180D 10FE CC2B 180D START1 EQU *
LDI LDG MEMEND initialize system stack ptr

18E1 8E 0918 LDI 80TYPE ask user for disk type
18E4 80 C01E JSR PSTRNQ
18E7 80 C009 JSR INCI
18EA 84 5F ANDA 015F get response from user
lowercase->uppercase

18EC 81 99 ORCA 0-Y reply is yes for 8"
18EE 26 03 BNE NO8 brnch if 5" (default)

18FD 7C 087E INC CONFIG set config for 8"
18F3 NO8 EQU *
*
* Initialize some constants first
*
18F3 108E 2097 LDI 0000 MAPTES addr of mapping tble
18F7 7D 087E TST CONFIG
18FA 27 11 BEQ FIVE setup buffer size for
type of disk

18FC 8E 0400 LDI 0BSIZ8
18FF 108E 207D LDI 0000 MAPTES
1C03 0C 0080 LDI 0SSIZ8
1C08 FD 0885 STD SECSIZ
1C09 86 1A LDA 0TBLEN8
1C0B 20 08 BRA STSIZ
1C0D FIVE EQU *
1C0D 8E 0800 LDI 0BSIZ5
1C10 CC 0160 LDI 0SSIZ5
1C13 FD 0885 STD SECSIZ
1C16 86 0A LDA 0TBLEN5
1C18 STSIZ EQU *
1C18 8F 087F STI BUFSIZ
1C1B 108F 0887 STI MAPADR
1C1F 87 0889 STA MAPLEN
1C22 86 C00C LDA 0DRV
1C25 87 087B STA DRV
1C28 8E 1C34 LDI 0MEMU
1C2B 8F CC16 STI 0BCDR
1C2E 86 0C04 LDA 0WIDTH
1C31 87 087D STA TTYWID

1C34 7F CC1A 1C37 MEM1 EQU *
CLR CLN clear current line num 1st
EQU *
1C37 7C CC22 1C37 MEM2 INC 0SMFCH
1C3A 8E 0966 LDI 0INTRO
1C3B 8D C01E JSR PSTRNQ ask for selection

1C40 8D C009 EQU *
1C43 8D C021 JSR INCH
1C46 25 42 BCS NO bad selection
1C48 81 33 ORCA 0-Y over 37

```



```

1C4A 22 3E      BHI  M1      bad selection
1C4C 84 0F      ANDA  #00001111 keep low 4 bits only
1C4E 48         ASLA                     selection=selection*2
1C4F 8E 1C54    LDX  #J      addr of jump table
1C52 30 86      LEAX  A,1      addr proper selection
1C54 6E 84      JPP  0,1      goto selection

1C56 20 06      JF      FRA  M0      selection=0
1C58 20 18      BRA  M1      =1
1C5A 20 20      BRA  M2      =2
1C5C 20 29      BRA  M3      =3

1C5E 8E 012A    EQU  *          do selection 0
1C61 80 C01E    LDX  #MELCOM print instructions
1C61 80 C01E    JSR  PSTRING

1C64 86 C003    LDA  DEPTH      breather
1C67 87 C01A    STA  CLN
1C6A 8E 0368    LDX  #MELC02 more to come
1C6B 80 C01E    JSR  PSTRING
1C70 20 20      BRA  SETPAU

1C72 80 32      EQU  *          PDIR      print dir
1C74 8E 0796    LDX  #MENDIR
1C77 80 C01E    JSR  PSTRING
1C7A 20 16      BRA  SETPAU

1C7C 80 0139    EQU  *          LBSR  C0PRIT  copy file
1C7F 8E 070F    LDX  #MENDCOP
1C82 80 C01E    JSR  PSTRING
1C85 20 1B      BRA  SETPAU

1C87 7E C003    EQU  *          JPP  M3      return to FLEI
1C87 7E C003    JPP  M3      return to FLEI

1C8A 8E 0A18    EQU  *          LDX  #MADIN  bad selection
1C8B 80 C01E    JSR  PSTRING
1C8D 20 AE      BRA  FROM      print msg

1C92 86 C003    EQU  *          LDA  DEPTH      pause (if set)
1C95 87 C01A    STA  CLN      so user can read stuff
1C98 20 90      BRA  MDC

1C9A 80 C03F    EQU  *          JSR  #MRO2  report error first
1C9B 8E 0947    LDX  #MRO2      get msg

1CA0 80 C01E    EQU  *          JSR  PSTRING  print msg
1CA3 7E C003    JMP  M3      and return to FLEI

*
* Name - PDIR
* Function - This routine is called to print out
*            directory information on a CP/M disk.
*            The directory starts at block 0.
*
*
* No parms are required
* All registers are used and not restored
*
1CA6 32 E9 FFFC  EQU  *          LEAS  -LSI.S  alloc local storage

1CA8 8E 07FD    LDX  #MORVMT  ask for a drive
1CA9 80 C01E    JSR  PSTRING
1CB0 17 01D4    LBSR  GETDRV  get drive #
1CB3 25 78      BCS  PDIXIT  if bad then leave

1CB5 17 0427    LBSR  ROUTE   where to route output
1CB8 25 73      BCS  PDIXIT  leave if bad
1CBA 80 C024    JSR  PDRLF
1CB8 86 0878    LDA  DRV      get drive
1CC0 87 C843    STA  SYSFCB+FCB0H save in FCB
1CC3 8A 30      ORA  #0       convert to ascii
1CC5 87 0792    STA  OUTDRV   save to print

```

```

1CC9 8E 0777    LDX  #DIRMSG  print msg
1CCB 17 0401    LBSR  PDATA

1CCE 8E 0106    LDX  #HEAD   print dir header
1CD1 17 03FB    LBSR  PDATA

1CD4 8C 0B7F    LDD  BUFSIZ  get buffer size
1CD7 58         ASLB                     and double it to hold
1CD8 49         ROLA                     all of dir data
1CD9 8E 0B8A    LDX  #BUFFER  calc end of buf ptr
1CD9 8E 0B8A    LEAX  D,1
1CDE AF E9 0002 STX  DSIZI.S  and save it

1CE2 8E 0B8A    LDX  #BUFFER  where to put data
1CE3 86 00      LDA  #DIRBEG start with 1st block of dir
1CE7 17 02F8    LBSR  GETBLK  get first block
1CEA 4C         INCA
1CEB 17 02F4    LBSR  GETBLK  and get second block

*
* BUFFER now has all directory data in it
*
1CEE 8E 0B8A    LDX  #BUFFER  position to 1st entry

1CF1 80 PD1     EQU  *          STX  ENTADR.S  save entry addr

1CF3 86 B4      LDA  CPMF,1  set entry indicator
1CF7 81 E3      CMPA  #DIREND does this entry exist?
1CF7 26 05      BNE  PD7      branch if res
1CF9 30 88 20   LEAX  CPMLEN,X goto next entry
10FE 20 27      BRA  PD8      and see if done yet

*
* Now print file name
*
1000 30 01      EQU  *          LEAX  #PMAN,1 "from" field
1002 C6 08      LDB  #CPMFL  length to move

1004 86 80      EQU  *          LDA  0,1+  get a char
1006 26 02      BNE  ++2+2  skip if not zero
1008 86 20      LDA  #SP
100A 80 C018    JSR  PUTCHR  print char
100D 5A         DBCB
100E 27 08      BEQ  PD6      exit if done
1010 C1 03      CPMF  #CPMFL  at extension ref?
1012 26 F0      BNE  PD5      branch if not
1014 86 2E      LDA  #'.      print a '.' first
1016 80 C018    JSR  PUTCHR
1019 20 E9      BRA  PD5      and continue

101B AE E9 0000 EQU  *          LDX  ENTADR.S
101F 17 00F4    LBSR  EXTNTS  get size of file and
1022 80 25      BSR  BLKOUT  print size and list
1024 80 C024    JSR  PDRLF

1027 AC E9 0002 EQU  *          CPMF  DSIZI.S  end of buffer ref?
102B 25 C4      BLO  PD1      branch if not

102D 17 0485    EQU  *          LBSR  RESET  clear things up first

1030 32 E9 0004 LEAS  LSI.S  release local storage
103A 39         RTS  return

*
* Data area for PDIR routine
*
1035 LPC SET *
0000 ORG #0000
0000 LCL SET *
0002 ENTADR FCB 2
0004 DSIZI FCB 2
0004 LSI EQU *-LCL  len of local area

1035 ORG LPC restore PC

```

\* Name - UNKOUT  
 \* Function - This routine prints out the 4 byte field in the CPM dsect starting with field "CPMDF". Printed only for debugging purposes.  
 \*  
 \*  
 \* On entry the X res points to the beginning of the 4 byte field.  
 \*  
 \*  
 \* Regs A and B are preserved.

```

1035 34 06      1035 UNKOUT EQU *
1037 86 20      PSNS D      save regs
1039 BD CD18    LDA BSP     print a space first
                        JSR PUTCHR

103C C6 04      LDB B4     print 4 bytes

103E BD CD3C    LDME UNK1 EQU *
1041 30 01      JSR OUTHEX print in hex
1042 5A        LEAX 1,X    to next byte
1044 26 F8      BNE UNK1   done yet?
                        branch if not

1046 35 06      PULS D      restore regs
1048 39        RTS         and return
  
```

\* Name - BLKOUT  
 \* Function - This routine prints out the size of the current file entry. ACC A contains the size of the file, and the area BLKLIST contains the list of blocks  
 \*  
 \*  
 \* belongs to the current file.  
 \*  
 \*  
 \* All registers are preserved

```

1049 34 36      1049 BLKOUT EQU *
104B 32 E9 FFFB PSNS A,B,X,Y save regs
                        LEAS -LS2,S alloc local storage

104F 1F 89      TFR A,B    copy file size
1051 86 20      LDA BSP     print a space
1053 BD CD18    JSR PUTCHR

1056 4F        CLRA
1057 7D 0B7E    TST CONFIG is the 5" or 8" disk?
105A 26 01      BNE @2+1L do not double if 8"
106C 58        ASLB        double file size for 5" disk
105D ED E9 0000 STD OUTSIZ,S save size of file
1061 30 E9 0000 LEAX OUTSIZ,S and print it
1065 C6 01      LDB #1     set spc suppression flag
1067 BD C139    JSR OUTDEC

106A 86 4B      LDA #K     with "K" prefix
106C BD CD18    JSR PUTCHR

106F 8E 0B28    LDX BSP3   print spaces
1072 17 035A    LBSR PDATA

1075 3E 0B81    LDX @BLKLIST print file size in sectors
1078 C6 01      LDB #1     set spc suppression flag
107A BD CD39    JSR OUTDEC
107D 8E 0B28    LDX BSP3
1080 17 034C    LBSR PDATA

1083 8E 1B8A    LDX @BLKLIST point to block list
1086 F6 CC29    LDB C0C   save current col #
1089 E7 E9 0002 STB COLUME,S
108D BD 108D    EQU *
108D C6 10      LDB #16   print 16 blks/line

108F BD 108F    EQU *
1091 27 1E      TST 0,X    end of list yet?
                        BEB BLK4 branch if yes

1093 BD CD3C    JSR OUTHEX print block#
1096 86 20      LDA BSP     print space
1098 BD CD18    JSR PUTCHR
  
```

```

1098 30 01      LEAX 1,X    go to next block
109D 5A        DECB        printed 16 yet?
109E 26 EF      BNE BLK3   branch if not

10A0 BD CD24    JSR PCRLF
10A3 E6 E9 0002 LDB COLUME,S space over to col COLUME
10A7 86 20      EQU *
10A7 BD 20      LDA BSP
10A9 BD CD18    JSR PUTCHR print space
10AC 5A        DECB
10AD 26 F8      BNE BLK6   continue spacing

10AF 20 DC      BRA BLK5   print another line now
  
```

```

10B1 BD 10B1    EQU *
10B1 32 E9 0003 LEAS LS2,S release local storage
10B5 35 36      PULS A,B,X,Y restore regs
10B7 39        RTS         return
  
```

\* Data area for BLKOUT routine

```

10B8 32 0000    10B8 LPC SET *
10B8 32 0000    LCL ORG #0000
10B8 32 0000    SET *
10B8 32 0000    OUTSIZ RMB 2
10B8 32 0002    COLUME RMB 1
10B8 32 0003    LS2 EQU #LCL len of local area

10B8 32 0000    ORG LPC restore PC
  
```

\* Name - COPYIT  
 \* Function - This rtn is called to copy a CP/M file to a FLEX file. The user is prompted for a "from" file (off the CP/M disk) and a "to" file (in FLEX), if the "to" file is not specified, output goes to the terminal.

\* All registers are used and not restored

```

10B8 32 E9 FFF3 10B8 COPYIT EQU *
10B8 32 E9 FFF3 LEAS -LS3,S alloc local storage

10BC 8E 0B43    LDX @INDRV set "from" drive
10BF BD CD1E    JSR PSIRNG
10C2 17 00C2    LBSR @ETDRV set drive#
10C5 25 47      BCS DP2    if bad then leave

10C7 8E 0B2C    LDX #INFILE prompt for "from" filename
10CA BD CD1E    JSR PSIRNG
10CB BD CD18    LDB INBUF set "from" filename
10CD 30 E9 0000 LEAX SVEFIL,S where to put filename
10D0 17 0000    LBSR GYSPEC set CP/M filename
10D4 25 35      BCS DP2    if bad then leave

10D9 17 0110    LBSR SRCHM go do search
10DC 24 08      BCC GETD   if found, get "to" file
  
```

\* File not found in CP/M directory -> flag  
 \* and reprompt user.

```

10DE 8E 0B44    LDX @BROFIL print msg
10E1 BD CD1E    JSR PSIRNG
10E4 20 28      BRA DP2    return
  
```

\* The CP/M directory entry has been found.  
 \* This entry and possibly subsequent entries,  
 \* contains a list of block extents that the  
 \* file covers on the disk. Copy all these

```

10E6 8D 2E      area
10E6 BD 2E      BSR EXTENTS set all the extents

10E6 8D 2E      *
10E6 8D 2E      * Find out where to route the output
10E6 8D 2E      *
10E8 17 02F4    LBSR ROUTE set route
  
```

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\* Name - GTSPEC  
 \* Function - This routine parses the system INBUF buffer for a CP/M file name. A valid filename must contain up to 8 chars, then an optional '.' and up to 3 char file extension. The I register points to an area to place the 11 char file name and any unused chars are padded with spaces.  
 \* Carry is clear if file spec ok, else it is set.  
 \* All regs are preserved.

1EAC 34 36 1EAC GTSPIC EQU \*  
 PSHS A,B,X,Y save regs

\* First blank out the "to" field

1EAE 86 20 LDA BSP pad with spaces  
 1EB0 A7 84 STA 0,X  
 1EB2 31 01 LEAY 1,X "to" field  
 1EB4 CC 000A LDO @CPMUL-1 length to move  
 1EB7 17 01F1 LBSR MVC move in spaces

1EBA 10BE C0B0 LDY @LINEBUF pt to system buffer  
 1EBE 86 00 LBA @CPMUL 0 chars to move

1EC0 E6 A4 LDB 0,Y user just hit return?  
 1EC2 C1 00 CMBB BCR

1EC4 27 21 BDB GTBAD yes, then bad return  
 1EC6 E6 A0 EQU 0  
 LDB 0,Y+ set a char

1EC8 C1 20 CMBB BSR space here?  
 1ECA 27 FA BEG GT1 skip spaces

1ECC C1 2E CMBB B. have an extension  
 1ECD 26 0A BNE GT2 no, then continue

\* The rest of the filename from the system  
 \* buffer is the extension. Adjust the  
 \* "to" ptr so that any unused chars in the  
 \* "to" name are spaces

1ED0 1F 89 TFR A,B copy A  
 1ED2 C0 03 SUBB @CPMUL  
 1ED4 2B 11 BBI GTBAD if neg then bad name  
 1ED6 30 85 LEAX B,X adjust "to" ptr  
 1ED8 20 EC BRA GT1 and continue

1EDA GT2 EQU \*  
 1E0A C1 00 CMBB BCR end yet?  
 1E0C 27 05 BEG GT3 done if so

1EDE E7 80 STB 0,X+ save to "to" field  
 1EE0 4A 0ECA DECA moved all chars?  
 1EE1 26 E3 BNE GT1 brnch if not

1EE3 GT3 EQU \*  
 1EE3 1C FE CLC good return  
 1EE3 30 02 BRA GTGOOD

1EE7 GTBAD EQU \*  
 1EE7 1A 01 SEC set bad return  
 1EE9 35 36 GTGOOD PULS A,B,X,Y restore regs  
 1EEB 39 RTS and return

\* Name - SROM  
 \* Function - This routine searches the directory on a CP/M disk for the file name pointed to by the I register. "SYSFCB" is the FCB used, and is assumed to contain the drive where the CP/M disk lies  
 \* On exit, I->CP/M file entry if found

file. If file is not found, the carry is set, otherwise it is clear

All registers are preserved

1EEC 34 26 1EEC SROM EQU \*  
 PSHS A,B,Y save regs  
 1EE2 32 E9 FF7E LEAS -LSS,S alloc local storage  
 1EF2 1F 12 IFR X,Y copy file ptr to Y  
 1EF4 FC 0B7F LDB BUFSIZ set size of buffer  
 1EF7 5B ASLB and double to hold all  
 1EF8 49 MOLA dir data  
 1EF9 8E 0B8A LDI @BUFFER calc end of buf ptr  
 1EFC 30 88 LEAX D,X  
 1EFE AF E9 0000 STX DSIZ2,S and save it

1F02 8E 0B8A LDI @BUFFER where to put dir data  
 1F05 86 00 LDA @DIRBEG set first dir block  
 1F07 17 00B8 LBSR GETBLK and read it in  
 1F0A 4C INCA get next block  
 1F0B 17 00DA LBSR GETBLK and read it in too  
 1F0E 1F 21 TFR Y,X copy back file ptr to X

1F10 10BE 0B8A LDY @BUFFER Y-> dir data  
 1F14 SRC2 EQU \*  
 1F14 A6 A4 LDA @CPHFY file entry exists?  
 1F16 B1 E5 CPHA @DIREND  
 1F18 27 00 BEG SRC3 branch if not

\* Compare dir file with file set down to  
 \* see if a match

1F1A 34 20 PSHS Y save entry ptr  
 1F1C 31 21 LEAY @CPHLEN,Y Y->string2  
 1F1E 86 08 LDA @CPMUL len to compare  
 1F20 17 019E LBSR CLC compare

1F23 35 20 PULS Y restore entry ptr  
 1F25 27 0E BEG FOUND brnch if a match

1F27 SRC3 EQU \*  
 1F27 31 A8 20 LEAY @CPHLEN,Y go to next entry  
 1F2A 10AC E9 0000 CPHY DSIZ2,S finished?  
 1F2F 25 E3 BLD SRC2 brnch if not

1F31 1A 01 SEC file not found  
 1F33 20 04 BRA RET return

1F35 1C FE 1F35 FOUND EQU \*  
 1F37 1F 21 CLC file found  
 1F39 RET TFR Y,X I-> found entry  
 EQU \*

1F39 32 E9 0002 LEAS LSS,S release local storage  
 1F3D 35 26 PULS A,B,Y restore regs  
 1F3F 39 RTS and return

\* Data area for SROM routine

1F40 LPC SET \*  
 0000 ORG 00000  
 0000 LCL SET \*  
 0000 DSIZ2 RMB 2  
 0002 LSS EQU 0+LCL len of local storage  
 1F40 ORG LPC restore PC

\* Name - PRIBLK  
 \* Function - This routine dumps the contents of BUFFER to the standard output.

All registers are preserved

1F40 34 36 1F40 PRIBLK EQU \*  
 PSHS A,B,X,Y save regs

```

1F42 FC 08B1      LOD BLKCNT      get sector counter
1F45 10B3 0008    CMPI #0008      in last blk yet?
1F49 22 34        BNE PB6        brnch if not

1F4B 9E 08BA      LDX #BUFFER    pt to beginning of buf
1F4E PB7          EQU #          *
1F4E 7D 087E      TST #00001G    is this a 5" disk
1F51 27 06        BEQ FB13       brnch if so
1F58 30 89 0080    LEAX SS1Z8,X  this is an 8"
1F57 20 04        BRA PB14
1F59 PB13         EQU #          *
1F59 30 89 0100    LEAX SS1Z5,X  incr by 5" size
1F5D PB14         EQU #          *
1F5D 7A          DECDB          *
1F5E 26 EE        BNE PB7

```

\* The block reached is the EOF block.  
 \* Scan from the end of this block, back  
 \* until a non-EOF character is reached

```

1F60 B6 087C      LDA OPNFLAG    set open flag
1F63 E6 1F        EQU #          *
1F63 C1 E5        LDB -1,X        set a char
1F67 27 00        CMPI #01REND  got an EOF char?
1F67 27 00        BEQ PB11       brnch if so

1F69 B1 C0        CMA #00PNTX    is this a text file?
1F68 27 05        BEQ PB10       then also check another EOF char

1F60 7D 087C      TST OPNFLAG    maybe we have no o/p file?
1F70 26 08        BNE PB12       set EOF addr if so

1F72 C1 1A        EQU #          *
1F72 C1 1A        CMPI #EOF      check if this EOF char
1F74 26 04        BNE PB12       set EOF addr if not

```

```

1F76 30 1F        EQU #          *
1F76 30 1F        LEAX -1,X      go back a character
1F78 20 E9        BRA PB9        and continue checking for EOF

```

```

1F7A PB12         EQU #          *
1F7A BF 08B3      STX EOBUFF      set EOF address here
1F70 20 11        BRA PB8        and leave

```

```

1F7F 33 0008      EQU #          *
1F82 FD 08B1      SUBD #8        reduce sector counter
1F85 FC 087F      STD BLKCNT      and save back
1F88 8E 08BA      LDD BUFSIZ    set end of buf addr to default
1F88 8E 08BA      LDX #BUFFER
1F88 30 88        LEAX D,X
1F80 BF 08B3      STX EOBUFF

```

```

1F90 PB8         EQU #          *
1F90 8E 08BA      LDX #BUFFER    point to buffer area
1F93 5F          CLRB          start column counter at 0

```

```

1F94 8C 08B3      EQU #          *
1F97 27 46        CMPI EOBUFF    end of buffer yet?
1F99 A6 80        BEQ PB3        brnch if so
1F99 A6 80        LDA 0,X        get a char

```

```

1F9B 34 04        PSMS B        save col ctr
1F9D F6 087C      LDB OPNFLAG    get open flag
1FA0 C1 A0        CMPI #00PNTX  file open and binary?
1FA2 35 04        PULS B        restore ctr
1FA4 27 33        BEQ PB4        brnch if so

```

```

1FA6 B1 0A        CMA #01F      line feed?
1FA8 27 EA        BEQ PB1        skip if so

```

```

1FAA B1 00        CMA #00F      a carriage return?
1FAC 26 00        BNE PB2        brnch if not

```

```

1FAE 5F          CLRB          clear column counter
1FAF 7D 087C      TST OPNFLAG    writing to a file?
1FB2 26 25        BNE PB4        brnch if so
1FB4 8D C024      JSR POUTF      print a CRLF
1FB7 20 04        BRA PB1

```

1FB9 PB2 EQU # \*

```

1FB9 B1 09        CMA #01ABOON a tab char?
1FB8 26 1C        BNE FB4        non, then output char as is

```

\* Encountered a tab character. If outputting  
 \* to a file, the tab must be converted to the  
 \* proper spaces, depending on the current  
 \* column.

```

1FB8 34 04        PSMS B        save current column ctr
1FBF C4 07        ANDB #000000111 B = mod(curcol,B)
1FC1 86 08        LDB 08
1FC3 34 04        PSMS B
1FC5 A0 E0        SUBA D,S+    A = B - mod(curcol,B)
1FC7 34 02        PSMS A        save this value too
1FC9 1F 89        TFR A,B      B = 0spaces to insert

```

```

1FCB B6 20        LDA #SP
1FCD B0 C018      EQU #          *
1FCD B0 C018      JSR PUTCHR    output a space
1FD0 5A          DECDB          until all done
1FD1 26 FA        BNE PB5

```

```

1FD3 35 04        PULS B        get back 4 spaces
1FD5 EB E0        ADDB D,S+    add to cur column ctr
1FD7 20 88        BRA PB1        and continue

```

```

1FD9 B0 C018      EQU #          *
1FDC 5C          JSR INCB      output a char as is
1FDD 20 85        BRA PB1        incr col ctr
1FDF 35 36        EQU #          *
1FE1 39          PULS A,B,X,Y  restore regs
1FE1 39          RTS          and return

```

\* Name - GETBLK  
 \* Function - This routine takes in a CP/M block  
 \* number in ACC A, and reads that block  
 \* into the buffer pointed to by X.  
 \* A CP/M block has SECBLK  
 \* sectors/block, each sector having SECSIZ  
 \* bytes of information.

\* On exit X points 1 byte past the  
 \* end of the buffer. All other  
 \* registers are preserved.

```

1FE2 34 26        EQU #          *
1FE4 32 E9 FFFD    LEAS -LS6,S    save registers
1FE4 32 E9 FFFD    LEAS -LS6,S    alloc local storage

```

```

1FB8 1F 12        TFR X,Y      copy buf ptr to Y
1FEA 1F 89        TFR A,B      put blk# in B
1FBC 4F          CLAR A=0      A=0 means read the block
1FED 1F 01        YFR D,X      X=block to read
1FEF 8D 38        BSR MAPPER    read the block

```

```

1FF1 C6 08        LDB #SECBLK  #sector per block
1FF3 E7 E9 0000    STB TMP1,S    save it

```

```

1FF7 1F 10        EQU #          *
1FF9 ED E9 0001    TFR X,D      save trk/sec value
1FFD 8E C840      STD TMP3,S
2000 17 D09E      LDX #SYSFCB  point to an FCB
2003 1026 FC93    LBSR READSS    read a sector
2003 1026 FC93    LBNE ERROR2  brnch if error

```

\* Transfer bytes just read, to BUFFER

```

2007 30 88 40      LEAX FCBSEI  "from" field
200A FC 08B5      LDD SECSIZ    length to move
200D 17 009B      LBSR MVC      perform move
2010 31 A0        LEAY D,Y      incr buf addr

```

TO BE CONTINUED

# MODEM 68

\*\*\*\*\* MODEM68 \*\*\*\*\*

## DISTRIBUTION RIGHTS:

I allow unrestricted distribution of this software on a non-profit basis. However any commercial distribution or application using either the programs or documentation requires my written permission in advance.

MODEM68 provides for file transfer between two computers using the FLEX operating system, or between a FLEX based machine and a CP/M computer. FLEX utility commands are supported from within MODEM68. The program uses Ward Christensen's protocol for file transfers with Chuck Forsberg's YAM batch file transfer mode.

The Christensen protocol is widely used in micro-computer applications and has been well documented in Byte and other journals. The only difference for a header block under YAM and a normal Christensen block is that the header has block number 0. The filename is sent as it appears on the screen in CP/M format and may or may not have a period and extension. The filename is terminated by a null. On completion of file transfer(s) a null file e is sent to terminate the session. For FLEX users, the standard FLEX conventions apply - e.g. .ASM will transfer any files with a .ASM extension, however due to differences in filename conventions some CP/M filename characters may be translated into an alpha character for FLEX (lowercase z in this version). Also, any CP/M files received without an extension will default to .TXT.

Files of any size can be handled, as there is no requirement for them to fit into memory. The I/O buffer has been restricted to the Christensen protocol block size of 128 bytes for ease of implementation. In practice this does not significantly increase file transfer times, although it may slow things down in terminal mode with data logging.

## USAGE:

The program is menu driven and prompts for all input when required. To return to the main menu, (aborting a file transfer) type a @E (control E) from the keyboard. All FLEX utilities obey the same conventions they use normally - e.g. COPY 0 1 will copy all files from drive 0 to drive 1.

Switches have been included to provide for batch file transmission, a means of logging all screen display to disk using XON/XOFF protocol and also for echoplex operation. These switches can be viewed by use of the menu item Z, and may be altered if desired. A carriage return entered in response to the prompt will return to the main menu without displaying the remainder of the switch settings. An additional switch to allow for cyclic redundancy checking instead of the normal checksum has been included in the code but not implemented in the current version. This switch must NOT be set.

## DESCRIPTION:

The entire program is written in M6800 assembler code to allow for assembly on FLEX2 and FLEX9 computers. Cyclic redundancy file checking has not been implemented in this version, but hooks have been provided in the code to facilitate its inclusion.

## MODEM68.TXT

This is a header file which calls in a number of library files - MOD68-1 to MOD68-11.

## MOD68-1.TXT

This file contains all equates used. It also contains some buffer space on page 0 for common variables. The soft switches are all defaulted to the OFF state - change these values to \$ff if you want them ON.

## MOD68-2.TXT

This is the mainline routine. It prints an initial signon message followed by a menu. All other modes (except return to FLEX) are called as subroutines and return to the menu on completion or after entry of EX from the keyboard. Responses to the menu prompts are independent of case.

## MOD68-3.TXT

All subroutines used by more than one module are contained in this file. There is also an initialisation routine for a 6850 ACIA used as a modem port.

## MOD68-4.TXT

Resets the stack pointer and returns to FLEX. This routine also calls the file management system to close any files left open.

## MOD68-5.TXT

This file contains all file reception stuff. It firstly checks to see what mode is used (normal or batch) and then uses the appropriate routine. Normal mode is fairly straightforward, it prompts for a filename, opens the file and then waits until the line is free to send an initial NAK. Batch mode is mostly concerned with the differences between FLEX and CP/M conventions. Once it gets that all sorted out it calls the normal receive routines until all transfers are completed. You may wish to alter the default compatibility character 'z', if so you will need to patch subroutines first and next.

## MOD68-6.TXT

This routine handles terminal communications mode. If in log mode, you will be prompted for a filename to which all screen data will be logged until you return to the main menu. This mode uses XON/XOFF protocol and is useful for capturing data from bulletin boards etc. When the buffer is nearly full, the system sends an XOFF to halt output from the remote computer. It then waits for a bit to see if anything else arrives before it writes the buffer out to disk. After completion of the write, it sends an XON to restart output from the remote computer. At present, the number of characters positions left in the buffer before an XOFF is sent is 5. This seems to work OK with an RCPM running at 300bps but may need adjustment for other systems or speeds. If this is the case, change bufend-5 in build.

If the ECHO switch is set, all input from the keyboard is displayed on screen after being sent to line, and anything received from line is returned. Without echo, only characters received from line are displayed. For dialogue between two systems using MODEM68, one must be in echo mode. Also when communicating with a CP/M computer running MODEM or YAM, the CP/M machine must be in CHAT mode or the FLEX machine must be in echo mode.

## MOD68-7.TXT

This file contains the file transmission bits. Similarly to receipt, the mode is checked first. Much of MOD68-5 (receive file) and MOD68-7 (transmit file) are concerned with the differences between FLEX and CP/M conventions. FLEX allows alpha characters only as the first character in a filename and an extension. It also requires an extension, so the additional characters allowed by CP/M are forced to an alpha character when in batch mode. No attempt has been made to implement file transmission times because FLEX uses space compression, so the amount of data stored is less than that transmitted. The batch send routine uses FLEX filename matching conventions. The code is based on the TSC pdel utility, and performs similarly for name matching, however the filename is accepted from within the program instead of appearing on the command line.

## MOD68-8.TXT

This routine allows any utility running in the utility command space within FLEX to be run. It issues the normal FLEX prompt when called, and appears to the user as if he is in FLEX. Any program that runs in low memory must not be executed with this routine - e.g. EDIT. When the utility has completed, it will return to the MODEM68 main menu.

## MOD68-9.TXT

This routine can be used to abort file transfers from a remote computer.

# MOD68-10.TXT

This is a routine to display and/or alter switches. Any additional switches should appear in MOD68-1 before endsw and should have a five letter name (including spaces) included in swmsgf in MOD68-11.

## MOD68-11

This file contains the command table, all strings and messages and file control blocks and buffers

## ADAPTING TO YOUR SYSTEM:

If using a FLEX2 system with 6850 ACIAs for keyboard and modem ports, the only changes required are to patch the addresses of your modem into aciac and keyboard into kbd in MOD68-1. For FLEX9 users, the base equate in MOD68-1 will also need to be changed.

If you are not using 6850s you will need to supply the appropriate initialisation, status and read and write routines in MOD68-3.

## IN CASE OF TROUBLE:

I welcome any suggestions or bug reports. Also any enhancements (such as CRC checking) are encouraged.

John Moorfoot  
2 Yarrayne St  
Leopold  
Victoria 3221  
Australia

N.B. FLEX is a trademark of Technical Systems Consultants. CP/M is a trademark of Digital Research.

```

*****
MOD68-10
*****
by John Moorfoot
2 Yarrayne Street
Leopold
Victoria
Australia 3221

Version 1.0 31 August 1983

Uses Christensen protocol for file transfer
allows for interchange of text and binary
files between FLE1 & CP/M based systems.

The program is menu driven and provides for
a full 8 bit bathos file transfers as well as
a dialog mode (with capture to file). File
operations can be executed from within the
program. Files accumulated are not limited by
memory size. There are switches provided to
enable batch mode transfers (obey's Chuck
Forsberg's VAR conventions); log screen dialog
to disk files and also to enable local echo -
required on one machine only in dialog mode.

Entering "E" (control E) at any time from the
keyboard exits the current mode and returns to
the main menu.

FLE1 is a trademark of Technical Systems
Consultants.
CP/M is a trademark of Digital Research.
*****

```

## EQUATES

```

base equ $0000 FLE12
base equ $0000 FLE19
end equ base+$0002
wordr equ base+$000C
lstrm equ base+$0011
bufptr equ base+$0014
hwrn equ base+$0015
getch equ base+$0015
putchr equ base+$0016
inbuf equ base+$0016
ptrng equ base+$0021
class equ base+$0021
pcrfl equ base+$0024
ntrch equ base+$0027
getfil equ base+$002d
stext equ base+$0033
outdec equ base+$003c
rptr equ base+$003f
dcmnd equ base+$0040
incls equ base+$0043
fsc equ base+$0046

aci ac equ $0000 modem port
aci ac equ $0001 keyboard acia status reg
abd equ $0000

nall equ 0
nall equ $15
nall equ $18
nall equ $1a
nall equ $1c
nall equ $1e
nall equ $1f
nall equ $1f

```

```

0005 cll equ $05 returns to menu
0010 cll equ $10 end of file error
0015 cr equ $15
000C if equ $0c cursor home & screen clear character

```

```

0000 org 0
Page 0 storage used for variables

0000 bufptr rdb 2 pointer to character position in buffer
0002 filend rdb 1 flag set when end of file found
0003 clusea rdb 1 checksum
0004 error equ 1 error counter
0005 stemp rdb 2 temporary register stores
0006 stemp2 rdb 2
0007 stemp3 rdb 2
0008 statst rdb 2 stack pointer store
0009 stloc rdb 2 pointer to starting string location
0010 endloc rdb 2 pointer to ending string location
0011 extemp rdb 2 extension temp buffer
0012 moduli rdb 2 pointer to end of input buffer

```

## VARIOUS FLAGS

```

0020 rcvll rdb 1
0021 filng rdb 1
0022 conff rdb 1
0023 lmg fcb 0
0024 allng fcb 0
0025 allng fcb 0
0026 allng fcb 0
0027 pthnam fcb 0
0028 qdly fcb 0

```

## SUBTCHES

```

0029 batch equ 0 set for open batch file transfers
002A batch fcb 0 display log off
002B echo fcb 0 local echo off
002C endsw equ 0 make sure we don't get to CRC until implemented
002C crc equ 0 CRC redundancy check off

```

```

0100 org 1100
0100 start bra start1
0101 vln fcb 1 version number
0102 fcc 1
0103 fcb 0
0104 fcb 0

***** PROGRAM STARTS HERE *****
0105 start1 sta statst save stack pointer
0106 lds statst initialise stack
0107 bsr exit initialise modem port
0108 lds statst print signal message & MENU
0109 jsr ptrng
0110 jsr ptrng
0111 jsr ptrng
0112 jsr ptrng
0113 jsr ptrng
0114 jsr ptrng
0115 jsr ptrng
0116 jsr ptrng
0117 jsr ptrng
0118 jsr ptrng
0119 jsr ptrng
0120 jsr ptrng
0121 jsr ptrng
0122 jsr ptrng
0123 jsr ptrng
0124 jsr ptrng
0125 jsr ptrng
0126 jsr ptrng
0127 jsr ptrng
0128 jsr ptrng
0129 jsr ptrng
0130 jsr ptrng
0131 jsr ptrng
0132 jsr ptrng
0133 jsr ptrng
0134 jsr ptrng
0135 jsr ptrng
0136 jsr ptrng
0137 jsr ptrng
0138 jsr ptrng
0139 jsr ptrng
0140 jsr ptrng
0141 jsr ptrng
0142 jsr ptrng
0143 jsr ptrng
0144 jsr ptrng
0145 jsr ptrng

```

## OFTEN USED SUBROUTINES

```

***** INITIALISE ACIA *****
initialise serial port
*****
0147 B6 03 init lds $03
0148 B7 E00A staa aciac
0149 B8 15 lds $15
0150 B9 E00A staa aciac
0151 39 rts

```

```

***** MODEM OUTPUT STATUS *****
sets carry flag when able to
accept character. 8 req destroyed
*****
0152 F6 E00A modout lds aciac get modem status
0153 57 asb
0154 39 rts

```

```

***** MODEM INPUT STATUS *****
sets carry flag when character
found. 8 req destroyed
*****
0155 F6 E00A modin lds aciac get modem status
0156 57 asb
0157 39 rts

```

```

***** KEYBOARD STATUS *****
sets carry flag when character
available from keyboard. 8 req
destroyed
*****
0158 F6 E000 kbd lds kbd get keyboard status
0159 57 asb
0160 39 rts

```

```

***** MODEM OUTPUT *****
outputs character in A reg. A12
register preserved
*****

```



\*\*\*\*\* RETURN TO SFI \*\*\*\*\*



29

```

* LOAD ERROR CODE INTO FCB & REPORT IT *
      ldr     ldrb    07h      syntax error
      stb     ldrb    1,3
      jmp     error    report error & return to menu

```

```

* CHECK FOR FLEJ LINE TERMINATION CHARACTER *
      testro  idaa  testro  check last terminator
      caga  and  carriage return?
      baa  testr2  yes - return
      caga  eol  FLEJ end of line character?
      testr2  rts

```

```

* END BATCH TRANSFER *
post   clr      boot      spl null file name
      eod      abllq      flag transfer complete
      jr       cond       send last block
      rls

```

```

GET DIR:REGDIR ENTRY
getir    lds    0dirfcb    point to directory fcb
         ldsa   0?         get information record
         staa   0,         0
         inc    0,         0

```

```

% GET FILENAME FROM LINE BUFFER %
getna0  clr  aliflg  clear all
        lds  bna00  file-name buffer
getna2  clr  0,a    clear it
        tos
        decb
        bna  getna2
        lds  bufpnt  line buffer pointer
        lds  0,a
        cofs  1,    just extent?
        bqs  getna5  res - go get it
        lds  bna00  clear lcb
        bqs  0,a
getna4  smc  bufpnt0  point to 1st char of ext
        lds  0,0
        lds  bpsl  go get it
getna5  jsr  match
        bcs  getna6  check for any more in ext
        stca 0,a
        smc
        decb
        bpl  getna5
        vcc
        rls
getna6  clic
        rls
getna7  iop  getna1  get name from line buffer

```

```
SEND FILE AFTER INITIAL NAME
send      idaa      0fff          set filesize to 0 EOF after name
          staa      friend        set pathname flag
          waaa      otheraa       donor read to clear acct
          jsr      acctin         set return count
          lslb      05
          slab      0000
          lslb      000          60 second initial timeout
send01    jsr      timeout        loop for input from receiver
          lsl      0100          anything there
          bne      count0         go get it
send2     dec      errcnt         adjust error count
          bne      timeout0
          lslb     0100
          lslb     0100
          bra      sendin
send3     jsr      acctin         do not character
```

1	and crrc test	smn crc	rooting available
2	capa	bnaz	initial MM?
3	bme	smn2	no - try again
4	clra		set block number to zero
5	lsl	tbltl	go send pathname block
6	lsl	tbltl	did receiver abort?
7	bne	smn5	
8	clr	tblnd	set up normal conditions
9	clra	tblnd	
10	smn6	tblnd	
11	lsl	tblnd	read file into buffer
12	lsl	tblnd	initial set
13	lsl	tblnd	
14	lsl	tblnd	10 second timeout list
15	smn5	tblnd	send file manually

```

=====
* MATCH DIRECTORY & INPUT NAMES *
* NOTES: routines based on ISC pool *
* FILE utility *
=====
$fil  lds  $dirich  directory &db
      ldsa  $a
      staa  $a
      jcr  fas
      bcc  $fil11  test for errors
      bra  $fil11
      inc  loop
      jcr  loop      get information record
      bcc  $fil13  test for errors
      jmp  $fil10
$fil13 lds  $dirich
      tst  $a
      bnc  $fil10  look for filename
      jmp  $fil10  return
$fil14 bcc  $fil12  get next record if deleted
      lds  $dirich+a
      tst  $fil11
      bne  $fil10
      vls  loop2
      lds  $name
      test  $fil10+a  test for filename match
$fil15 vls  loop3
      cps  loop2+5  end of filename buffer?
      bcc  $fil17
      ldsa  $a
      lms
      str  loop1
      lds  loop2
      tst

```

```

beg      $fil6      null catches anything
capa     0,         if we hit catch?
bne      $fil7      no - get next record

inc
sta      rlang2
bra      $fil5
jor      $mem2a     get filename into ich, buffer & printline
jor      $rch
ldaa     0
open     for read
staa     0,
jor      $as
beq      $fil9      test for errors
bra      $fil11
;
ldaa     0          get error type
capa     00         and at directory?
bne      $fil10     res - return
bra      $fil11
jdaab    0?
ldx      $eqq?      "opened"
sta      $filoc
ldx      $eqq0f
sta      $eqloc
jor      $eqtrq
ldx      $eqq0a     show us what's happening
jor      $eqraq
jor      $eqs       send this file
jor      $eqs       now close it
eqo      $fil2
rts      $eqs       are more files catch this name?

inc      error
no read? error

```

```
SEND MAIL:ME +      clear end of file flag
clr          +      clear pathname flag
ldaa $1          +      open for read access
sr          +      (open)
ld          +      (negl)
jmr          +      initial NAK message
sr          +      full buffer
sr          +      dummy read to clear acia
ldab $0          +      one minute timeout
sr          +      timeout
lsl          +      flag
beq          +      (cond)3
sr          +      noack
cpa          +      (nak)
ane          +      (cond)
jmr          +      if no initial nak - cancel
jmr          +      (lsl)
jmr          +      back to zero after normal transfer
jmr          +      (lsl)
```

NAME	FILE (ADDRESS)	COMMENT
flag	00000000	Flag for filename
ptr	00000001	get name() into line buffer
lptr	00000002	allow us to read buffer
bufptr	00000003	line buffer pointer
temp	00000004	initialize flags
alldev	00000005	all drives?
noget	00000006	no - get name from buffer
fd	00000007	find drive number
err	00000008	test for errors
buf	00000009	restore buffer pointer
end	00000010	set last terminator
lptr	00000011	get line buffer pointer
chr	00000012	get first character
chr	00000013	classify character
chr	00000014	alphanumeric?
chr	00000015	numeric?
chr	00000016	looping?
chr	00000017	need exit filename - back to menu
chr	00000018	working drive
chr	00000019	put it in fcb
chr	00000020	both drives if off
chr	00000021	
chr	00000022	
chr	00000023	
chr	00000024	
chr	00000025	
chr	00000026	
chr	00000027	
chr	00000028	
chr	00000029	
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chr	00000117	
chr	00000118	
chr	00000119	</



```

097A 9C          signon lcb      11      fore load
097B 20 20 20 20 20 lcc
097C 84 80          fcb      100,100      Address0 by John Roarfast'
097D 20 20 20 20 20 fcc
097E 60 0A 0A 0A 0A fcb      100,100,100,100,100      Version 1.0 Aug/83'
097F          patpat 0000
0980 20 20 43 20 20 lcc      C - Cancel file transmission or reception
0981 8A 80          fcb      100,100
0982 20 20 51 20 20 fcb      100,100      Return to list'
0983 8A 80          fcb      100,100
0984 20 20 52 20 20 fcc      0 - Receive file

```

[illegible]

being run on the computer, and the amount of non-VMA activity. Hazelwood claims no program dependency due to their even/odd address refresh. This consistent distribution of odd/even addresses appearing on the bus, in addition to the conventional non-VMA insure "hard" memory data retention.

In addition to the above, this system does not begin its read or write cycle until the bus signal  $\overline{CS}$  goes from high to low (mid-cycle), allowing maximum time for the dynamic address translator (commonly referred to as the DAT, a portion of the ROM system firmware) to develop the effective bus address. Some static designs are detracted linearly from access time margins by DAT activity.

### Set-Up

The DM-256 has several options:

Addressing for the SS50 Bus to 1MB and to 16 MB on the  
Hazelwood S64 Bus.

There are no jumpers "E" - "F":

"E" The E Jumper controls response in the XEXX range. The three (3) possible responses are:

1. Always - not normally used due to address conflicts with the I/O structure.
2. Never - used for non-extended addressing.
3. Respond to all except "F" - gives maximum available memory to extended memory systems.

"F" The F jumper controls response in the XFFF range. The three (3) possible responses are:

1. Always - not normally used because of address conflicts with the CPU card.
2. Never - see 2 above.
3. Respond to all except "F" - see 3 above.

An 8 pole switch controls board selection:

1,2 determines whether the board appears as the first, second, third or fourth 256KB board in the system. 1=LS bit 2=MS bit. ON= binary 0 - OFF= binary 1.

3-6 for addresses beyond 1 megabyte range and could only be used with the Hazelwood "HELIX" 64 pin system which can address up to 16 megaby es of memory. Presently set to OFF.

7 disables response to address line A19 and is normally ON.

8 selects contiguous or non-contiguous mode of addressing.

## Conclusion

The system comes assembled, tested and burned in. Gold bus connectors insure stable trouble-free operation. And is backed up by a one year factory warranty.

**Hazelwood Computer Systems, Inc.**  
907 East Terra  
O'Fallon, MO 63366  
314 281-1055

PRICE \$995.00 suggested factory price

The Hazelwood 256K dynamic system may be ordered direct from the factory or CALL DATA-COMP 1-800-338-6800 for delivery and pricing information.

— — —

**Editor's Note:** Coming soon will be reviews of additional Hazelwood and HELIX™ systems and components. Also we hope to have soon a review of the new SWTPC desktop computer, as well as other computers and systems. All because you our readers have asked us to. In addition will be some 68000 systems, some with software you know and some you don't. Fact is as this is being written.

## HAZELWOOD OM-256 MEMORY SYSTEM

About this time a year ago we published a review of the Hazelwood DM-64 Memory System. It was a good review, the memory board is a good system. It was just that simple.

For the past couple of years I have used the DM-64 in my personal computer and it has never dropped a bit, or even hinted a cough. Even at system speeds in the range of 2 mhz it works. Which should dispel the old assumption that dynamic memory has some sort of problem.

In the early, early days some dynamic memory did exhibit quirks, so did some static. However, most of the problems, in the beginning was due to poor engineering practices on the part of hardware design types. They didn't know how to interface to dynamic devices. Granted it is simpler to design to a static system rather than a dynamic, but once done properly there should be little of any difference in thru-put of the host system. Hazelwood's Mike Smith has done his homework well, he knows how to design to dynamic standards. I'lls (Hazelwood) memory systems work.

### DM-256KB Memory Board description

As the name implies the system is a 256 kilobyte dynamic memory system, engineered for one board. Operating system speeds up to 2.5 mhz are guaranteed with NO clock stretching or cycle stealing. As pioneered on the Hazelwood 64KB board this 256KB system also utilizes "High Speed Transparent Refresh" a technique developed by Hazelwood.

As delivered the system can be utilized on either the Hazelwood 64 pin backplane or the Standard 550 Bus backplane equally well. The only system that may present some problem for this memory system is the GIMIX II<sup>®</sup> system, due to hardware consideration of the GIMIX system. On all other computer systems we have used this memory system with, including FLEX<sup>®</sup>, OS9<sup>®</sup>, UNIFLEX<sup>®</sup> and a couple other non-standard, it worked flawlessly.

With the SS50 Bus there is 20 bit addressing, and 4 additional bits of addressing available on the SS-64 Bus.

The system may be programmed by dip-switches to respond as 4 64KB boards or as 16 16KB boards. By this arrangement total access is afforded to practically all current operating systems.

To quote from the specifications sheets for the DM-256:

"The refresh control on the DM-256 uses odd/even interleaving to optimize the charge on each memory cell."

What this means is simply this. While an even address is on the bus all of the odd cells are refreshed and while an odd address appears on the bus the even cells are refreshed. In addition all cells receive refresh burst when there is a non-VMA cycle. Simple!

The net result of this is that there is an actual "over refreshing" by common standards, but it insures that cell charges are held to a maximum value over the entire refresh cycle and to the next.

Many other dynamic memory designs refresh on the non-VMA cycle only, to our knowledge only Hazelwood dynamic memory systems enjoy the benefits of "over-refresh". The guarantee is for operation up to 2.5 mhz system speed (on the standard S550 or S564 system this would require a 10 mhz xtal). Bolled down this means that whereas most other dynamic systems using only non-VMA refresh are prone to "soft" memory errors. And depends to a large extent on the particular program

two of our staff are out of town collecting information on 68000 systems. Our new review policy began with a review of the GIMIX III system last month. These will be inhouse reviews, as opposed to readers' reviews.

We still encourage reader reviews, however, things are beginning to move in varied directions and there will be more inhouse reviews presented in the coming months. Don't be surprised at we broaden into the 68000 systems as well. And if you have anything on ANY 68000 systems, hardware or software, please let me know. Our readers are beginning to show a lot more 68000 interest since the advent of some fairly decent 68000 applications software, and nearing release of more 68000 devices for the Standard \$50 Bus. Many at very good prices. Low enough in cost to appeal to the hobbyist, remember them, they were the guys (and some gals) who got it all started. I was one of them, still am, proud of it and getting ready to get my "feet wet" with a semi-kit 68000 system. Join me, I'm gonna have some fun again!

There is one thing that I want to make very clear; we (Computer Publishing and 68 Micro Journal) will not forget or abandon our FLEX<sup>™</sup>, UnifLEX<sup>™</sup>, SSB<sup>™</sup>, OS9<sup>™</sup>, etc. readers. I have heard your complaints of non-support or total abandonment of our group, by some of the originators in our group. I can assure you that it has come back home to roost for some. Their reputations suffered. (i.e. pocketbooks and business volume also) I know of two in particular who spoke from both sides of their mouths. They didn't reckon on the power of user satisfaction, or word of mouth reputation. They kept promising to support what they had sold us while all the time devoting effort that should have been put to work getting the stuff they sold us bug-free, developing other markets, with a "to hell" attitude towards us. I know for an absolute fact it has cost them dearly!! Funny, but you would have thought that they would have suspected that someone interested in their products might just ask one of us. You would be deeply surprised at the feedback I get.

As some of you might know, I have not been too active recently. Back on November 28th last year, I underwent open-heart surgery and five bypasses. Since then I have not been in the office much, but am feeling better each day, so expect to be more active - soon! To all of you who sent flowers, cards, fruit, letters, telephone calls, etc. THANK YOU! And PLEASE keep in touch! GOD BLESS!!

DMW

- - -

## MULTI-TASKING WITHOUT INTERRUPTS

### MULTI-TASKING WITHOUT INTERRUPTS

by Graham Trott (author of PL/9)

Have you ever written a program that does two or more jobs at the same time? If you have, then you've probably run across the problem of resource allocation, going something like this:

The main program is cycling round, looking at the keyboard to see whether you want it to do anything. When you hit a key it goes into a flurry of activity while it sorts out your request. If this activity only means updating some variables or doing a few calculations, then the program will get back to the keyboard before your finger has even released the key. But suppose that you've asked it to set in motion some complex I/O function, such as ramping up the voltage on a D/A converter, which requires the program to change a value every time a timer overflows? In a single-task program, your keyboard will go dead until that task has finished. The problem is compounded if there are several D/A converters, each ramping at different rates.

OK, so it's possible to write a single-task program that can handle this sort of job. The problem then is that each of the tasks gets thoroughly bound up with each of the others, to the extent that maintaining the program becomes a real pain. What would be really nice would be to write each of the tasks separately and tell the CPU to execute all of them in parallel.

Now I know that OS-9 is out there just waiting to be used for this sort of job. OS-9 is great if the drivers already exist for the I/O device you have to handle, but the job of writing special interface drivers is just too much for simple hackers. Furthermore, you don't actually know when OS-9 is going to suspend a task and start up another, which makes interlocking variables a bit tricky. However, if you can handle OS-9 and if OS-9 can handle your application, fine.

If you're still here then you must be a FLEX hacker like me. (Either that or you're an OS-9 nut with a streak of curiosity, waiting for me to make a fool of myself. No matter.) What I'm going to describe is a simple technique for writing programs that are divided into separate tasks, all operating concurrently, with as much or as little interaction between them as you wish, and all without the use of interrupts.

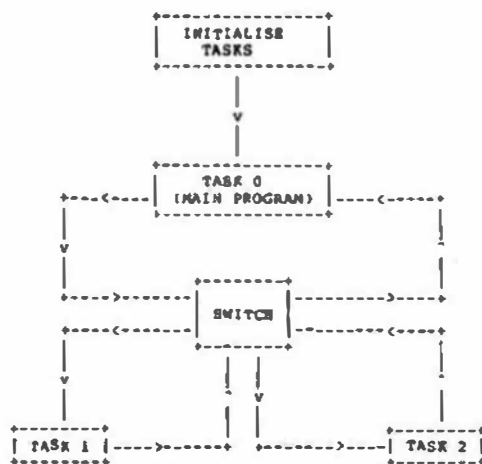
The heart of the technique is a routine called SWITCH, whose job it is to change from one task to another. Suppose that three tasks are set up; a main program and two secondary jobs called TASK1 and TASK2. Each must contain sufficient calls to SWITCH to avoid hogging the CPU. When SWITCH is called, if the main program is running then it is suspended and TASK1 is resumed; if TASK1 is running then TASK2 is resumed and if TASK2 is running then the main program is resumed. Each task can run for as long as it likes, then a call to SWITCH gives the next task some CPU time in a round-robin fashion.

The advantage of this scheme is that unlike one that uses timer interrupts there is no need to guard against one process interrupting another and interfering with a variable that is being worked with. If you want to mess with a variable you do it, only handing control over to the next task when you're good and ready.

The way the system works is quite simple. As part of your program's scratch space you allocate a stack for each of the secondary tasks. The starting address of each secondary task is placed at the very top of its reserved stack area. A two-byte pointer to this stored address is also kept for each task and one is reserved for the main program. Lastly, a byte is reserved that records which task is currently active. This byte is initially set to zero, the main program.

Having done this initialisation, the main program can now start. At some point it is necessary to start up the multi-tasking system. A good place to put a call to SWITCH is in the program loop that polls the keyboard to see if a key has been pressed. This ensures that as long as no key is operated a steady supply of calls to SWITCH will be generated.

The first call to SWITCH causes the stack pointer to be saved in the two-byte pointer reserved for the main program. It is then re-loaded with the contents of task 1's pointer and a return-from-subroutine is executed. This causes the address of task 1 to be put into the program counter, which starts it executing. At some point it too calls SWITCH which in the same way saves the current status and starts up task 2. When task 2 calls SWITCH the main program is resumed at the point it left off. This can be shown clearly by diagram:



Note the most important feature of the diagram, that once the multi-tasking has begun it goes on forever. None of the tasks can be allowed to finish or the system will stop. In practice, this restriction can be overcome by allocating flags that tell the SWITCH procedure which tasks are currently active. In this way, new tasks can be introduced when they are needed. The alternative is for an inactive task to stay in an endless loop, generating calls to SWITCH.

The following PL/9 program should illustrate the use of this technique. Firstly, the necessary scratch variables. These are declared here as GLOBAL variables but they could equally well be declared using the AT statement. Since this program will actually run, I'll put ORIGIN and STACK statements first:

```

/* Multi-Tasking Demonstration Program */
origin=$8000; stack=*; /* i.e. stack=$8000 */
constant no_of_tasks=3; /* Tasks 0, 1 and 2 */
global

byte dummy1(100); /* Length of stack for task 1 */
integer stack1;

byte dummy2(100); /* Length of stack for task 2 */
integer stack2;

integer task_table(no_of_tasks);
byte task;

```

The user's own variables can be included anywhere in this list. Note that a pointer in "task\_table" is provided for each of the tasks, including the main program. Each of the secondary tasks has a stack area (variables "dummy1" and "dummy2") and space for the return address (variables "stack1" and "stack2"). The main program's stack is the rest of the system RAM so there's no need to allocate a separate stack. The byte variable "task" records which task is currently active.

The SWITCH procedure must be near the start of the program, since it's called by other procedures (PL/9 requires that procedures and variables are declared before they are called). It looks like this:

```

Procedure switch;
  xreg=.task_table(task);
  gen $10,$0T,$84; /* STS 0,X */
  task=task+1;
  If task=no_of_tasks then task=0;
  accd=task_table(task);
  gen $1f,$03; /* TFR D,S */
endproc; /* Do a "RTS" */

```

Now comes the body of the program, including the secondary tasks. The first of these is called task 1 (but you can use any name you wish). Its job is to cause the terminal bell to ring at regular intervals. Note the call to SWITCH in the inner loop:

```

procedure task_1; integer count;
repeat
  count=1000;
  repeat
    switch;
    count=count-1;
  until count=0;
  acca=7; /* ASCII "BELL" */
  call $cd18; /* FLEX "PUTCHAR" */
forever;
endproc;

```

The second task, called task 2, sends to the terminal the complete ASCII character set from \$20 through \$70, over and over again:

```

procedure task_2; byte char;
repeat
  char=$20;
  repeat
    switch;
    acca=char;
    call $cd18;
    char=char+1;
  until char=$7e;
forever;
endproc;

```

The next routine forms part of the main program. It echoes to the printer every key that is typed. It is so designed as to generate calls to SWITCH while it is waiting for a key:

```

procedure echo_to_printer; byte char;
call $ccc0; /* Initialise printer */
repeat
  repeat
    switch;
    call $cd4e; /* FLEX "STAT" */
    until (ccr and 4)=0;
    call $cd15; /* FLEX "GETCHR" */
    char=acca;
    call $cce4; /* Output to printer */
    until char=$1b; /* Wait for <ESCAPE> */
  repeat
endproc;

```

Lastly we have the main program, which as its first job must set up the multi-tasking system:

```

procedure main;

task=0;

task_table(1)=.stack1; /* Address of stack1 ->
                        table */
stack1=.task_1; /* Address of task 1 ->
                stack1 */

task_table(2)=.stack2; /* Address of stack2 ->
                        table */
stack2=.task_2; /* Address of task 2 ->
                stack2 */

echo_to_printer;

```

All that "main" does after initialising the multi-tasking system is to call "echo\_to\_printer" to run the primary task.

If you were expecting a serious example I'm sorry to have disappointed you. A multi-tasking system of this kind is designed to cope with real-time applications where actions are taken dependent upon some outside event, and it's rather difficult to produce a realistic



example when the only outside event is the keyboard. You can satisfy yourself that the program works, however, providing you've got a copy of the PL/9 compiler. Otherwise it is a fairly simple task to translate the above into assembly language or the high-level language of your choice (but be warned, many languages will find stack manipulations such as are used here difficult to swallow).

## REPAIRING MINI-FLOPPY DISK DRIVES

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### REPAIRING MINI-FLOPPY DISK DRIVES

Usually we do not like fiddling with our disk drives, preferring to ignore that these are the most error-prone parts of our computer system. But when DIS READ ERRORS begin to appear daily, and when swapping disks written by another system becomes a pain, then there is no issue: demount your drive and recalibrate it!

When I remember the troubles I have had with floppy-drives, they can be classified as follows:

1. recalibration needed
2. mechanical troubles
3. electronic faults

This list gives the true frequency of errors creeping up: most problems boil down to a decalibration, some to a mechanical fault and very few to an electronic failure. Let me discuss in that order these problems and tell you what can be done, without being an electronics professional or holding a degree in mechanical sciences.

#### 1. Recalibration

To recalibrate a drive, you first need a calibrating diskette (for instance VERBATIM OS5-S01-CE); these are expensive but you cannot do a proper work without. The steps to follow are described in detail on the enclosure of the diskette, and you also find plenty of hints in the operation manual of your drives manufacturer. Another must is a dual-trace oscilloscope.

From personal experience I can say that often you only need to recalibrate track 16 (look at the cat-eyes pattern) and the track 0 sensor (a micro-switch or a photo-transistor). Programs helping you to position the head on one of the available tracks have been published in MICRO68; for instance I use the DISKEDIT program written by R.P. Lajeunesse (MICRO68 Sept. 1979): it's a very useful old goodie! You may have to modify it to your maximum track number, but that's no problem.

If you do not have a calibration diskette nor an oscilloscope there is a last-hope method: load a disk-inspector program such as TSC EXAMINE and try to read informations from every track, adjusting the stepper-motor by rotating it gently after having loosened the 2 or 4 screws who fix it. If you get no more read-errors for neither track, you may be somewhat confident that your drive is recalibrated to the diskette you are using. If that diskette is a copy made at a time where your drive was still ok, that may do it; if not, alas, using a calibration diskette is a must!

Not every manufacturer makes his drives with the same care as others do; MPI drives are notorious for their unreliability; if you have to sell a computer system to a non-technical minded customer, do not give him MPI drives! As a result, many customers are swapping their MPI drives to SHUG RT for instance, so that you may have the opportunity to buy very cheaply an old MPI. The most error-prones seem to be the double-headed, 40 tracks 52-models. Often only one side may be used, the second giving you plenty of formatting errors or refusing formatting at all. Do not be afraid: these drives may be cured!

Begin to control the rotational speed of the disk motor, looking at the stroboscope disk-pattern; then recalibrate track 16 and control track 0 alignment, eventually track 39.

Looking at the electronic boards you see 2 female connectors coming from the 2 heads; remove one, and try to format the disk in single-sided mode. If you get a crash at the verifying procedure, you removed the wrong head. Swapping the connectors (pay attention at their orientations!) gives you a clue that both are working.

On most drives you notice that the second head may be slightly moved independly from the first one; I found that fiddling with the skew of the second head can solve the problem (for instance: if you are unable to read a long catalog on a double-sided disk, who spreads over both sides, try the adjustments until you may read the whole catalog: now you are sure that both heads are working and may only need some minor alignments.)

Recalibrating a drive requires time and patience, but it pays off! There is nothing more rewarding than to read a "1404 sectors free" message on a drive that always gave you a "formatting aborted". It is a good idea to set up a workbench with a separate power supply for your defect drive; I included a Y-toggle switch between my controller and the normal drives, so that flipping a multiple-pole switch connects the controller to the drive in repair.

#### 2. Mechanical faults

Here are the most frequent faults I found:

\* the guiding rods of the head assembly give too much friction; in theory these rods ought not to be oiled, but in practice a droplet of paraffine-oil often will do miracles.

\* the latest MPI drive I repaired behaved in a strange manner: formatting a new diskette sometimes worked well, then at other times nothing could be done. Careful inspection showed that the principal head (that mounted on a hexagonal base) had broken loose. This head usually is glued to the carriage with some sort of two-component glue; in my case, a barely visible crack showed that the glue was broken, so that sometimes the head moved slightly from its normal position. One drop of standard two-component glue fixed the problem!

\* the most gruesome mechanical fault I found was also in a MPI model 52 drive. That factory-new drive had passed the MPI quality controls, but refused absolutely to work. The disk motor rotated well, but the stepper motor was tightly blocked.

I disassembled the whole hardware, opened the stepper-motor and found that it COULD not rotate: the diameter of the rotor was too large, so that the dents of the magnetic rotor gripped those of the stator; the whole motor-case was filled with magnetic chips! This (Japan-made) motor never could have made a single turn! How this piece of equipment passed the various inspections and quality controls remains sort of a mystery. (\*\*) At our school, we gently removed with a lathe a few fractions of a millimeter from the rotor, and now that drives works with a delicate "snurr"; it is my best-sounding and best-working drive! So do not be afraid to open a stepper-motor, even if you never have done it before. First it is instructive, and second it contains very few parts, so that disassemble and reassembling is quite easy.

#### 3. Electronic faults

As a rule of thumb you can be sure that the odds that your non-working drive has an electronic failure are 1 to 99. In 3 years I only found one fault on the electronics board: that was a 35 track SHUGART drive whose disk-motor suddenly began to rotate, even if the drives were not accessed by the controller (no motor-on). The disk-motor is operated through a current-driver transistor: it's the only big transistor on the board. Shugart uses a TIP140 transistor; replacing it cured the problem. On some MPI models, that transistor runs very hot, as it is only marginally cooled by a little piece of aluminum; so if you notice a motor who starts moving when he ought not (usually at a very low speed) first have a look at that transistor.

#### Conclusions:

From all what I wrote, you may see that repairing a drive requires some time and patience; that's the main reason for some big vendors not to repair at all a faulty drive, but to replace it by a new one and to scrap the non-working drive.

For you as a hobbyist you here have an opportunity to

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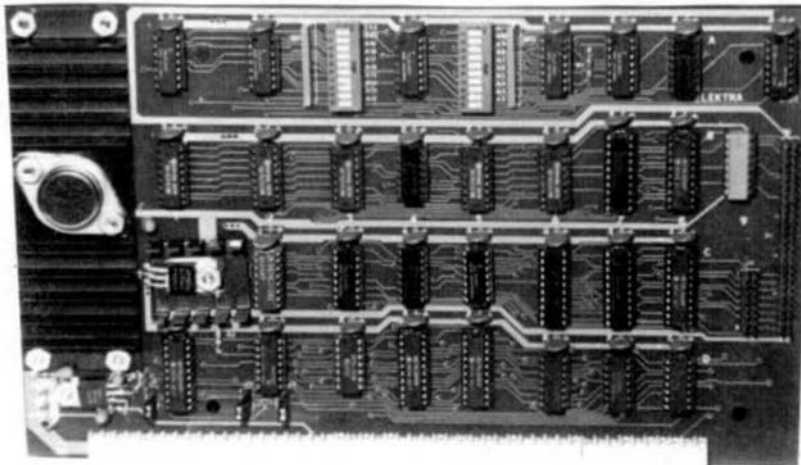
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				Add. Man.	Only	
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SWTPC DOS w/Edit & ASMB	—	—	25	150	40	550
Advanced Programmers Guide	—	—	25	—	—	—
Editor	100	250	25	50	—	—
Assembler	150	250	25	50	—	—
Debug	175	250	25	75	—	—
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Basic Precompiler	—	—	25	50	10	25
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Cobol	—	—	—	—	30	75
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RMA/68000 Cross Assembler	—	—	400.00
BASIC9* w/Run-Time	50.00	N/A	25.00
BASIC9* Tour Guide Book	—	18.95	200.00
"C" Compiler	—	25.00	250.00
C Programming Language (Kernighan & Ritchie)	—	19.95	—
CIS Cobol Compiler w/Forms 2 Prog. Gen.	50.00	N/A	40.00
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Spell'n Fix by Peter Stark 178.58 Write'n Spell by Peter Stark 75.11  
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64 X 16 Video Boards	198.71	100.00	Dual prt par, 2 cables	138.32	110.00
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We have introduced our line of computer equipment with the purpose of offering the highest quality of components possible at affordable prices. These products are intended for OEM applications where it is the responsibility of the purchaser to integrate these components with suitable memory, disk controllers, drives, and software along with I/O terminals to form working computer systems.

buy very cheaply such a drive; as you do not count in dollars the time you spend on repair, you may get some interesting insights into the inner workings of your machine, and some really good vibrations when your stone-dead drive is resuscitated!

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(\*\*) Editor's Note: I believe! - A few years back I purchased, at a computer show from a fellow cruising the Isles, a pair of these drives, brand new, sealed in the box, with data sheets and quality control(?) stickers attached. (At a real good price I must admit)

Upon arriving back at the office the following Monday I licked my chops, as I unpacked the new MPI drives, could hardly wait to double my disk storage. Which was strained as 68 Micro Journal was growing and my 'work at home' system was supplied only with single-sided 5" drives, later expanded to DS/DD 548" drives and a 20 MByte winchester. This was in the days when double sided, double density drives were not only expensive but short due to demand. Finding these two was a real bonus from the show.

After about an hour of figuring out how to program the drives for side select, etc., and getting them installed in the power supply cabinet, I open the door to drive #0, inserted the 'System' disk, typed 'U' for 5" disk and waited, and waited and waited some more. The motor buzzed and hummed, the LED function light lit up as befits a proper disk drive but nothing else. Somewhat downcast I reached up and opened the drive door - maybe the diskette wasn't seated just right, when - ZAP, BANG, FLOP, ZOOOOO!!! >, out of the door opening flew a steel rod, a couple of machined parts and assorted nuts, bolts, screws and lockwashers! Talk about surprise!

Spent nearly an hour sifting and sorting around down on the floor searching for the 'flying' drive parts. Got into all sorts of problems as I finally came up with about twice as many nuts, screws and washers, as there were empty holes in the drives. Happily the other drive had not reacted so violently, so I 'carefully' removed it also from the cabinet. This was to be my guide to where, whatever, was to be replaced. It appeared to be intact and not prone to self-destruction so with a large sigh of relief I commenced to reassemble the sick drive - but wait, none of the screws will fit in the holes properly, and what was even more upsetting was that on inspection there were in some places no holes, drilled or cast, where they were on the good drive. And to make matters worse most of the holes that did exist had no threads!

Mind you this drive was factory sealed and had 'qc' stickers pasted all over.

Spent the next couple hours drilling holes and cutting threads to proper size. Reassembled the drive per the good one, did an eyeball alignment and reinstalled the drives in the cabinet, punched 'U', watched the LED light up, listened to the motor whirr and purr - and behold the screen displayed the proper FLEX banner. IT WORKS !!

Used these drives without any faltering on their part for nearly two years. Then one day it became apparent that the original 'good' drive had slipped sync or was out of head alignment. Upon removing it from the cabinet, after a two year or so of faultless operation, I observed a shiny small brass piece dangling on the end of a fine thread of wire - yep - was the pole piece of one of the heads. Too much this time - back to the factory. Actually both drives as I discovered that the other, original bad, drive was also losing its pole piece. Seems the white or light plastic the pole pieces had been imbedded in had turned to granulated small particles, much as sugar or salt. Thus allowing the pole pieces to literally fall out, and dangle on the pole coil wire.

After about a month of waiting and having complied with their request that I call first and secure a service authorization number, I started calling every day or so, but never could get a straight answer. Finally I called on a Saturday afternoon, and got in contact with a young man in the 'service department', who informed me that he was a high school student and helped out occasionally in the service department. According to what he told me there was only one full time service man and he was 'snowed under'. After giving him my authorization number he requested that I call back in a couple of hours. "As there is a large pile of drives behind the door and I will have to go through them all to find yours." I called back a few hours later but no one answered the phone. I was beginning to wonder.

The following Monday I called and gave the nice young lady who answered the phone a quick rundown on the 'fun' I had been having with my drives. Also told her that I was filing a complaint with the FTC and any other threat I thought might have leverage. IT worked as she called me back within the hour. She informed me that my drives had been misplaced, but would be rushed to the front of the line, repaired, and returned post haste...

Well to make a rather boring story short I did receive a parcel from them a few weeks later. Inside were two drives, not my old ones, but new current models. Also I received a letter from some official explaining that my instance was an isolated one. Seems that some employee had pilfered some production line drives and sold them to some one else, who sold them to some one else, who sold them to an unsuspecting(?) individual who hawked them off at a computer show. I bought a couple. I knew others who also bought a couple and checking with them, things were running smooth - no complaints. For that reason I never did say anything about it in 68 Micro Journal. Maybe it was an isolated case but lightning sure struck twice over at my place.

---

## BIT BUCKET

**SPECTRUM**

Electronics

March 16, 1984

Computer Publishing Center  
68' Micro Journal  
5900 Cassandra Smith  
P.O. Box 849  
Nixon, TN  
USA , 37343

Dear sir(s):

I have received a sample copy of your January '84 issue and find the advertisements most enlightening. Prior to this I felt that my SMTP 6800/6809 system was old, and good for a boat anchor!

The review of CCSM Standard MUMPS by Peter Dibble compels me to brush away the cob webs and try to make use of my hardware for business applications. Where can I get a version of MUMPS that will run on my Percom LPD 400, either with MINIdos 1.4 (6800), or MPX-9 (6809)?

Will their be regular column related to MUMPS?  
If I can't obtain MUMPS then I will most likely opt for PLEX and subscribe to your informative magazine.

Your speedy reply will be most appreciated.

Sincerely,

E. Vander Ryd / mgr

EVR/wf

14 Knightswood Crescent, Brantford, Ontario, Canada, N3R 7E6



Glad to see that you have come to realize that there is a lot of life still left in the Standard S50 Bus machine you own.

You will find that there is available for your SWTPC computer practically any applications program you need.

MUMPS has caught on with quite a few, but we will not be devoting a regular column to MUMPS. We will be looking for articles on MUMPS for future publication however.

As to PERCOM support, for any language or operation, I am afraid you will find little if any remaining. PERCOM left the Standard S50 Bus some time back, and due to their small following no one stepped in to fill the void.

As to your use of FLEX<sup>™</sup>, well there is still support for FLEX, and FLEX has a wealth of applications type programs available from varied sources. See 68 MICRO JOURNAL. FLEX has gained such a wide base of support that it will survive as a viable disk system, with or without originator support. FLEX is not as well supported by TSC, in my opinion, as is some of the other systems, i.e. Microware's OS9, which is gaining in popularity as more and more users switch or new users come along. However, you would do well with FLEX, it is a proven, very popular disk system, and has a larger user base on Standard S50 Bus computers, than practically any other. On non Standard S50 Bus computers OS9 seems to be the most popular. However, in the next year or so things might change somewhat, so you really have only good choices remaining. Good luck!

OMW

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## Press Information

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512 928-6804

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512 982-6860

### Motorola Announces the Raster Memory System (RMS) Graphics Chip Set

Austin, Texas, January 26, 1984... Motorola Microprocessor Division announces a new concept for graphics/text generation and display. A 2-chip set, the Raster Memory System (RMS) is composed of the MC68487 Raster Memory Controller (RMC) fabricated in HCMOS, and the MC68486 Raster Memory Interface (RMI), implemented in Motorola Oxide-Isolated Self-Aligned Implanted Circuit (MOSAIC) bipolar technology. This low-cost, high-resolution chip set contains a myriad of features to provide the control for high-quality CRT graphics. Its capabilities are so pervasive that the graphics quality is primarily dependent on the power of the MPU, the amount of memory available, and the sophistication of the software, not by any limitations within the RMS system itself.

The RMS has been designed for personal computer, videotex, games, workstation, computer animated graphics and word processing markets.

"We feel that the RMS, with its many capabilities and low price tag, will revolutionize the market and set a new industry standard for intermediate graphics applications," states Steve Marsh, Marketing Manager for Motorola's Microcomputer Group.

The RMS operates with most popular CRT's. It will drive monitors with a 50 Hz field refresh rate used in Europe, or with a 60 Hz rate popular in the U.S. There is a choice of resolutions up to 640 pixels per line, and up to 500 lines per screen. For text reproduction, screen resolutions of either 32, 40, 64, or 80 characters per line are available in full color. For graphics interpretation, choices of 256, 320, 512, or 640 pixels per line provide the needed definition to fit a variety of applications. These choices are software selectable from internal horizontal and vertical resolution registers.

The RMS chip set has been designed to operate with Motorola's M6809 and M68000 class of microprocessors. Flexibility is inherent in the RMS due to the variety of operating modes available. The RMS is backward compatible with Motorola's popular MC6847/MC6883 existing graphics solution. However, the use of high-performance CMOS technology in the RMC permits many new enhancements. The Raster Memory System can operate in either a bit-plane mode, where each pixel of display is governed by a unique location of RAM, or in one of six character-oriented modes called list modes. If one of the list modes is selected, the user can choose from 96 alphanumeric characters and 64 MOSAIC characters stored in the RMC's internal ROM. In addition to these, up to 32K dynamically redefinable characters can be supported. All of these character types can have attributes applied to them, such as color, underline, flash, invert, double high, and/or double wide.

The RMS chip set can simultaneously display up to 32 colors from its palette of 4096 colors. Color data selected is stored in a Color Mapping RAM which is addressed at the pixel rate as the video picture is generated. This extremely color selective system permits the programmer to use adjacent pixels which are only slightly different in shade, as in flesh tones, or completely different in color, as in the reproduction of company logos.

The RMS has two screen definitions; the virtual screen and the actual (display) screen. The virtual screen is usually much larger so that the actual screen can be positioned anywhere within the virtual screen. The effect is to be able to smoothly scroll in any direction without continuously changing memory. Up to 1 megabyte of DRAM memory can be accommodated. It is possible to sync the RMS to an external source to allow overlay of RMS data onto an existing VCR or broadcast TV picture. Fixed are redefinable characters with unique attributes. Such object attributes as color, priority, shading and collision detection make them especially useful in game applications.

The RMI is a bipolar chip that provides high-speed interfaces between the external DRAM, MPU, RMC. The RMI provides red, green, blue for the MPU, DRAM, and RMC and synchronization signals to the monitor, and provides interface signals.

The price of the RMC and RMI is expected to be in the \$20 range in high volume within a year from full production. Samples will be available in the 4th quarter of 1984 and production is slated for the 1st quarter of 1985.

Contact Literature Distribution Center in Phoenix, 616 W. 24th St., Tempe, AZ 85282 for brochures, or your local Motorola Sales Office for further information.

## SUPPORT YOUR ADVERTISERS

User-entered Filenames  
Under SWTPC BASIC 3.5

By Geoffrey A. Gass  
5240 S. W. Dosch Rd.,  
Portland, Oregon 97201

Those of us who thought we'd work up some clever program in SWTPC BASIC 3.5 to use operator-supplied FLEX filenames, got a quick come-uppance. This 6800/FLEX disk BASIC by Bob Uiterwyk does not support use of a string variable in an OPEN statement. So normally, there's no way the operator of an application program can enter a name for a new data file except by escaping to the BASIC editor and changing the program itself.

However, if you're willing to live a little dangerously and in violation of some cardinal rules of programming, here's a process by which operator input can provide the filename for a data file generated during operation of a program.

We can write the program using a dummy filename, and then use PEEK and POKE statements to change that program line as required. This is called "self-modifying code", and in some shops will get you thirty lashes and a dock in pay ... so be warned.

First of all, we need a routine to find the OPEN statement. This allows the statement to be located anywhere, and allows program changes that would relocate it.

The statement is not found by looking for the letters O-P-E-N. The first verb on each line is "tokenized" in Uiterwyk's BASIC interpreters, and the token is the two-byte address in the command table where the verb and the address of its implementing routine are stored. In my version of BASIC 3.5, the token for OPEN is hexadecimal 0339, and the two bytes we are looking for are decimal 03 and 57.

Unfortunately, these tokens are subject to change when the command table is modified. To find

Gass User-entered Filenames

the token in your version, enter the statement:

```
1 OPEN #1, DDD
```

Now, examine hex location 002E to find out where line 1 is, and examine the contents of that line, which will be:

```
00 01 cc cc LL 23 31 2C 20 44 44 44
(Line #)(Token) (*) # 1 , (SP) D D D
```

\* = Line length

The token is displayed by the monitor as two hexadecimal bytes. Convert these to decimal ... or let BASIC do it for you! In direct mode, enter:

```
A=256*PEEK(46)+PEEK(47): PRINT PEEK(A+2);PEEK(A+3)
```

So now, up in the initialization section, we can put in lines 255-295 (see listing).

This routine sets up variable A8 to point to the start of the first position for a possible filename. If the line containing the OPEN statement is well into the program, the initialization

time will take 20 seconds to several minutes. To minimize the delay, make the OPEN statement a quickie subroutine near the start of the program:

```
100 GOTO 120
110 OPEN #1, FILENAME.EXT : RETURN
120 ...
```

It is critical that the dummy filename be entered just as shown here, to hold the space for the one which will be supplied by the operator.

The second part of the chore is to build a filename-checker routine, to intercept any illegal-format names. Otherwise, BASIC may butt in with a stupid "ERROR #X ON LINE Y # READY", which the operator may not be prepared to cope with.

So, we add the subroutine consisting of lines 1500-1870 (see listing). This code does not allow use of the underscore character, and requires a 3-letter extension to be explicit (no default extension). If you desire, some added code can make this routine less restrictive. As it stands, it

Gass User-entered Filenames

does guarantee that an acceptable filename is presented to FLEX.

Finally, we add the subroutine which enters the filename into the program code. This is contained in lines 1900-1990 of the listing. The routine always enters 12 characters, with leading spaces if the filename N\$ is short. Just before calling the OPEN statement, it resets A8 so it can be used again.

The routines herewith get most of the job done. An additional checking routine might be worthwhile to forestall an operator trying to open a new file before closing the old one. A perfectly foolproof system would also look to see if the disk drives were ready before allowing an OPEN operation -- but I'll leave that for another time.

```
0010 REM * USERNAME *
0020 REM * Routines to enter *
0030 REM * Operator-furnished *
0040 REM * Filenames under SWTPC *
0050 REM * BASIC 3.5 (6800/FLEX) *
0060 REM
0070 REM * By Geoffrey A. Gass *
0080 REM * Portland, Oregon 97201 *
0090 REM
```

```
0099 REM * OPEN statement kept up front *
0100 GOTO 120
0110 OPEN #1, FILENAME.EXT : RETURN
```

```
0254 REM * Finding the OPEN statement *
0255 A6 = PEEK(47) + 256*PEEK(46) : REM Start of this program
0260 A9 = PEEK(43) + 256*PEEK(42) : REM End of this program
0265 A7 = A6 : REM Point to start of line
0270 IF PEEK(A7+2) <> 03 THEN 280 : REM Looking for OPEN token
0275 IF PEEK(A7+3) = 57 THEN 295 : REM Token is hex 03 9
0280 A7 = A7 + PEEK(A7+4) : REM 5th byte is distance to next line
0285 IF A7 < A9 THEN 270 : REM Not to end yet
0290 PRINT "No 'OPEN' statement."
0295 A8 = A7+9 : REM Start of filename if correctly entered
```

```
0999 REM * START NEW DATA FILE *
1000 GOSUB 9000 : REM Clear screen
1010 GOSUB 1500 : REM Get filename
1020 GOSUB 1900 : REM Open file
1030 REM Perform other file initialization chores
1090 RETURN
```

```
1499 REM * Get Filename *
1500 GOSUB 9000 : PRINT
1510 INPUT "Name of data file",N$
1520 IF LEN(N$) > 12 THEN 1800
1530 IF LEN(N$) < 1 THEN 1510
```

```

1540 IF LEFT$(NS,1) < "A" THEN 1800
1550 N=0 : P = 0 : X = 0 : E = 0
1560 FOR I = 1 TO LEN(NS)
1570 SS = MID$(NS,I,1)
1580 IF P > 0 THEN 1630 : REM This is extension
1590 IF SS = "-" THEN 1700
1600 IF SS = "." THEN P = P + 1 : GOTO 1680
1610 IF SS < "1" THEN 1670 : REM ERROR
1620 IF SS <="9" THEN 1700 : REM OK
1630 IF SS < "A" THEN 1670 : REM ERROR
1640 IF SS <="Z" THEN 1700
1650 IF SS < "a" THEN 1700
1660 IF SS <="z" THEN 1700
1670 E = E + 1 : GOTO 1720
1680 IF N < 1 THEN E = E + 1
1685 IF P > 1 THEN E = E + 1
1690 GOTO 1720
1700 IF P > 0 THEN X = X + 1
1710 IF P = 0 THEN N = N + 1
1715 IF N > 8 THEN E = E + 1
1720 NEXT I
1725 IF E > 0 THEN 1800
1730 IF X = 3 THEN RETURN
1740 IF P = 0 THEN NS = NS + "." : P = 1
1750 IF X <> 0 THEN 1800
1760 INPUT "Filename extension",DS : NS = NS + DS
1770 FOR I = 1 TO LEN(DS) : SS = MID$(DS,I,1)
1780 GOTO 1580
1800 PRINT "A filename must be 1 to 8 characters long."
1810 PRINT "It must start with a letter."
1820 PRINT "It may contain only letters, numbers and hyphens."
1830 PRINT "A filename extension must be 3 letters long."
1840 PRINT "Please enter a filename and extension conforming"
1850 PRINT "to FLEX requirements."
1860 INPUT "OK", NS
1870 GOTO 1500

1899 REM Self-modifying BASIC code enters filename
1900 IF LEN(NS) = 12 THEN 1950
1910 FOR J = 12 TO LEN(NS)+1 STEP -1
1920 POKE( A8,32)
1930 A8 = A8 + 1
1940 NEXT J
1950 FOR J = 1 TO LEN(NS)
1960 POKE( A8,ASC(MID$(NS,J,1))) : A8 = A8 + 1
1970 NEXT J
1980 A8 = A7 + 9 : RETURN
1990 GOTO 110 : REM Open file

8999 REM * Erase Routine *
9000 PRINT CHR$(16);CHR$(22)
9010 RETURN

```

#### Getting out of XBASIC.

Have you ever wanted to be able to get out of XBASIC, and go back to FLEX under the control of the XBASIC program? I did recently in setting up an EXEC file, and couldn't find any command to do it.

The following routine should be self-explanatory. What easier way to cut off an XBASIC program than to use a CHAIN SAM?

```

5000 REM .....
5010 REM *
5020 REM *
5030 REM *
5040 REM *
5050 REM *
5060 REM *
5070 REM *
5080 REM *
5090 REM *
5100 REM *
5110 REM *
5120 REM *
5130 REM *
5140 REM *
5150 REM *
5160 REM *
5170 REM *
5180 REM *
5190 POWER HEX("C100"),HEX("7E") : REM Put files were start
5200 POWER HEX("C101"),HEX("C00") : REM address at SC100.
5210 A = POWER(HEX("C020")) - 2 : REM End of memory - 2.
5220 POWER A,HEX("C100") : REM Point to sub-routine.
5230 A = UBR(0) : REM Execute it.

```

Yours sincerely,

*Alan M. Fowler*  
Alan M. Fowler.

Whitethorn,  
3 Lowen Road,  
North Balwyn, Vic. 3104.  
AUSTRALIA.  
Telephone -613-857-7118 (Home)  
30th January, 1984.

#### MATERIALISM

POSSESS is a program that allows one to build up an inventory of one's personal belongings for insurance purposes, etc. The entries are made in DATA statements starting at LINE 1690 and include the name of the item, the manufacturer of the item, the serial number of the item, the quantity of the item, and the dollar value of the item. The entire file, or items made by a particular manufacturer, or the item type, or items having a value greater than an entered value can be listed either to the CRT or to a printer. Items that have a serial number and presumably have a large value should be entered as single items. The dollar value of a lot of items can be assigned as the total value or the value per item. This is left up to the user but consistency of assignment is suggested.

Jeffrey M. Craig  
Apt. 912 - 3001 S. King Dr  
Chicago, IL 60616

```

10 LET N=0
20 DIM I(3);DIM J(3);DIM M(3);DIM N(3)
30 PRINT "THIS PROGRAM WAS WRITTEN BY JEFFREY M. CRAIG"
40 PRINT "JANUARY 26, 1980"
50 PRINT "PRESENT ADDRESS: APT. 912 - 3001 S. KING DR."
60 PRINT "CHICAGO, IL 60616"
70 PRINT "INVENTORY OF PERSONAL BELONGINGS"
80 PRINT "AS OF MARCH 14, 1982"
90 PRINT "THIS PROGRAM IDENTIFIES THE OBJECT BY ITEM,"
100 PRINT "MANUFACTURER, SERIAL NUMBER, QUANTITY, AND VALUE"
110 PRINT "IF YOU WANT HARD COPY OF THE EXECUTED PROGRAM"
120 PRINT "THEN ENTER 'Y' - IF YOU WANT A CRT DISPLAY ENTER"
130 PRINT "'N' FOR NO."
140 INPUT P$
150 IF P$="Y" AND P$="N" THEN GOTO 110
160 PRINT "DO YOU WANT TO SEARCH FOR A PARTICULAR ITEM, MANUFACTURED,"
170 PRINT "OR ITEMS OF A GREATER DOLLAR VALUE THAN THAT"
180 PRINT "REQUESTED?"
190 PRINT "(IF YOU ENTER 'N' THE PROGRAM WILL BEGIN PRINTING)"
200 PRINT "THE ENTIRE LISTING OF ITEMS, MANUFACTURERS, SERIAL"
210 PRINT "NUMBERS AND VALUES FROM DATA STATEMENTS)"
220 PRINT "ENTER 'Y' FOR YES OR 'N' FOR NO."
230 INPUT A$
240 IF A$="Y" AND A$="N" THEN GOTO 160
250 IF A$="N" THEN GOTO 430
260 PRINT "DO YOU WANT TO SEARCH FOR AN ITEM?"
270 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."
280 INPUT B$
290 IF B$="Y" AND B$="N" THEN GOTO 260
300 IF B$="Y" THEN GOTO 620
310 PRINT "DO YOU WANT TO SEARCH FOR A MANUFACTURER?"
320 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."
330 INPUT C$
340 IF C$="Y" AND C$="N" THEN GOTO 310
350 IF C$="Y" THEN GOTO 930
360 PRINT "DO YOU WANT TO SEARCH FOR ITEMS OF GREATER VALUE?"
370 PRINT "THAN AN ENTERED VALUE?"
380 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."
390 INPUT D$
400 IF D$="Y" AND D$="N" THEN GOTO 360
410 IF D$="Y" THEN GOTO 1240
420 GOTO 1420
430 IF P$="Y" THEN OPEN "B.PRINT" AS O
440 N=99
450 READ I$,M$,S$,Q$,V$
460 IF I$="END" THEN GOTO 1400
470 IF P$="Y" THEN GOTO 500
480 GOSUB 1440
490 GOTO 510
500 GOSUB 1350
510 PRINT "ENTER 'CONT' TO KEEP LISTING ITEMS"
520 STOP
530 GOTO 450
540 PRINT "ALL ITEMS IN THE LISTING HAVE BEEN EXAMINED"
550 PRINT
560 PRINT "DO YOU WISH TO EXIT PROGRAM?"
570 PRINT
580 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."

```

```

590 INPUT E1
600 IF E1="Y" AND E2="N" THEN GOTO 540
610 IF E1="Y" THEN GOTO 1400
620 RESTORE
630 PRINT "ENTER NAME OF ITEM TO BE SEARCHED"
640 INPUT J0
650 IF P0="Y" THEN OPEN "0.PRINT" AS 0
660 FOR I=1690 TO 9999 STEP 50
670 READ I0,M0,S0,Q,V
680 IF I0="END" THEN GOTO 540
690 I0(1)=LEFT$(I0,11)
700 J0(1)=LEFT$(J0,11)
710 IF I0(1)<J0(1) THEN GOTO 840
720 I0(2)=LEFT$(I0,21)
730 J0(2)=LEFT$(J0,21)
740 IF I0(2)<J0(2) THEN GOTO 840
750 I0(3)=LEFT$(I0,31)
760 J0(3)=LEFT$(J0,31)
770 IF I0(3)<J0(3) THEN GOTO 840
780 IF P0="Y" THEN GOTO 810
790 GOSUB 1400
800 GOTO 820
810 GOSUB 1550
820 PRINT "ENTER 'CONT' TO CONTINUE SEARCH"
830 STOP
840 NEXT I
850 PRINT "ALL MANUFACTURERS HAVE BEEN EXAMINED"
860 PRINT
870 PRINT "DO YOU WISH TO EXIT THIS PROGRAM?"
880 PRINT
890 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."
900 INPUT P0
910 IF P0="Y" AND P1="N" THEN GOTO 870
920 IF P0="Y" THEN GOTO 1400
930 RESTORE
940 PRINT "ENTER NAME OF MANUFACTURED TO BE SEARCHED"
950 INPUT M0
960 IF P0="Y" THEN OPEN "0.PRINT" AS 0
970 FOR I=1690 TO 9999 STEP 50
980 READ I0,M0,S0,Q,V
990 IF I0="END" THEN GOTO 850
1000 M0(1)=LEFT$(M0,11)
1010 M0(1)=LEFT$(M0,11)
1020 IF M0(1)<M0(1) THEN GOTO 1150
1030 M0(2)=LEFT$(M0,21)
1040 M0(2)=LEFT$(M0,21)
1050 IF M0(2)<M0(2) THEN GOTO 1150
1060 M0(3)=LEFT$(M0,31)
1070 M0(3)=LEFT$(M0,31)
1080 IF M0(3)<M0(3) THEN GOTO 1150
1090 IF P0="Y" THEN GOTO 1120
1100 GOSUB 1400
1110 GOTO 1130
1120 GOSUB 1550
1130 PRINT "ENTER 'CONT' TO CONTINUE SEARCH"
1140 STOP
1150 NEXT I
1160 PRINT "THE ENTIRE LISTING OF ITEMS HAS BEEN EXAMINED"
1170 PRINT
1180 PRINT "DO YOU WISH TO EXIT PROGRAM?"
1190 PRINT
1200 PRINT "ENTER 'Y' FOR YES, 'N' FOR NO."
1210 INPUT P1
1220 IF P1="Y" AND P2="N" THEN GOTO 1160
1230 IF P1="Y" THEN GOTO 1400
1240 RESTORE
1250 PRINT "ENTER SELECTED VALUE IN DOLLAR AMOUNTS"
1260 PRINT "(DO NOT ENTER THE DOLLAR SIGN)"
1270 INPUT U
1280 IF P0="Y" THEN OPEN "0.PRINT" AS 0
1290 FOR I=1690 TO 9999 STEP 50
1300 READ I0,M0,S0,Q,V
1310 IF I0="END" THEN GOTO 1160
1320 IF U=U THEN GOTO 1390
1330 IF P0="Y" THEN GOTO 1360
1340 GOSUB 1400
1350 GOTO 1370
1360 GOSUB 1550
1370 PRINT "ENTER 'CONT' TO CONTINUE SEARCH"
1380 STOP

```

```

1390 NEXT I
1400 IF M0="Y" THEN GOTO 1420
1410 PRINT "ALL ITEMS IN THE LISTING HAVE BEEN EXAMINED."
1420 IF P0="Y" THEN CLOSE 0
1430 PRINT "YOU HAVE EXITED PROGRAM."
1440 PRINT "TYPE 'RUN' TO RE-ENTER PROGRAM"
1450 END
1460 PRINT 00,"
1470 IF M0="Y" THEN GOTO 1490
1480 PRINT 00,"LINES "I;" THROUGH "I+40
1490 PRINT 00,"THE ITEM IS: "I;
1500 PRINT 00,"THE MANUFACTURER IS: "M;
1510 PRINT 00,"THE SERIAL NUMBER IS: "S;
1520 PRINT 00,"THE QUANTITY OF THE ITEM IS: "Q;
1530 PRINT 00,"THE VALUE OF THE ITEM IS: $ "V;
1540 RETURN
1550 IF M0="Y" THEN GOTO 1570
1560 PRINT "LINES "I;" THROUGH "I+40
1570 PRINT "THE ITEM IS: "I;
1580 PRINT "THE MANUFACTURER IS: "M;
1590 PRINT "THE SERIAL NUMBER IS: "S;
1600 PRINT "THE QUANTITY OF THE ITEM IS: "Q;
1610 PRINT "THE VALUE OF THE ITEM IS: $ "V;
1620 PRINT
1630 RETURN
1640 REM DATA NAME OF ITEM
1650 REM DATA MANUFACTURER
1660 REM DATA SERIAL NUMBER
1670 REM DATA QUANTITY
1680 REM DATA DOLLAR VALUE EACH
1690 DATA BOICAR
1700 DATA GREENVILLE CAR COMPANY
1710 DATA 123456
1720 DATA 1
1730 DATA 10000
1740 DATA LOCOMOTIVE
1750 DATA LINA
1760 DATA 2000
1770 DATA 1
1780 DATA 100000
1790 DATA CARDOOSE
1800 DATA U.S. FOUNDRY
1810 DATA 456321
1820 DATA 1
1830 DATA 25000
1840 DATA END
1850 DATA END
1860 DATA 0
1870 DATA 0
1880 DATA 0

```



**MOTOROLA INC.**  
MOS Integrated Circuits Group

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AUSTIN, TEXAS 78721

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512 928-6804

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512 928-6801

Motorola Announces The  
MC6805K2 8-Bit Microcomputer

Austin, Texas, November 2, 1983... Motorola Microprocessor  
Division announces the latest member of its 8-bit M6805 family of low-

cost single-chip microcomputers, the MC6805K2. This unique 40-pin microcomputer is ideal for use in low-end and mid-range 8-bit systems designed for consumer, industrial and automotive applications. It contains a CPU, on-chip oscillator, 96 bytes of RAM, 2K bytes of user ROM, and 128 bytes of securable user EEPROM that has byte and bulk programming and erasure capabilities. The MC6805K2 is designed for 5-V only operation.

Its capability includes 32 bidirectional I/O lines which are divided into 4 8-bit ports. Each of the 32 I/O lines can be individually programmed as an input or output. All I/O port lines are CMOS and TTL input compatible and TTL output compatible. Additionally, Port A may be configured for CMOS output compatibility. Port B may also be used for interlacing with LEDs or darlington transistors because it is capable of sinking and sourcing large currents. Port C is electrically similar to Port A, however, it may not be configured for CMOS output compatibility. All 8 Port D terminals may be simultaneously used as open drain outputs.

Two important features of the MC6805K2 are its flexible 4-channel, 8-bit analog-to-digital (A/D) converter and its Serial Peripheral Interface System (SPI). The Serial Peripheral Interface in this part is capable of functioning in the following 7 operating modes:

- a) 1 wire - autoclocked (e.g. NRZ)
- b) 2 wire - half duplex \*
- c) 2 wire - half duplex with clock arbitration
- d) 3 wire - half duplex with slave select/busy line
- e) 3 wire - full duplex \*
- f) 3 wire - full duplex with clock arbitration
- g) 4 wire - full duplex with slave select/busy line

\* Half/full duplex terminology used here is not the same as that for standard ACIA, but corresponds to direction of instantaneous information flow.

Additionally, the MC6805K2 provides an internal bootstrap loader program which allows loading of the RAM from an external source. The K2 also has self-checking capability, which allows this device to check its RAM, (internal) ROM, timer, and I/O ports for functionality.

The MC6805K2 has two programmable 8-bit timers and one 7-bit software programmable prescaler. They can be used in various configurations:

- 2 independent 8-bit timers one of which is driven by a 7-bit prescaler.
  - An 8-bit timer driven by another 8-bit timer functioning as an 8-bit prescaler clocked by a 7-bit prescaler.
  - A 16-bit timer driven by a 7-bit prescaler.
  - A pulse width modulation digital to analog converter \*.
- (\*) requires an external low-pass filter

The basic structure of both timers is similar. Each timer has one modulus latch and one capture latch. Each timer is an 8-bit programmable down counter which can be loaded under program control. The timers have a modulus latch which memorizes the last write to the timer data register. The contents of the modulus latch are loaded to the timer in various ways as defined by the timer control register. Each timer has a capture latch which, when enabled, memorizes the contents of

the timer as a result of an external event. When disabled, it is transparent and its contents are equal to the current value of the timer. A timer overflow pulse is generated at the transition of timer to "00" state. Timer read can occur at any time "on the fly" without disturbing the countdown mechanism of the timer. Timer read does not disturb the contents of the modulus latch. At resets each timer and their modulus latch contents are set to \$FF.

Samples will be available in the second quarter of 1984, and priced at \$14.95 each in quantities of 1000.

Contact your Motorola Sales Office or local distributor for further information.

Dear Mr. Williams:

I am one of those persons who still haven't reached the limit of capability of the 6800 micro-processor. I'm not quite ready to up-grade yet.

The problem is, of course, that software gets scarcer and scarcer! Can you provide the address for any of the original 6800 users groups which might have programs available for free or for sale.

Sincerely yours,

*Jerome Korman*  
Jerome Korman

415 Barberry Ave.  
Kalamazoo, MI 49002  
February 27, 1984

A. G. WOOD,  
P. O. BOX C294,  
CLARENDON ST.,  
NSW 2000,  
AUSTRALIA.  
16/2/1984.

'68' Micro Journal,  
5900 Cassandra Smith,  
Hixson, TN 37343.

Dear Sirs,

I have just received your Feb '84 issue, and I see that there is still interest in prime number programs.

I would like to point out some errors which appeared in the tables reported in BYTE in Jan '83, relating to 6809 and 68000.

In the BYTE article I was mentioned as the source of the results for 68000 and IBM 3033 assembly language programs. Unfortunately, both of the results I contributed were incorrectly reported. Firstly, my results, like those of Richard Maurer (see Jan '83 BYTE) have not been "adjusted for 10 iterations". Secondly the figure given as being from an IBM 68000, was actually from a 6809!

The "factor of 10" error appears to have been made for most of the other results in their tables. For example, two results were given for Fortran on the 3033, the one figure is about 10 times the other. I believe that many of these errors are due to the fact that in the original "Ernstoschmidt" article (Sep '80 BYTE), it was not made 100% clear that the figures in the tables were for 10 iterations.

I wrote to both BYTE and Jim Gilbreath, pointing out these errors, but they did not bother to publish a correction.



6800 SOFTWARE  
1927 CURTIS AVENUE  
REDONDO BEACH, CA 90278  
(213) 376-0641

March 3, 1984

## NEW PRODUCT ANNOUNCEMENT

COMPILER, INTERPRETER, AND ASSEMBLER  
FOR EPSON EX-20 NOTEBOOK COMPUTER

**Announcing a rom based language system for fast interactive code development for data collection, analysis, communications, and computation for the ZPSOX BX-20 notebook computer.**

System is 10-15 times faster than BASIC.

Contains full FIG-FORTH Standard Interpreter and compiler, a complete 6800/6801/6301 assembler, and many extensions for EX-20 I/O devices. Supplied in two CMOS SR EPROMs plus a 64 page Reference Manual. May be installed in place of BASIC in the EX-20 case (leaving you about 12K of RAM working space) or in the EX-20 Expansion Unit (about 28K of RAM working space).

It provides full, detailed access to the EX-20's ROM system routines. There are families of words for control of the LCD screen graphics as well as alphanumeric, internal printer, microcassette tape, communications port, function keys, and sound generator. The assembler is designed for use to produce CODE words for even greater speed. There is a full string handling package.

For dedicated applications, a Target Compiler development system for the RX-20 or any other 6801/6301 hardware is available for 380 CP/M-80 or 68000 CP/M-68K 8" disk systems.

2PORTS/8X20 2PROM set and Manual:	\$190.00
2PORTS/8X20 Manual only:	\$20.00

Please add \$3.00 for shipping, \$22.00 Foreign. VISA and MC ok.

Talbot Microsystems, 1927 Curtis Ave., Redondo Beach, CA 90278  
(213) 376-8941.

March 1, 1986  
946 Evans Rd  
Nashville, TN 37204

Mr Larry Williams  
\*68\* Micro Journal  
5900 Cassandra Street Rd  
Kilgus, TX 77441

Dear Mr Williams:

Here is something that I think will fit in the "Bit Bucket." I needed to sort strings in BASIC without regard to case, a should equal a, and I should follow z. BASIC extended BASIC for DOS does have string compare operators, but they compare the ASCII order of characters. Because BASIC BASIC does not have a "compare" function, I needed to write my own version.

My first idea was to do it all in BASIC. The code looked a little like this:

```

1000 LET T3 = T2 + LET T73 = 0 + SET TEMPORARY SWITCHES
1010 LET L15 = LEM(A%(J5))
1020 IF L15 = 0 THEN 1080
1030 FOR K5 = 1 TO L15
1040 LET A9% = ASC(WD$(A%(J5),K5,1))
1050 IF (A9% > 96) AND (A9% < 123) THEN LET A9% = A9% - 32
1060 LET T15 = T15 + COS(A9%)
1070 NEXT K5
1080 LET L15 = LEM(A%(J5))
1090 IF L15 = 0 THEN 1150
1100 FOR K5 = 1 TO L15
1110 LET A9% = ASC(WD$(A%(J5),K5,1))
1120 IF (A9% > 96) AND (A9% < 123) THEN LET A9% = A9% - 32
1130 LET T24 = T24 + COS(A9%)
1140 NEXT K5
1150 IF T15 > T24 THEN SWP A%(J5), A%(J7)

```

This ran extremely slowly, because string functions are very time consuming.

A look at the PTR function showed a faster way. The PTR returns the address of the variable, which in the case of string variables contains another pointer to the actual string and the length of the string. In BASIC code, then could look like the following:

```
1000 LET T13 = A9(I6)
1010 LET T79 = A9(J6)
1020 LET A6 = USR(FTR(T13))
1030 LET A6 = USR(FTR(T79))
1040 IF T14 > T24 THEN GO TO 44(I5), A9(J5)
```

All that needs to be done is to set up the `UPPER` function to convert a string from lower case to upper case.

For 6800 BASIC, my USR function looked like this:

	ORG \$A100	
	LDI	\$0026
	LDR	P.I
	STR	COUNT
	LDI	\$0026
	LDR	O.IX
LOOP	LOA B	COUNT-1
	LOA A	COUNT
	STB B	#1
	SBC A	#0
	BCC	OVER
	LOA A	O.IX
	CMP	#-2
	BWT	SKIPP
	CMP	#-6
	BCC	SKIPP
SKIPP	STB A	#-2
	STA A	O.I
	DEX	
	BRA	LOOP
OVER	R73	
COUNT	R0B	2
	END	

I stored the USA function in data statements and then used a for-next loop to poke the machine language program into memory. Since I used the utility command space of P-1's, I had to be careful to avoid the EXEC command, and shuffling the USA code with a disk resident program.

6809 BASIC users should ORD their version at 8C100 and keep the string length count in the IX register.

```

      ORG      $2100
      LDR      (PC, R0)
      LDR      2(X)
      LDR      1
      LOOP    CMP      R0
      BNE      OVER
      LDR      1(X)
      LDR      -1, Y
      CMP      R0
      BEQ      SKIP
      CVA      0(X)
      SUB      SKIP
      SUB      13
      SKIP    STA      1(X)
      BFA      LOOP
      OVER    RTS

```

My 6809 code is a little hypothetical, since I'm one of those die-hards still running a 6800. Ask me instead about the MOSK machine I'm building.

Very truly yours,  
William R. Hamblen  
William R. Hamblen

Greetings,

68 Micro Journal is a fine magazine, one I can honestly say I look forward to each month. Thanks, and keep up the good work.

Computers are not my only interest (my wife wouldn't believe this). I have been an amateur radio operator for more years than I choose to remember. Recently I have been noticing a lot more ads for good quality and inexpensive interfaces for RTTY and CW in the amateur radio literature. Unfortunately, the ads are generally for VIC's, TRS's, IBM's and the like with no mention of SHARC, Blnix or some of the other names familiar to 68 Micro readers.

I would like to hear from other "Hams" on this subject. Do you know of software available by name, and/or for name? Not only RTTY and CW terminal programs, but other things useful in the ham shack.

If I get enough input, I will put together a catalog to distribute to others of like interest. CU name with 800x computers, I want to hear from you. 73!

Sincerely,

His background, WATCH

4522 N. 22 Street  
Allentown, PA. 18104

## IMPROVEMENTS? TO TSC "FILES" UTILITY

The FILES utility supplied by TSC provides a nice compact listing of all the files on a particular disk, however it does not indicate the number of free sectors remaining, or the name or number of the disk being processed. (See Example 1)

The latter is particularly important when generating hard copy listings of a number of disks, as trying to correlate "X" number of sheets of paper, each headed "FILES ON DRIVE NUMBER 1", with an equal number of disks, is a difficult task. The accompanying source listing is a version of the FILES utility which has been modified to overcome the above shortcomings. (See Example 2)

For those readers without the facilities to assemble the above source listing, an object code listing is provided. NOTE: The last pair of digits on each line is a checksum.

One other change not previously mentioned, is that the spacing between columns has been reduced by two. This allows five columns of files to fit on an ASR 33 printout, instead of the normal four. (TTYSET Width parameter must be set to the default value of 80 characters per line for this patch to work). Reversion to the original spacing can be achieved by changing the value at line 257 from 13 to 15, or the byte at memory location \$A2D8 from \$0D to \$0F.

Also, the value at line 72, (memory location \$A12C) which is the default column count, can be changed to 03 for those with PR-40 printers. This last change, together with the column spacing change above, allows a three column printout on a PR-40 or similar printer.

```

1.00=      OPT      PAG
2.00=      OPT      NOG
3.00= TTYL MODIFIED "FILES" UTILITY
4.00=      PAG
5.00==
6.00=====
7.00== MODIFIED TSC "FILES" UTILITY
8.00== FOR FLEX 2
9.00== BY TERRY WALTERS
10.00==
11.00== SHOWS VOLUME NAME AND NUMBER
12.00== OF DISK BEING PROCESSED
13.00== DATE OF CREATION OF DISK
14.00== AND NUMBER OF SECTORS FREE
15.00=====
16.00=
17.00== GLOBAL VARIABLES
18.00=
19.00=EQU      EQU      $AC02
20.00=WIDTH    EQU      $AC04
21.00=FCB      EQU      $A840
22.00=WASN     EQU      $AC0C
23.00=ASN      EQU      WASN
24.00=LSTTRM   EQU      $AC11
25.00=BUFPNT   EQU      $AC14
26.00=INDEX    EQU      $AC3D
27.00=DATFNT   EQU      $AC41
28.00=SPC      EQU      $20
29.00=FMSCLS   EQU      $B403
30.00=FMS      EQU      $B406
31.00=WARMS     EQU      $AD03
32.00=GETCHR   EQU      $AD15
33.00=PUTCHR   EQU      $AD18
34.00=PSTRNG   EQU      $AD1E
35.00=CLASS    EQU      $AD21
36.00=PCRLF    EQU      $AD24
37.00=NITCH    EQU      $AD27
38.00=GETFIL   EQU      $AD2D
39.00=OUTDEC   EQU      $AD39
40.00=OUTHEX   EQU      $AD3C
41.00=RPTERR   EQU      $AD3F
42.00=GETHEX   EQU      $AD42
43.00=INDEC    EQU      $AD48
44.00=
45.00=      ORG      $A100
46.00=FIL      BRA      FIL1
47.00=VN       FCB      2
48.00=NAME     FCB      0,0,0,0
49.00=         FCB      0,0,0,0
50.00=EXT      FCB      0,0,0
51.00=ALLFLG   FCB      0
52.00=ALLDR    FCB      0
53.00=LOOP     FCB      0
54.00=POINT    FCB      0
55.00=COUNT   FCB      0
56.00=COL      FCB      0
57.00=NPL      FCB      0
58.00=DEC1     FCB      0
59.00=TEMP     FCB      0
60.00=
61.00== PROGRAM STARTS HERE
62.00=
63.00=FIL1     LDX      BUFPNT    POINT TO BUFFER
64.00=         STX      POINT     SAVE POS
65.00=         LDA A   WIDTH     GET WIDTH
66.00=         BEQ      FIL12    SET?
67.00=         ASRA         DIVIDE BY 16
68.00=         ASRA
69.00=         ASRA
70.00=         ASRA
71.00=         BRA      FIL15
72.00=FIL12    LDA A   #5        SET DEFAULT
73.00=FIL15    STA A   NPL       SET NUM PER LINE
74.00=         JSR      PCRLF    PRINT CR & LF
75.00=FIL2     TST      ALLDR    ALL DRIVES?
76.00=         BEQ      FIL22
77.00=FIL21    LDX      #FCB     POINT TO FCB
78.00=         LDA A   #20      FIND DRIVE
79.00=         STA A   0,X
80.00=         JSR      FMS      CALL FMS
81.00=         BNE      FIL27    ERROR?
82.00=         LDX      PDINT    RESTORE POINTER
83.00=         STX      BUFPNT
84.00=         LDA A   0,X
85.00=         STA A   LSTTRM   GET CHARACTER
86.00=         BRA      FIL4     SET TERM
87.00=FIL22    LDX      POINT    GET POINTER
88.00=         LDA A   0,X
89.00=         CMP A   #'.      GET CHARACTER
90.00=         BEQ      FIL25    IS IT PERIOD?
91.00=         JSR      CLASS    CLASSIFY IT
92.00=         BCB      FIL25    TERM?
93.00=         CMP A   #'9      IS IT NUMBER?
94.00=         BLB      FIL3

```

95.00=FIL25	TST	LOOP	LOOPING?	188.00=ERR	JSR	RPTERR	REPORT ERROR
96.00=	BEQ	FIL29		189.00=ERR2	JMP	WARMS	RETURN TO FLEX
97.00=FIL27	JMP	WARMS	RETURN TO FLEX	190.00=			
98.00=FIL29	LDA	#FCB	POINT TO FCB	191.00=	TEST FOR TERMINATOR		
99.00=	LDA	A	ASN	192.00=			
100.00=	STA	A	3,X	193.00=TSTTRM	LDA	A	I,STTRM
101.00=	BPL	FIL4	GET DRIVE	194.00=	CMP	A	#10
102.00=	STA	A	ALLDR	195.00=	BEQ	TSTR2	CHECK LAST TERM
103.00=	BRA	FIL21	SET ALL DRIVES	196.00=	CMP	A	EOL
104.00=FIL3	LDA	POINT	RESTORE POINTER	197.00=TSTR2	RTS		IS IT EOL?
105.00=	STX	BUFPT		198.00=			
106.00=	JSR	NXTCH	GET NEXT CHAR	199.00=	PRINT	DIRECTORY LIST	
107.00=	AND	A	MASK NUMBER	200.00=			
108.00=	LDA	#FCB	POINT TO FCB	201.00=PRDIR	CLR	COL	
109.00=	STA	A	3,X	202.00=	LDA	#FCB	POINT TO FCB
110.00=	JSR	NXTCH	SAVE IN DRIVE	203.00=	LDA	A	#6
111.00=	BCS	FIL35	GET NEXT CHAR	204.00=	STA	A	0,X
112.00=	JMP	SERR	TERM?	205.00=	JSR	FMS	
113.00=FIL35	LDA	BUFPT	IF NOT, ERROR	206.00=	BNE	ERR	ERRDRS?
114.00=	STX	POINT	SET POINTER	207.00=	INC	LOOP	SET LOOP FLAG
115.00=FIL4	LDA	#FCB	POINT TO FCB	208.00=PRDIR2	JSR	PCRLF	PRINT CR & LF
116.00=	LDA	A	OPEN SYSTEM REC	209.00=PRDIR3	JSR	GETIR	GET INFO RECORD
117.00=	STA	A		210.00=	BEQ	PRDI32	ERRORS?
118.00=	JSR	FMS		211.00=	JMP	PRDIR6	
119.00=	BNE	FIL43	ERROR?	212.00=PRDI32	LDA	#FCB	POINT TO FCB
120.00=	JSR	GETIR	GET FIRST RECORD	213.00=	TST	4,X	NAME PRESENT?
121.00=	BNE	FIL43	ERROR?	214.00=	BNE	PRDI34	
122.00=	LDA	#FILST	POINT TO STRING	215.00=	TST	COL	CHECK COLUMN
123.00=	JSR	PSTRNG	PRINT IT	216.00=	BEQ	PRDI33	
124.00=	LDA	#FCB+4	POINT TO NAME	217.00=	JSR	PCRLF	PRINT CR & LF
125.00=	LDA	B	SET COUNT	218.00=PRDI33	JMP	PRDIR7	
126.00=	JSR	PDATA	PRINT NAME	219.00=PRDI34	BMI	PRDIR3	DELETED NAME?
127.00=	LDA	#NMST	POINT TO STRING	220.00=	LDA	A	15,X
128.00=	LDA	B	SET COUNT	221.00=	AND	A	#10
129.00=	JSR	PDATA	PRINT IT	222.00=	BNE	PRDIR3	
130.00=	LDA	#FCB+15	POINT TO NUMBER	223.00=	LDA	#FCB+4	POINT TO NAME
131.00=	JSR	OUTDEC	PRINT IT	224.00=	TST	ALLFLG	ALL FILES?
132.00=	LDA	#VST	POINT TO STRING	225.00=	BNE	PRDIR4	
133.00=	LDA	B	SET COUNT	226.00=	STX	DATPNT	SAVE POINTER
134.00=	JSR	PDATA	PRINT IT	227.00=	LDA	#NAME	TRY TO MATCH NAME
135.00=	LDA	#FCB+2	POINT TO DRIVE	228.00=	STX	INDEX	
136.00=	CLR	0,X		229.00=PRDI35	LDA	INDEX	
137.00=	CLR	B		230.00=	CPX	#EXT+3	
138.00=	JSR	OUTDEC	PRINT IT	231.00=	BEQ	PRDIR4	
139.00=	LDA	#FRST	POINT TO STRING	232.00=	LDA	A	0,X
140.00=	JSR	PSTRNG	PRINT IT	233.00=	INX		
141.00=	CLR	B		234.00=	STX	INDEX	
142.00=	LDA	#FCB+21		235.00=	LDA	DATPNT	
143.00=	JSR	OUTDEC	PRINT FREE SECTORS	236.00=	TST	A	CHAR NULL?
144.00=	LDA	#CSTR	POINT TO STRING	237.00=	BEQ	PRDI37	
145.00=	LDA	B		238.00=	CMP	A	DO COMPARISON
146.00=	JSR	PDATA	PRINT IT	239.00=	BNE	PRDIR3	
147.00=	CLR	B		240.00=PRDI37	INX		MOVE TO NEXT
148.00=	LDA	#FCB+23	POINT TO DATE	241.00=	STX	DATPNT	
149.00=	JSR	OUTDAT	PRINT IT	242.00=	BRA	PRDI35	
150.00=	JSR	PCRLF	PRINT CR & LF	243.00=PRDIR4	LDA	#FCB+4	POINT TO NAME
151.00=FIL42	JSR	TSTTRM	TEST TERMINATOR	244.00=	CLR	COUNT	
152.00=	BNE	FIL45		245.00=	LDA	B	#8
153.00=	CLR	ALLFLG	CLEAR FLAG	246.00=	JSR	PRNAM	SET COUNTER
154.00=	BRA	FIL6		247.00=	LDA	A	#1
155.00=FIL43	JMP	ERR	REPORT ERROR	248.00=	JSR	PUTCHR	PRINT NAME
156.00=FIL45	LDA	BUFPT	RESET POINTER	249.00=	LDA	B	#3
157.00=	LDA	A	GET NEXT CHAR	250.00=	JSR	PRNAM	SET COUNT
158.00=	CMP	A	IS IT PERIOD?	251.00=	INC	COL	PRINT EXT
159.00=	BEQ	FIL5		252.00=	LDA	A	BUMP COLUMN COUNT
160.00=	JSR	CLASS	CLASSIFY IT	253.00=	CMP	A	COL
161.00=	BCS	SERR	TERM?	254.00=	BNE	PRDI45	GET COUNT
162.00=	CMP	A	IS IT NUMBER?	255.00=	CLR	COL	MAX YET?
163.00=	BMI	FIL5		256.00=	JMP	PRDIR2	CLEAR COUNT
164.00=FIL47	JSR	NXTCH	GET NEXT	257.00=PRDI45	LDA	B	REPEAT
165.00=	BCS	FIL47	TERM?	258.00=	SUB	B	FILL SPACES
166.00=	BRA	FIL42		259.00=PRDIR5	LDA	A	SETUP SPACE
167.00=FIL5	JSR	GETNAM	INPUT NAME	260.00=	JSR	PUTCHR	OUTPUT IT
168.00=	BCS	SERR	ERROR?	261.00=	DEC	B	DEC THE COUNT
169.00=FIL6	JSR	PRDIR	GO PRINT DIR	262.00=	BNE	PRDIR3	
170.00=	BSR	YSTTRM	TEST TERM	263.00=	JMP	PRDIR3	
171.00=	BNE	FIL3		264.00=PRDIR6	LDA	A	GET ERROR TYPE
172.00=	JSR	PCRLF	PRINT CR & LF	265.00=	CMP	A	IS IT EOF?
173.00=	JMP	FIL2	REPEAT	266.00=PRDI65	BEQ	PRDIR7	
174.00=				267.00=	JMP	ERR	REPORT ERROR
175.00=	EXIT	DIR UTILITY		268.00=PRDIR7	RTS		
176.00=				269.00=			
177.00=EXIT	JSR	PCRLF	PRINT CR & LF	270.00=	OUTPUT SPACE		
178.00=	JMP	WARMS	RETURN TO FLEX	271.00=			
179.00=				272.00=OUTSP	LDA	A	SETUP SPACE
180.00=	REPORT	SYNTAX ERROR		273.00=	JMP	PUTCHR	PRINT IT
181.00=				274.00=			
182.00=SERR	LDA	#FCB	POINT TO FCB	275.00=	GET	INFO RECORD	
183.00=	LDA	B	SET IN ERROR	276.00=			
184.00=	STA	B		277.00=GETIR	LDA	#FCB	POINT TO FCB
185.00=				278.00=	LDA	A	GET IR CODE
186.00=	REPORT	DISK ERROR		279.00=	STA	A	
187.00=				280.00=	JMP	FMS	CALL FMS
				281.00=			

```

376.00=MNTH      FCC      'JANFEBMARAPRMYJUN'
377.00=           FCC      'JUL AUGSEP OCT NOV DEC'
378.00=           END      FIL

```

\*FILES ON DRIVE NUMBER 1

```

COPY.CMD      DIR.CMD      COPY.V.TST    FILEZ.TST     FILEZ.CMD
A.CMD         LIFECON.TST  FR.TST       FR.CMD
RET.TST       RET.CMD      NOBACK.BIN   LETTER.CMD
CALL.TST      PAUL.CMD     PRIMS.TST    SC-SING.TST
SC-SING.TST   LETTER.TST

```

**Example 2.** Header produced by accompanying program.

FILES ON DISK - NOW 4402" IN DRIVE #1  
SECTIONS FREE = 141      CREATED: 12-SEP-81

```

COPY.CMD      DIR.CMD      COPY4.TXT      FILE1.TXT      FILE2.CMD
P.CMD         LIFECON.TAT  3LIFECON.CMD   FR.TXT         FR.CMD
GET.TXT       REF.CMD      FILELETR.TAT   NPASCAL.BIN   LETTER.DRG
CALL.TXT      PRUL.CMD      PRIMES.TAT     BC-6YN2.TAT   BC-BTN.TAT
BC-BTN1.TAT   LETTER.TAT

```

[illegible]

## continued from page 11

1000 00 01

- 
- 
- REAL VARIABLES

100C	HEIGHT	FTD	6
1012	RADIUS	RTD	6
1018	HEIGHT	FTD	6

- \* CALCULATION OF WEIGHT ASSUMES THAT HEIGHT AND
- \* RADIUS OF CYLINDER HAVE BEEN CALCULATED OR ENTERED
- \* AND ARE AVAILABLE IN THE ABOVE TWO VARIABLE LOCATIONS

- THESE ASSUME THE EXISTENCE OF A MATH PACKAGE AND A
- ROUTINE "MOVE6" THAT MOVES SIX BYTES FROM LOCATION
- POINTED AT BY X TO LOCATION POINTED AT BY Y

```

0000          ORG 0          PROGRAM ORIGIN
0000 BE 1000  CALCMT  LDX 0PI
0003 10BE 3006  LDY 0SIGN
0007 BD 2015  JSR MOVE6
000A BE 1012  LDX 0RADIUS
000D 10BE 100C  LDY 0YSIGN
0011 BD 2015  JSR MOVE6
0014 BD 2000  JSR FPMUL      MULTIPLY, RESULT IN RSIGN PI*R
0017 BD 0052  JSR RTOX
001A BE 1012  LDX 0RADIUS
001D 10BE 300C  LDY 0YSIGN
0021 BD 2015  JSR MOVE6
0024 BD 2000  JSR FPMUL      PI * R^2
0027 BD 0052  JSR RTOX
002A BE 1006  LDX 0RHO
002D 10BE 300C  LDY 0YSIGN
0031 BD 2015  JSR MOVE6
0034 BD 2000  JSR FPMUL      PI * R^2 * RHO
0037 BD 0052  JSR RTOX
003A BE 100C  LDX 0HEIGHT
003D 10BE 300C  LDY 0YSIGN
0041 BD 2015  JSR MOVE6
0044 BD 2000  JSR FPMUL      PI * R^2 * RHO * HEIGHT
0047 BE 3000  LDX 0RSIGN
004A 10BE 101B  LDY 0HEIGHT
004E BD 2015  JSR MOVE6      PUT RESULT IN WEIGHT
0051 39      RTS

*
* SUBROUTINE RTOX
*
0052 BE 3000  RTOX  LDX 0RSIGN
0055 10BE 3006  LDY 0SIGN
0059 7E 2015  JMP MOVE6

005C BYTCT EQU *      BYTE COUNT FOR CALC
*
END

```

0 ERROR(S) DETECTED

```

*
* PROGRAM TO CALCULATE WT OF CYLINDER USING BINARY
* MATH PACKAGE WITH INTERPRETER
*
* MATH PKG EQUATES
*
0300 MATH EQU $300
*
* MATH FUNCTION MNEMONICS EQUATES
*
0001 MUL EQU $01
0007 PSH EQU $07
000B STR EQU $0B
*
1000          ORG $1000
*
* CONSTANTS
*
1000 00 00 00 00  PI  FCB 000,000,000,000
1004 00 00 00 00  RHO FCB 800,000,000,000
*
* VARIABLES
*
1008 RADIUS RMB 6
100E HEIGHT RMB 6
1014 WEIGHT RMB 6
*
0000          ORG 0
*
0000 BD 0300  CALCMT JSR MATH
0003 1000  FDB PI
0005 07      FCB PSH
0006 1008  FDB RADIUS
0008 07      FCB PSH
0009 01      FCB MUL
000A 1008  FDB RADIUS
000C 07      FCB PSH
000D 01      FCB MUL
000E 100C  FDB HEIGHT
0010 07      FCB PSH
0011 01      FCB MUL

```

```

0012 1004  FDB RHO
0014 07      FCB PSH
0015 0001  FDB MUL
0017 1014  FDB WEIGHT
0019 0B      FCB STR
001A 39      RTS
001B BYTCT EQU *
END

0 ERROR(S) DETECTED

*
* CALCULATE THE WEIGHT OF A STEEL CYLINDER USING
* TSC BCD MATH PACKAGE ASSUMING 6809 VERSION AVAILABLE
* THIS VERSION WITH MACROS NOT EXPANDED
*
* MATH PKG EQUATES
2000 FPMUL EQU $2000
2015 MOVE6 EQU $2015
3000 RSIGN EQU $3000
3006 XSIGN EQU $3006
300C YSIGN EQU $300C
*
* MACRO DEFINITIONS
*
FPMUL MACRO
    MFC 41,0
    JSR RTOX
    ELSE
    LDX $01
    LDY 0SIGN
    JSR MOVE6
    ENDDIF
    LDX $02
    LDY 0YSIGN
    JSR MOVE6
    JSR FPMUL
    ENDM
*
STORE MACRO
    LDX 0RSIGN
    LDY $01
    JSR MOVE6
    ENDM
*
* CONSTANTS
*
1000          ORG $1000
*
1000 00 03 14 15  PT  FCB 000,003,014,015,092,001 SIGN, 7 DIGITS, Exp
1004 02 01
1006 00 04 52 80  RHO FCB 800,004,052,080,000,001 DENSITY OF STEEL
100A 00 01
*
*
* REAL VARIABLES
*
100C HEIGHT RMB 6
1012 RADIUS RMB 6
101B WEIGHT RMB 6
*
* CALCULATION OF WEIGHT ASSUMES THAT HEIGHT AND
* RADIUS OF CYLINDER HAVE BEEN CALCULATED OR ENTERED
* AND ARE AVAILABLE IN THE ABOVE TWO VARIABLE LOCATIONS
*
* THESE ASSUME THE EXISTENCE OF A MATH PACKAGE AND A
* ROUTINE "MOVE6" THAT MOVES SIX BYTES FROM LOCATION
* POINTED AT BY 1 TO LOCATION POINTED AT BY Y
*
0000          ORG 0          PROGRAM ORIGIN
*
0000  CALCMT  FPMUL  PI,RADIUS
0017  FPMUL  0,RADIUS
0027  FPMUL  0,RHO
0037  FPMUL  0,HEIGHT
0047  STORE  WEIGHT
0051 39      RTS
*
* SUBROUTINE RTOX
*
0052 BE 3000  RTOX  LDX 0RSIGN

```



```

0035 10BE 3006      LDY 01SIGN
0039 7E 2015        JMP MOVE6
                     003C BYTECT EQU *          BYTE COUNT FOR CALC
                     *
                     END

0 ERROR(S) DETECTED

* CALCULATE THE WEIGHT OF A STEEL CYLINDER USING
* TSC AND MATH PACKAGE ASSUMING 8809 VERSION AVAILABLE
* THIS VERSION WITH MACROS EXPANDED
*
* MATH PKG EQUATES
2000 FPMUL EQU $2000
2015 MOVE6 EQU $2015
3000 R5IGN EQU $3000
3006 R5IGN EQU $3006
300C YSIGN EQU $300C
*
* MACRO DEFINITIONS
*
FPMUL MACRO
    IFC $1,0
    JSR RTD1
    ELSE
    LD1 $01
    LDY 01SIGN
    JSR MOVE6
    ENDDIF
    LD1 $02
    LDY 01SIGN
    JSR MOVE6
    JSR FPMUL
    ENDM

STORE MACRO
    LD1 01SIGN
    LDY $01
    JSR MOVE6
    ENDM

*
* CONSTANTS
*
1000      ORG $1000

1000 09 03 14 15 P1 FCB $00,$03,$14,$15,$92,$001 SIGN, 7 DIGITS, EXP
1004 92 01
1006 00 04 52 80 RND FCB $00,$04,$52,$80,$00,$001 DENSITY OF STEEL
100A 00 01

*
* REAL VARIABLES
*
100C      HEIGHT RMB 6
1012      RADIUS RMB 6
1018      WEIGHT RMB 6
*
* CALCULATION OF WEIGHT ASSUMES THAT HEIGHT AND
* RADIUS OF CYLINDER HAVE BEEN CALCULATED OR ENTERED
* AND ARE AVAILABLE IN THE ABOVE TWO VARIABLE LOCATIONS
*
* THESE ASSUME THE EXISTENCE OF A MATH PACKAGE AND A
* ROUTINE "MOVE6" THAT MOVES SIX BYTES FROM LOCATION
* POINTED AT BY X TO LOCATION POINTED AT BY Y
*
0000      ORG 0          PROGRAM ORIGIN

0000      CALCUT FPMUL PI,RADIUS
0000 BE 1000      LD1 $P1
0003 10BE 3006      LDY 01SIGN
0007 3D 2015        JSR MOVE6
000A BE 1012        LD1 01RADIUS
000B 10BE 300C      LDY 01SIGN
0011 00 2015        JSR MOVE6
0014 00 2000        JSR FPMUL
                     ENDM
0017      FPMUL $,RADIUS
0017 00 0052        JSR RTD1
001A BE 1012        LD1 01RADIUS
001B 10BE 300C      LDY 01SIGN
0021 00 2015        JSR MOVE6

```

```

0024 00 2000        JSR FPMUL
                     ENDM
0027      FPMUL $,RND
0027 00 0052        JSR RTD1
002A BE 1006        LD1 01RND
002D 10BE 300C      LDY 01SIGN
0031 00 2015        JSR MOVE6
0034 00 2000        JSR FPMUL
                     ENDM
0037      FPMUL $,HEIGHT
0037 00 0052        JSR RTD1
003A BE 100C        LD1 01HEIGHT
003B 10BE 300C      LDY 01SIGN
0041 00 2015        JSR MOVE6
0044 00 2000        JSR FPMUL
                     ENDM
0047      STORE WEIGHT
0047 BE 3000        LD1 01SIGN
004A 10BE 1018      LDY 01HEIGHT
004E 00 2015        JSR MOVE6
                     ENDM
0051 39            RTS

* SUBROUTINE RTD1
*
0052 BE 3000        RTD1 LD1 01SIGN
0055 10BE 3006      LDY 01SIGN
0059 7E 2015        JMP MOVE6
                     005C BYTECT EQU *          BYTE COUNT FOR CALC
                     *
                     END

```

0 ERROR(S) DETECTED

## COMPILER EVALUATION SERVICES By: Ron Anderson

The S.E. MEDIA Division of Computer  
Publishing Inc.  
Is offering the following **SUBSCRIBER  
SERVICE**:

### COMPILER COMPARISON AND EVALUATION REPORT

Due to the constant and rapid updating and enhancement of numerous compilers, and the different utility, appeal, speed, level of communication, memory usage, etc., of different compilers, the following services are now being offered with periodic updates.

This service, with updates, will allow you who are wary or confused by the various claims of compiler vendors, an opportunity to review comparisons, comments, benchmarks, etc., concerning the many different compilers on the market, for the 6809 microcomputer. Thus the savings could far offset the small cost of this service.

Many have purchased compilers and then discovered that the particular compiler purchased either is not the most efficient for their purposes or does not contain features necessary for their application. Thus the added expense of purchasing additional compiler(s) or not being able to fully utilize the advantages of high level language compilers becomes too expensive.

The following COMPILERS are reviewed initially, more will be reviewed, compared and benchmarked as they become available to the author:

PASCAL "C" GSPL WHIMISCAL PL/9

Initial Subscription - \$39.95  
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Flex and Uniflex, Technical Systems Consultants  
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PDP-11, Digital Equipment Corp.  
UNIX, Bell Laboratories  
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Whenever a new DOS is introduced, there's always the problem of developing software to work with it. So we did it the opposite way — we analyzed the requirements of software that already exists and developed a DOS that met them... and exceeded them! The result is STAR-DOS Level I, a new DOS for 6809 systems, ideal for single-user industrial, control, and advanced hobbyist applications. This includes SS-50 systems and single-board computers from a variety of vendors.

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Write or call for more information. STAR-KITS Software Systems Corporation. P.O. Box 209, Mt. Kisco N.Y. 10549 (914) 241-0287.



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## ASSEMBLERS

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#### ASTRUK09

A "Structured Assembler for the 6809" which requires the TSC Macro Assembler. Allows direct use of structured statements such as IF, ELSE, DO, REPEAT, etc., and provides indented level formatting of the listing so that the structure is apparent. Re. '68' Micro Journal, Sept. '83 (program was called "STASH09"; has been renamed due to conflicts).

#### A User reports

"... I'm very pleased and am now writing almost exclusively in (ASTRUK09). I've selected it over --- for all future systems development... As (one) of my early evaluations, I rewrote a rather elaborate routine originally done in assembly. Out of the 1000 bytes of code generated, the (ASTRUK09) version used only 20 more bytes than the original. --- could not handle this program since it uses triple-precision fixed point arithmetic... I have a large body of code already written that is incompatible with --- constructs. No problem with (ASTRUK09) and the structure sure helps in understanding the logic!"

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The FLEX STANDARD Assembler. F,CCF \$50.00  
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Computer Systems Consultants Super Sleuth is a "Time Tested", reliable, PROVEN Disassembler that has gained acceptance through out the SS-50 Bus Community as an extremely POWERFUL, INTERACTIVE, Software Tool. The Super Sleuth Software Package consists of 3 Programs; SLEUTH (the Disassembler), CHGNAME (used to globally Change Labels to a meaningful Name), and XREF (a Cross Reference Generator for Source Code Files). SLEUTH will Disassemble Memory Resident 6809 Code and 6800, 6801, 6802, 6803 (the "Baby CoCo"), 6805, 6808, 6809, and 6502 (Apple, Atari, Commodore, etc.) Binary Disk Files. (See Aug. '83 '68' Micro Journal "Color Users Notes" Column for a full Review.)

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An "easy to use", powerful Disassembler for Disk Resident 6809 and 6800 Binary Files. Allows the development of a "Control File" of various Program "Boundaries" during successive disassemblies; can use a Label File which automatically replaces a Hex location with a Label Name; includes an XREF Utility; etc. Label Files provided for Mini-FLEX, FLEDC, FLEDC9, Color Computer (for use with Color FLEX Systems), etc. OS-9 Version includes special OS-9 options.

CCF, Obj. Only \$100.00  
CCO, " " \$150.00  
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U, " " \$300.00

## COMPILERS & DECOMPILERS

### 6809 "Structured" Assembly Lang. Compilers

### Windrush Micro Systems

#### PL/9

By Graham Trott. A "Structured" Assembly Language Editor/Compiler/Debugger, all in ONE PACKAGE, provides a totally INTERACTIVE Program Development Cycle. The Compiler exports large Symbol Names, Variable Types, Pointers, Control Structures, Stack, A-, B-, and D-Register manipulation, etc. The Source-Oriented Trace/Debugger provides Single Stepping, Breakpointing, etc. An excellent Software Development Tool for utilizing the power of the 6809 in developing small to medium sized packages.

F, CCF - \$198.00

### Whimsical Developments

#### WEDMICAL

Need the Ease of Design and Maintainability of "Structured Programming" AND the Speed and Control of Assembly Language? Then WEDMICAL was designed for you! This Single Pass, Recursive Descent Compiler provides the tool for developing simple Utilities to MAJOR Systems in Assembly Language. Supports 3 "Lex" Levels which allow one level of Procedure nesting, or more within "Modules". It is easy to develop programs written for other machines since you are working at the Assembly Language level. Features unified, user-defined I/O; produces relocatable, recursive, re-entrant Code; Structured style and statements with Procedures and Modules; supports Byte and Double-Byte primitives with 3 types of Integers (up to 32 bit), Char and Boolean, and unlimited sized Arrays (vectors only); Interrupt handling; unlimited length Variable Names; Variable Initialization (defaults to \$00); Include "Source File" directive; Conditional compiling; direct Code insertion; control of the Stack Pointer; etc. To quote Ron Anderson in his review of WEDMICAL in the Sept. '83 issue of '68' Micro Journal that, except for the lack of floats, "... I have to give this one VERY high rating, ...". It is a FAST Compiler which produces FAST Code (his "Prims" Benchmark ran at 9 secs. on a 2 Mhz System).

F and CCF - \$195.00

### 'C' Compilers

### Windrush Micro Systems

#### C Compiler

By James McCosh. Full featured C Compiler for the FLEX Operating System. Includes a Reloc. Assemb., but needs the TSC Relocating Assembler/Linking Loader (which includes a Library Manager) for those "full blown" system packages.

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A full-featured C, streamlined for the 6809. Generates very efficient object code. Output "benchmarks" close to 100% 68000 in 8 Bit Operations; 1.5 times faster than a 4 Mhz 286 when using a 2Mhz 6809 System (Re. p 43, '68' Micro Journal, May '83). Floata, etc.

F, CCF, and O - \$375.00

U - \$425.00

One Year Maint. - \$100.00



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\*OS9 is a trademark of Microware

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**PASCAL Compilers**

**TSC**

**PASCAL Compiler**

Native Code Compiler (UCSD Oriented).

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**PASCAL Compiler**

P-Code Compiler (ISO Standard). Designed especially for Microcomputer Systems: Run-time System checks available resources for each task, allowing operation on even minimal computer systems. Allows linkage to Assembly Code for maximum flexibility.

F and CCF 5" - \$190.00  
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For the PROFESSIONAL: ISO Based, Native Code Compiler. Primarily for Real-Time and Process Control applications. Use custom I/O devices in place of the Pascal INPUT and OUTPUT; Long Int. (32 Bit); Dynamic length strings; Interrupt processing, ROM-able, PIC, Re-entrant Code, etc. **ROMABLE** Includes Source for the Symbolic Debugger, Runtime, and several utilities. Requires a "Motorola Compatible" Relocating Assembler and Linking Loader.

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One Year Maint. - \$180.00

#### UNCOMPILERS

**Southeast Media**

**DUB** (A UniFLEX "basic" Compiler)

Re-Create a Source Listing from UniFLEX Compiled basic Programs. Easy to Use: works w/ ALL Versions of UniFLEX basic; output to Disk or Terminal. Time TESTED and PROVEN: SOLID!

U - \$219.95

#### UTILITIES

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**BASIC9 XRef**

This BASIC9 Cross Reference Utility is a BASIC9 Program which will produce a "pretty printed" listing with each line numbered, followed by a complete cross referenced listing of all variables, external procedures, and line numbers called. Also included is a Program List Utility which outputs the listing without the overhead of building the cross reference table, which allows it to run considerably faster when only a "pretty printed" listing with line numbers is desired. Requires BASIC9 or RUN8 for operation.

```

72  GET DIRPath:Name
73  GET DIRPath:Name \ OF DIRPath:Name
74  GET DIRPath:Name \ OF DIRPath:Name
75  REPEAT
76  UNTIL char=13
77  UNTIL char=13
78  UNTIL char=13
79  UNTIL char=13
80  UNTIL char=13
81  UNTIL char=13
82  RETURN

```

File	3	26	68	78	79
DirPath	4	19	20	81	
char	4	20	20	81	
File	78	79	80		
DirPath	4	22	80	72	
char	8	11			
File	11	13			

O and CCO - Obj. Only -- \$39.95  
O and CCO - w/ Source - \$79.95

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**OS-9 VDisk**

Give your OS-9 (Level I) System the speed of memory access that can be several orders of magnitude over your present floppy disk drive. Use that Extended Memory capability of your SWTPC or Gimix CPU card (or any other that has the same format DAT). The size of the Virtual Disk is completely variable in whole increments of 4K up to 960K, which is all that these systems can address beyond the base page that OS-9 Level I uses. By putting all of your CMDS Directory on your Virtual Disk, you can have the fastest execution speed possible (next to eating up System Memory with all of them). You can also set up high speed inter-process communications via random virtual disk files and not eat up valuable system memory with pipe buffers. Some Assembly Required - Level I ONLY.

O, obj. only - \$79.95  
w/ Source - \$149.95



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**REFORMAT:** A BASIC9 Program that reformats a chosen amount of an OS-9 disk to FLEX Format so it can be used normally by FLEX.

**FLEX:** A BASIC9 Program that does the actual read or write function to the special O-F Transfer Disk, all selected from a user-friendly menu. Functions provided include reading the FLEX Directory, Deleting FLEX Files, Copying both directions, etc. All selections are interactive and complete, including all necessary prompts to the operator.

FLEX users can read, write and use the special disk as any other FLEX disk, provided the FLEX directory is not allowed to continue beyond track zero (too many files).

F and CCF - \$79.95

**Southeast Media**

**COPYMULT**

--- Copy LARGE Disks to several smaller disks ---

The following FLEX utilities allow the backup of ANY size disk to any SMALLER size diskettes (Winchester to 8's or 5's, 8" to 5's, etc.). By simply inserting diskettes as requested by COPYMULT, a large disk system may be downloaded to your present floppy disk system, any size. No need to fiddle with directory deletions or any of the other tedious operations that must be done using the normal copy routines.

**COPYMULT.CMD** understands normal "copy" syntax and always keeps up with files already copied by maintaining directories for both host and receiving disk system, eliminating hours of tedious keyboard entries and other time consuming cleanup chores.

**BACKUP.CMD** is a special program that downloads "random" type files, any size.

**RESTORE.CMD** is a special program to restructure copied "random" files for copying, or recovering back to the host system.

**FREELINK.CMD** is a "bonus" utility that "relinks" the free chain of floppy or hard disk thereby eliminating fragmentation.

Completely documented source files included.

ALL 4 Programs (8" or 5") \$99.50

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**CHESS 6809**

Requires FLEX and DISPLAYS On Any Type Terminal

Features:

- \*Four levels of play.
- \*Swap side. \*Point scoring system.
- \*Two display boards. \*Change skill level.
- \*Solve Checkmate problems in 1-2-3-4 moves.
- \*Make move and swap sides. \*Play white or black.

This is one of the strongest CHESS programs running on any microcomputer, estimated USCF Rating 1600+ (better than most 'club' players at higher levels).

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##### DIET-TRAC Forecaster

DIET-TRAC Forecaster is an X BASIC program that plans a diet in terms of either calories and percentage of carbohydrates, proteins and fats (C P G) or grams of Carbohydrate. Protein and Fat food exchanges of each of the six basic food groups (vegetable, bread, meat, skim milk, fruit and fat) for a specific individual.

Sex, Age, Height, Present Weight, Frame Size, Activity Level and Basal Metabolic Rate for normal individual are taken into account. Ideal weight and sustaining calories for any weight of the above individual are calculated. When a weight goal is given (either gain or loss), and a calorie plan is agreed upon between the computer and the individual, the number of days to reach the weight goal is projected. The starting and ending rate of weight loss is calculated, and a daily calendar with each day's weight for a 30-day period is printed.

F - \$59.95  
 U - \$89.95

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##### XDATA

##### A COMMUNICATION Package

for the UniFLEX Operating System

Allows UniFLEX Based Systems to Transmit and Receive files to and from other Computer Systems via Modem. Use with CP/M, Main Frames, other UniFLEX Systems, etc.

- Verifies Transmission integrity using checksum or CRC
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- Transmits data in 128 byte blocks

U - \$299.99

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##### JUS

##### Text Formatter

JUS, a Text Formatter developed by Ron Anderson, provides numerous features which make it a valuable addition to any FLEX Users Software Library. JUS is designed for formatting Text Output for Dot Matrix Printers and provides many unique features:

- Output the "Formatted" Text to the Display for format analysis and change.
- Output the "Formatted" Text to a Text File for use with the supplied FPRINT.COM for producing multiple copies of the Text on the Printer INCLUDING IMBEDDED PRINTER COMMANDS (this Utility is very useful at other times also, and worth the price of the program by itself).
- "User Configurable" for adapting to other Printers (comes set up for Epson MX-80 with Grafix); provides for up to ten (10) imbedded "Printer Control Commands", such as Italics on and off, boldface on and off, etc.
- Automatic compensation for a "Double Width" printed line.
- Includes the normal line width, margin, indent, paragraph, space, vertical skip lines, page length, page numbering, centering, fill, justification, etc.
- Use with ANY Editor.
- Supplied with "Structured Source" (Mindrush PL/9); easy to see the flow of the program.

F and CCF - \$49.95

#### Lucidata

##### PASCAL UTILITIES

Requires LUCIDATA Pascal ver 3.

XREF -- produce a Cross Reference Listing of any text; oriented to Pascal Source.

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INCLUDE -- allows the inclusion of other Files in a Source Text; has unlimited nesting capabilities. Also allows Binary File Inclusions.

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PROFILER -- produces an indented, Numbered, "Structogram" of a Pascal Source Text File. Allows viewing the overall structure of large programs, and provides clues as to the integrity of the program. Supplied as Source Code; requires compilation.

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##### COPYCAT

Pascal NOT required

Allows reading TSC Mini-FLEX, SSB 00S68, and Digital Research CP/M Disks while operating under FLEX 1.0, FLEX 2.0, or FLEX 9.0 with 6800 or 6809 Systems. COPYCAT will not perform Miracles, but, between the program and the manual, you stand a good chance of accomplishing a transfer. Includes Utilities to List Directories, Copy Files, and convert Text Files when required. Also includes a Utility for investigating Physical Compatibility problems. Programs supplied in Modular Source Code (Assembly Language) to make it easier to solve unusual problems.

F and CCF 5" - \$50.00  
 F 8" - \$65.00

#### Computer Systems Consultants

##### FLEX DISK UTILITIES

Eight (8) different FLEX Utilities that should be a part of every FLEX Users Toolbox; Assembly Language (Source Code):

Copy a File with CRC Errors, so it can possibly be salvaged; Test Disk for errors; Compare two Disks; a fast Disk Backup Program; Edit Disk Sectors; Linearize Free-Chain on the Disk; print Disk Identification; and Sort and Replace the Disk Directory (in sorted order).

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EXTREMELY Powerful Screen-Oriented Editor/Word Processor. Almost 50 different commands; EXCELLENT Documentation (over 300 pages), including a full Tutorial Section to help you learn how to use the system. Features Cursor-based editing, dynamic Screen Formatting (what you see is what you get), Multi-Column display and editing, "decimal align" columns (AND add them up automatically, if wanted), define multiple keystroke macros, even add page number headers and footers, lebed printer control codes in text, full justification series of commands, full "help" support, store common command series on disk for future use, etc. Easy "Set-Up" (for example, you just hit the key you want to use for a specific function, such as "cursor up", and the System reads an stores that key - no digging into tech manuals for codes, etc.); use supplied "set-ups", or remap the keyboard to what you are used too. Except for proportional printing, this package will DO IT ALL!

6800 or 6809 FLEX or SSB 00S, OS-9 - \$175.00

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##### STYLOGRAPH

A full-screen oriented WORD PROCESSOR -- (now runs on the Data-Comp and FHL Color FLEX Systems; uses the 51 x 24 Display Screens). Full screen display and editing (i.e., what you see is what you get); supports the Daisy Wheel proportional printers.

SPECIAL CCF - \$195.00

F and O - \$295.00

U - \$395.00

##### SPELL

Fast Computer Dictionary.

F, CCF, DS/9 - \$125.00

U - \$175.00

##### MAIL MERGE

Greatly extends the power and flexibility of STYLOGRAPH.

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U - \$195.00



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## MAIL MERGE

Greatly extends the power and flexibility of **STYLISH**. Allows Multiple Text files to be printed out as one large document. Provides for merging information into the Text File during printing (such as different names and addresses), etc.

F, CCF, O - \$145.00  
U - \$195.00

## Southeast Media

### SPELLB "Computer Dictionary"

OVER 120,000 words!

No more "Let your fingers do the walking through the Dictionary" while you are entering Text with your favorite Editor or Word Processor. **SPELLB** is more than just "another Spelling Checker"; it allows you to look up a word from within your Editor or Word Processor so that you **KNOW** it is right WHEN YOU TYPE IT IN with the **SPN,CD** Utility (which operates in the **FLEX** Utility Space). Yes, it **ALSO** allows you to check and update the Text after you are finished; along with allowing you to ADD WORDS to the Dictionary, "Flag" questionable words in the Text for evaluation later, "View a word in context" before changing or ignoring, etc. **SPELLB** first checks a "Common Word Dictionary", then the normal Dictionary, then a "Personal Word List", and finally, any "Special Word List" you may have specified. **SPELLB** also allows the use of Small Disk Storage systems.

F and CCF - \$129.95

## Great Plains Computer Co.

### SPZL

Fast Computer Dictionary — allows directly changing the Text File, adding words to the dictionary, etc. 75,000 words in less than 400 seconds.

F, CCF, OS/9 - \$125.00  
U - \$175.00

## DATA BASE MANAGEMENT SYSTEMS

### Universal Applied Systems

#### XIMS

Possibly one of the most powerful Database Management Systems available, this machine language program is small enough to operate on a single sided 5" disk, yet provides the speed of M.L. and power limited only by the user's imagination. This DMS supports Relational, Sequential, Hierarchical, and Random Access File Structures, and has Virtual Memory capabilities for those Giant Data Bases. **XIMS Level I** provides a functional "entry level" System which provides for defining a Data Base, entering and changing the Data, and producing Reports. **XIMS Level II** adds the **REXPL** "EXPLAINS" facility which uses an English Language Command Structure in manipulating the Data to create new File Structures, Sort, Select, Calculate, etc. **XIMS Level III** adds several special "Utilities" which provide additional ease of working with the various structures, changing System Parameters, etc.

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**XIMS Level II** - F & CCF - \$199.95

**XIMS Level III** - F & CCF - \$269.95

**XIMS System Manual** only - \$24.95

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An **XBASIC**, Menu Driven, DBMS with "Built-In" Audit Tracking, Extremely Powerful Report & Format Capabilities, etc. This **REPORTING DBMS** will become the "Work Horse" of your Software Stable.

F and CCF \$295.00  
U \$395.00

## ACCOUNTING PACKAGES

Great Plains Computer Co. and Universal Data Research, Inc. both have Business Packages written in TSC **XBASIC** for **FLEX**, **CoCo FLEX**, and **UnifLEX** ---

\*\*\*\*\*  
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\*\*\*\*\*



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F and CCF - \$100.00. U - \$150.00

**REQUIRES PRECOMPILER**

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F = **FLEX**, CCF = Color Computer **FLEX**  
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CCD = Color Computer Disk  
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 THE Electronic Spread Sheet for 6809 Computer Systems. An extremely POWERFUL Business Tool, this Program will find an unlimited number of "non-business" applications, also (for example, a Full Junior College Electronics Curriculum was set up using DYNACALC). Advanced features like "Table Lookup" make Income Tax work easy; Column or Row Sorting for numerous applications; etc. Completely "Memory Resident", Machine Language, this Program is FAST. Provides STANDARD FLEX Text File output for use with BASIC, Word Processors, Pascal, "C", etc. Also available for Data-Comp and FHL FLEX systems using the 50 x 24 Displays.

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 U = \$395.00

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 This package supports any Serial Terminal with cursor control of Memory-Mapped Video Displays. The package substantially extends the screen input/output capabilities of TSC's Extended BASIC programs by providing a simple, table-driven method of describing and using full screen displays. These table entries are easy to set up and maintain, and are normally stored on disk and read as required. A simple, interactive means of generating the forms and the data field definitions is provided.

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**FULL SCREEN MAILING LIST**  
 The Full Screen Mailing List System provides a means of maintaining simple mailing lists. Using a random fill structure based on the first character of the name field, it maintains the file in alphabetical order for easier inquiry. With the FIND command, the user may locate all records matching on partial or complete name, city, state, zip, or attributes. Printed listings and output to labels may also be produced on the same selective basis. It requires TSC's Extended BASIC.

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**NOTE --** The initial release of OTWASHARE is for **OS/9** Computers, but versions will also be available for other popular extended-memory (up to 1M20K) systems, such as **HELDK** and **GWKX**. A minimum of 128K of RAM will be required with ALL versions. OTWASHARE requires 64K of RAM for each active task: thus a 256K system could allow foreground-background operation on two terminals, or foreground-only operation on four terminals.

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For the past several months, we at the Southeast Media Division of Computer Publishing, Inc. (CPI), the parent company of '68' MICRO JOURNAL and COLOR MICRO JOURNAL, have debated expanding our software distribution business. Many other magazines have been doing so for years (in fact, MOST were in the Software Distribution Business BEFORE they began to publish a Magazine). Presently there are many fine examples of software that has been developed by YOU, our readers, that will never see the "light of day" due to the Cost of Advertising and TIME and Cost involved in the production, distribution, and Customer SUPPORT of that software unless SOMEONE, with enough exposure and the willingness to continually advertise, runs with the ball.

Software is the "backbone" for the REAL utilization of any Computer System, and ours are no exception! This has been no simple decision. While we realize that there could be some conflict with some of our advertisers, we ALSO hear a LOUD and CONTINUOUS cry for HELP from our Readers. From day one, the foremost concern of '68' MICRO JOURNAL has been it's READERS! Therefore, our Southeast Media Division will accept, for appraisal for possible Distribution, 6809 software; Games, Utilities, Software Development, Business Application Programs, etc.



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Of course, we will expect, no -- **DEMAND**, that the author keep the product free of errors (bugs), and maintain it in a prompt and business like manner. Also we shall require that authors be willing to furnish 'source' for those programs that justify, by price and utility, inclusion of same. The lack of source code, properly commented, is a continual complaint we hear. Not all programs will be sold with source, but where necessary, we will insist that it be included.

In some instances the program may be small or short and not justify itself as a "single" sale product. In this event it will be combined with other like programs, and offered as a package. In that event, the royalties will be split between the various authors.

If you have software that you feel will qualify under this program, please contact one of the people below. Remember, if your software has any problems or "funnies" -- **GET IT STRAIGHT BEFORE YOU CONTACT US!** Also get your source code in proper shape and well commented; there is too much 99% code already drifting around.

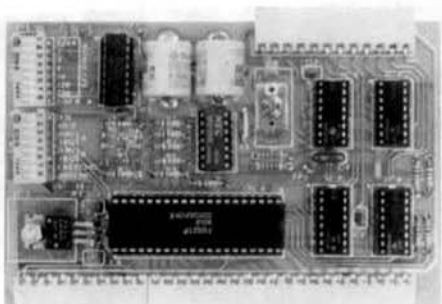
If your software is **READY** contact:  
Bob May, Don Williams, or Tom Williams

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# CALENDAR-CLOCK / TIMER / PARALLEL PORT



## Calendar - Clock

CLK68-1

- Keeps date and time whenever the computer is on
- All clock functions software controlled
- On card battery (included) and charged circuit board for accurate day of week, month/year, hour/minute (12/24 hr)

## Interval Timer

- For printer spacing, multi-tasking, etc.
- Compatible with OS-9 and Plus 2/4
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## Parallel I/O Port

- Full buffered 8 bit parallel port
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## Construction

Manual -- Well documented - 36 pages

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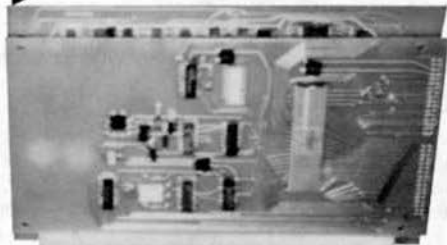
		A	B	C	D	E	F
INTERFACE	S30	PAR	PAR	SER	S30	SER	SER
INTELLIGENT	NO	NO	NO	YES	NO	YES	YES
PROGRAMS							
2704*							
2508	•						
2708*							
275	•						
2516	•						
2716	•						
2716*	•						
2532	•						
2732	•						
2732A	•						
2564	•						
2764	•						
2528	•						
27128	•						
2816	•						
68764							
6746							
6749							
TOTAL	11	3	12	6	11	11	11
PRICE	\$125	\$45*	\$169	\$289	\$375	\$489	\$575

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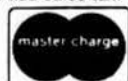
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---

**1. What is an electronic spread-sheet, anyway?**

Business people use spread-sheets to organize columns and rows of figures. DYNACALC simulates the operation of a spread-sheet without the mess of paper and pencil. Of course, corrections and changes are a snap. Changing any entered value causes the whole spread-sheet to be re-calculated based on the new constants. This means that you can play, 'what if?' to your heart's content.

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That's a good question. Basically the answer is that DYNACALC will let your computer do just about anything you can imagine. Ask your friends who have VisiCalc™, or a similar program, just how useful an electronic spread-sheet program can be for all types of household, business, engineering, and scientific applications. Typical uses include financial planning and budgeting, sales records, bills of material, depreciation schedules, student grade records, job costing, income tax preparation, checkbook balancing, parts inventories, and payroll. But there is no limit to what YOU can do with DYNACALC.

**4. Do I have to learn computer programming?**

NO! DYNACALC is designed to be used by non-programmers, but even a Ph.D. in Computer Science can understand it. Even experienced programmers can get jobs done many times faster with DYNACALC, compared to conventional programming. Built-in HELP messages are provided for quick reference to operating instructions.

**5. Do I have to modify my system to use DYNACALC?**

Nope. DYNACALC uses any standard 6809 configuration, so you don't have to spend money on another CPU board or waste time learning another operating system.

**6. Will DYNACALC read my existing data files?**

You bet! DYNACALC has a beautifully simple method of reading and writing data files, so you can communicate both ways with other programs on your system, such as the Text Editor, Text Processor, Sort/Merge, STYLOGRAPH™ word processor, RMS™ data base system, or other programs written in BASIC, C, PASCAL, FORTRAN, and so on.

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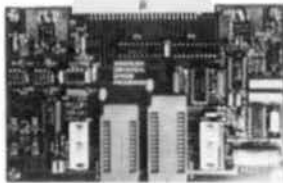
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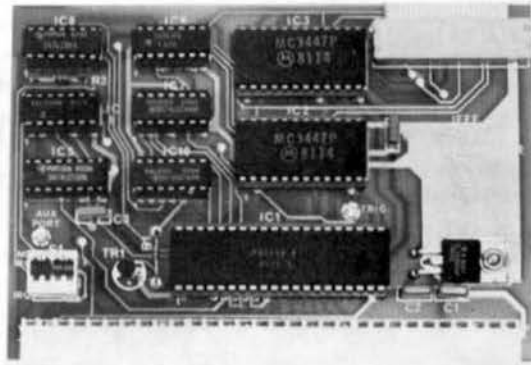
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- Control statements: IF..THEN..ELSE, IF..CASE1..CASE2..ELSE, BEGIN..END, WHILE.., REPEAT..UNTIL, REPEAT..FOREVER, CALL, JUMP, RETURN, BREAK, GOTO.
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- Friendly interactive environment where you have INSTANT access to the Editor, the Assembler, FLEX and your System Monitor.
- MACE can also produce ASMPROC's for PL/9 with the assembly language source passed to the output file as comments.
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This is the FLEX version of the James McCash 'C' compiler that is also available on UNIFLEX from SUTP and OS-9 from Microware:

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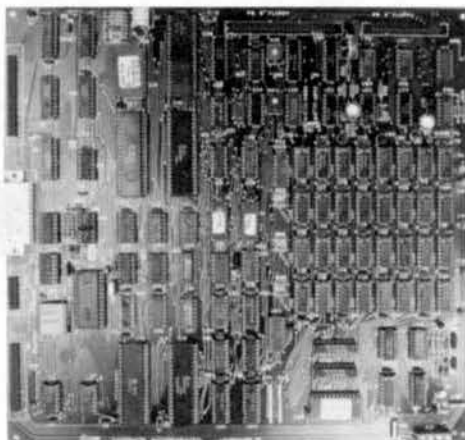
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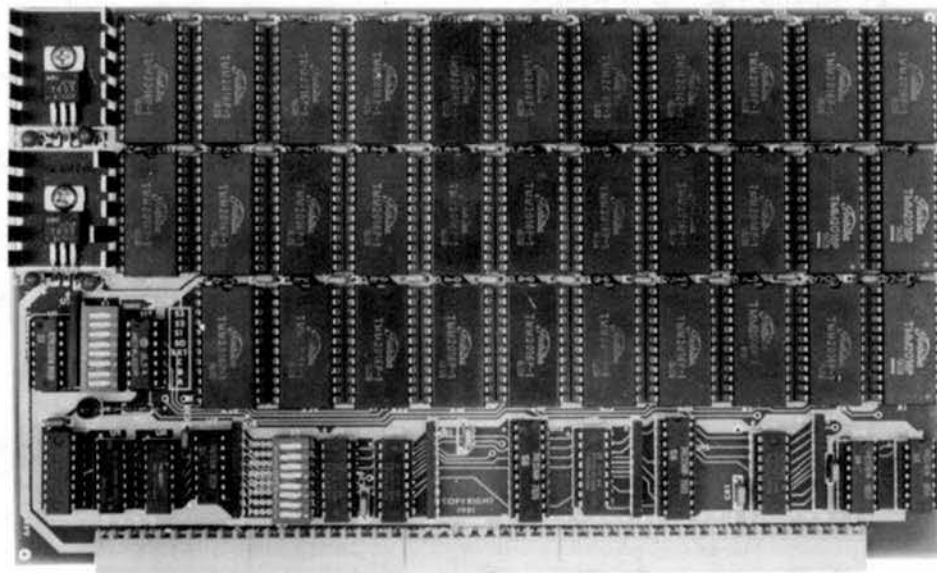
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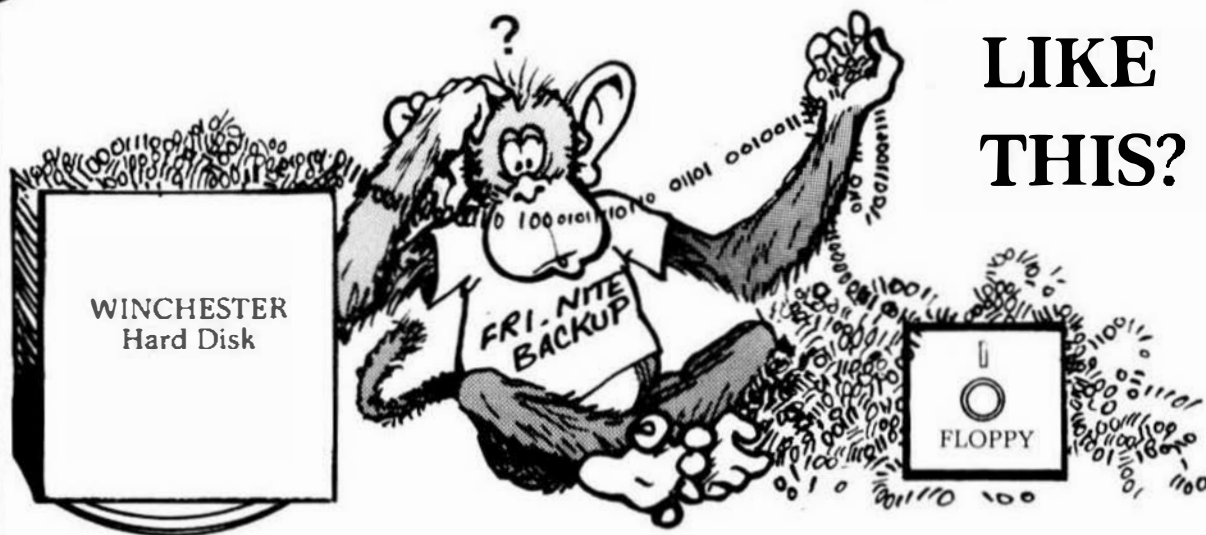
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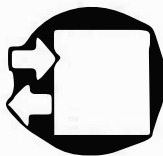
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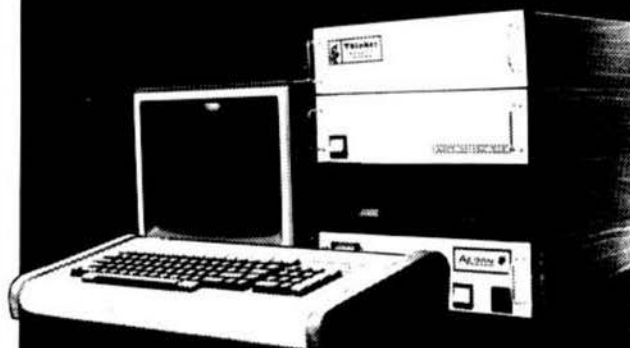
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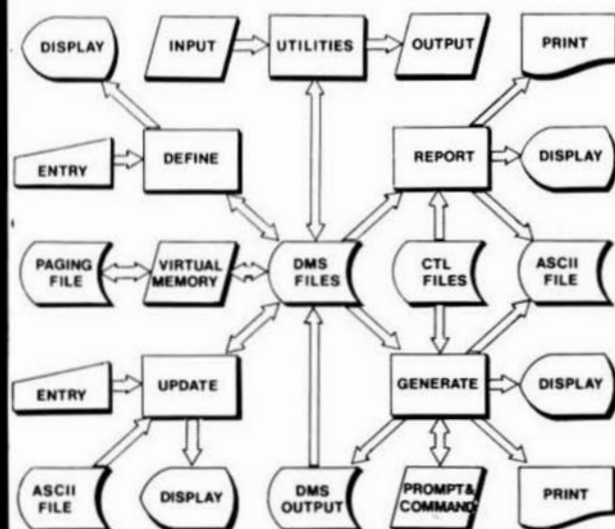
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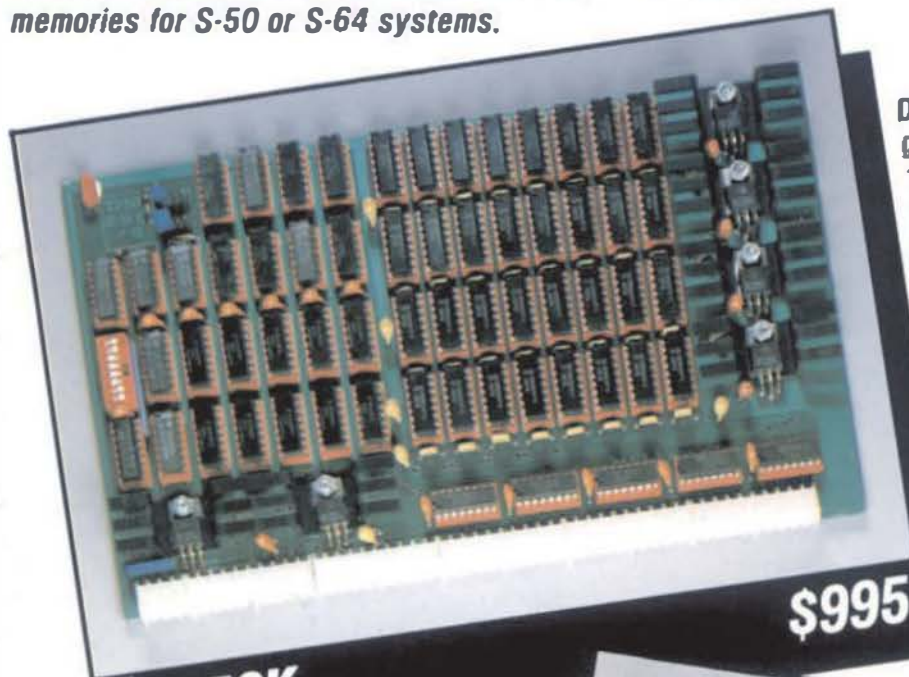
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